



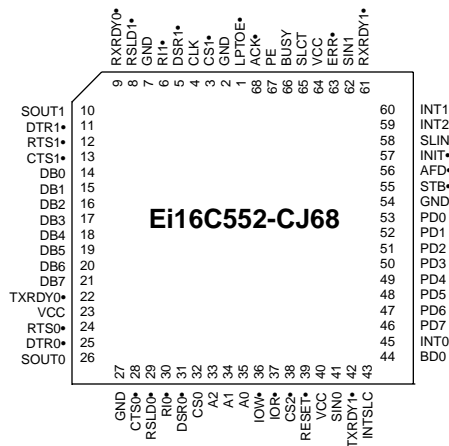
### FEATURES

- IBM PC AT™ and PS/2™ compatible
- Dual channel version of Ei16C550 with Centronics printer interface
- Independent control of transmit, receive, line status and data set interrupts on each channel
- Programmable serial interface characteristics for each channel:  
-5, 6, 7 or 8 bit characters  
-Even, odd or no parity bit generation and detection  
-1, 1.5 or 2 stop bit generation
- Programmable baud rate generator divides CLK input by a divisor between 1 and (2<sup>16</sup> -1)
- Tri-State® TTL drive capability for bidirectional data bus and control bus on each channel
- 16 byte FIFO for receiver as well as for transmitter.
- Advanced CMOS low power technology with single 5 volt supply

### DESCRIPTION

The **Ei16C552** is an enhanced dual channel version of the Ei16C550 Universal Asynchronous Receiver Transmitter (UART). The device serves two serial input/output interfaces simultaneously in microcomputer or microprocessor based systems. Each channel performs a serial-to-parallel conversion on data characters received from peripheral devices or modems, and a parallel-to-serial conversion on data characters transmitted by the CPU. The complete status of each channel of the dual UART can be read at any time during functional operation by the CPU. The information obtained includes the type and condition of the transfer operations being performed, and error conditions. In addition to its dual communications interface capabilities, the Ei16C552 provides the user with a fully bidirectional parallel data port that fully supports the parallel Centronics type printer. The parallel port, together with the two serial ports, provide IBM PC AT™ and PS2 compatible computers with a single device to serve the three system ports.

### PIN CONFIGURATION





NOTES ON PIN DESCRIPTION:

- 1) Pin 4 and Pin 2 can be used by external crystal oscillator in future versions
- 2) Pin 23 and Pin 43 can be used as OUT20• and OUT21• respectively in future versions

ORDERING INFORMATION

Ei16C552-CJ68 68 Pin PLCC package

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BLOCK DIAGRAM

