HAT2037T

Silicon N Channel Power MOS FET High Speed Power Switching

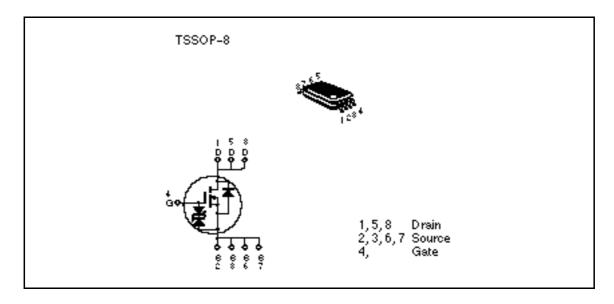
HITACHI

ADE-208-530 B (Z) 3rd. Edition July 1997

Features

- · Low on-resistance
- Capable of 2.5 V gate drive
- · Low drive current
- High density mounting

Outline





HAT2037T

Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

| Item | Symbol | Ratings | Unit |
|--|------------------------------|-------------|------|
| Drain to source voltage | $V_{\scriptscriptstyle DSS}$ | 28 | V |
| Gate to source voltage | $V_{\rm GSS}$ | ±12 | V |
| Drain current | I _D | 5.5 | A |
| Drain peak current | Note1 D(pulse) | 44 | A |
| Body-drain diode reverse drain current | I _{DR} | 5.5 | A |
| Channel dissipation | Pch Note2 | 1.3 | W |
| Channel temperature | Tch | 150 | °C |
| Storage temperature | Tstg | -55 to +150 | °C |

Note: 1. PW 10µs, duty cycle 1 %

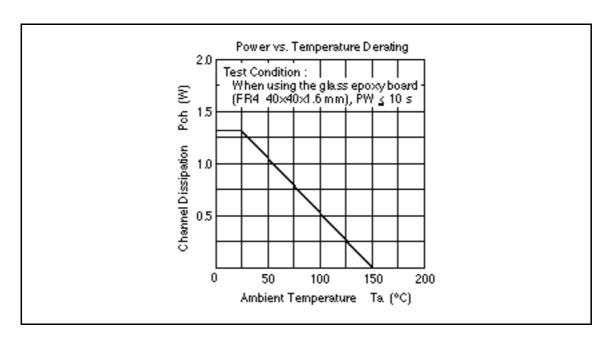
2. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW $\,$ 10s

Electrical Characteristics ($Ta = 25^{\circ}C$)

| Item | Symbol | Min | Тур | Max | Unit | Test Conditions |
|---------------------------------|---------------------|-----|-------|-------|------|-------------------------------------|
| Drain to source breakdown | $V_{(BR)DSS}$ | 28 | _ | _ | V | $I_{D} = 10 \text{mA}, V_{GS} = 0$ |
| voltage | | | | | | |
| Gate to source breakdown | $V_{(BR)GSS}$ | ±12 | _ | _ | V | $I_G = \pm 100 \mu A, V_{DS} = 0$ |
| voltage | | | | | | |
| Gate to source leak current | I _{GSS} | _ | _ | ±10 | μΑ | $V_{GS} = \pm 10V, V_{DS} = 0$ |
| Zero gate voltege drain current | I _{DSS} | _ | _ | 1 | μΑ | $V_{DS} = 28 \text{ V}, V_{GS} = 0$ |
| Gate to source cutoff voltage | $V_{GS(off)}$ | 0.4 | _ | 1.4 | V | $V_{DS} = 10V$, $I_D = 1mA$ |
| Static drain to source on state | R _{DS(on)} | _ | 0.021 | 0.028 | | $I_D = 3A$, $V_{GS} = 4V^{Note3}$ |
| resistance | R _{DS(on)} | _ | 0.027 | 0.038 | | $I_{D} = 3A, V_{GS} = 2.5V^{Note3}$ |
| Forward transfer admittance | y _{fs} | 9 | 14 | _ | S | $I_{D} = 3A, V_{DS} = 10V^{Note3}$ |
| Input capacitance | Ciss | _ | 780 | _ | pF | V _{DS} = 10V |
| Output capacitance | Coss | _ | 470 | _ | pF | $V_{GS} = 0$ |
| Reverse transfer capacitance | Crss | _ | 190 | _ | pF | f = 1MHz |
| Turn-on delay time | t _{d(on)} | _ | 20 | _ | ns | $V_{GS} = 4V$, $I_D = 3A$ |
| Rise time | t _r | _ | 130 | _ | ns | $V_{DD} \div 10V$ |
| Turn-off delay time | $t_{d(off)}$ | _ | 155 | _ | ns | _ |
| Fall time | t _f | _ | 160 | _ | ns | _ |
| Body-drain diode forward | V_{DF} | _ | 0.81 | 1.06 | V | IF =5.5A, $V_{GS} = 0^{Note3}$ |
| voltage | | | | | | |
| Body-drain diode reverse | t _{rr} | _ | 55 | _ | ns | IF = 5.5A, V _{GS} = 0 |
| recovery time | | | | | | diF/ dt =20A/µs |
| | | | | | | |

Note: 3. Pulse test

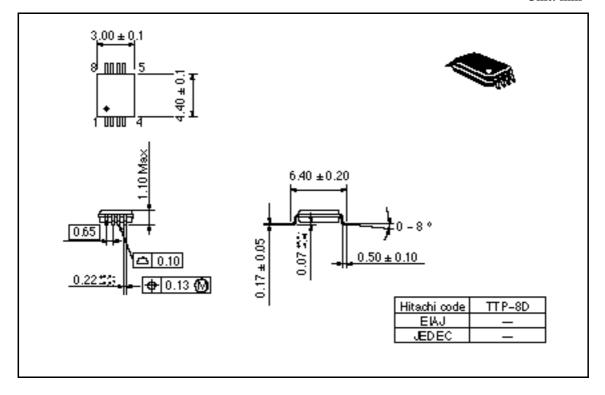
Main Characteristics



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Package Dimensions

Unit: mm



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