

# CNA1006N

## Photo Interrupter

For contactless SW, object detection

### ■ Outline

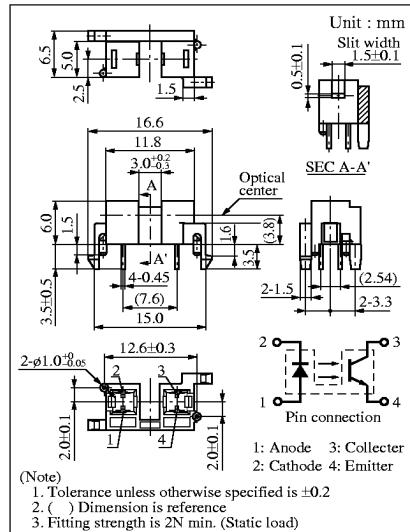
CNA1006N is a transmittive photosensor in which a high efficiency GaAs infrared light emitting diode is used as the light emitting element, and a high sensitivity phototransistor is used as the light detecting element. The two elements are arranged so as to face each other, and objects passing between them are detected.

### ■ Features

- Highly precise position detection : 0.3 mm
- Gap width : 3 mm
- Type which is fastened using hooks with printed circuit board (PWB) positioning pins

### ■ Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Ratings	Unit
Input (Light emitting diode)	Reverse voltage (DC)	$V_R$	3 V
	Forward current (DC)	$I_F$	50 mA
	Power dissipation	$P_D^{*1}$	75 mW
Output (Photo transistor)	Collector current	$I_C$	20 mA
	Collector to emitter voltage	$V_{CEO}$	30 V
	Emitter to collector voltage	$V_{ECO}$	5 V
	Collector power dissipation	$P_C^{*2}$	100 mW
Temperature	Operating ambient temperature	$T_{opr}$	-25 to +85 $^\circ\text{C}$
	Storage temperature	$T_{stg}$	-30 to +100 $^\circ\text{C}$



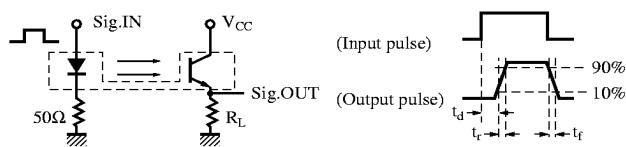
\*<sup>1</sup> Input power derating ratio is 1.0 mW/ $^\circ\text{C}$  at  $T_a \geq 25^\circ\text{C}$ .

\*<sup>2</sup> Output power derating ratio is 1.33 mW/ $^\circ\text{C}$  at  $T_a \geq 25^\circ\text{C}$ .

### ■ Electrical Characteristics ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Conditions	min	typ	max	Unit
Input characteristics	Forward voltage (DC)	$V_F$ $I_F = 20\text{mA}$		1.25	1.4	V
	Reverse current (DC)	$I_R$ $V_R = 3\text{V}$			10	$\mu\text{A}$
Output characteristics	Collector cutoff current	$I_{CEO}$ $V_{CE} = 10\text{V}$		10	200	nA
	Collector current	$I_C$ $V_{CE} = 5\text{V}$ , $I_F = 20\text{mA}$	0.7		14	mA
Transfer characteristics	Collector to emitter saturation voltage	$V_{CE(\text{sat})}$ $I_F = 40\text{mA}$ , $I_C = 1\text{mA}$			0.4	V
	Response time	$t_r$ , $t_f^*$ $V_{CC} = 5\text{V}$ , $I_C = 1\text{mA}$ , $R_L = 100\Omega$		5		$\mu\text{s}$

\* Switching time measurement circuit



$t_d$ : Delay time

$t_r$ : Rise time (Time required for the collector current to increase from 10% to 90% of its final value)

$t_f$ : Fall time (Time required for the collector current to decrease from 90% to 10% of its initial value)

