XN01213 (XN1213)

Silicon NPN epitaxial planer transistor

For switching/digital circuits

Features

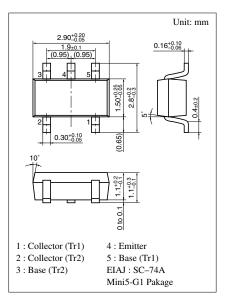
- Two elements incorporated into one package. (Emitter-coupled transistors with built-in resistor)
- Reduction of the mounting area and assembly cost by one half.

Basic Part Number of Element

• UNR1213(UN1213) × 2 elements

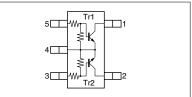
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Parameter		Symbol	Ratings	Unit
Rating of element	Collector to base voltage	V _{CBO}	50	V
	Collector to emitter voltage	V _{CEO}	50	V
	Collector current	I _C	100	mA
Overall	Total power dissipation	P _T	300	mW
	Junction temperature	Tj	150	°C
	Storage temperature	T _{stg}	-55 to +150	°C

Absolute Maximum Ratings (Ta=25°C)



Marking Symbol: 9L

Internal Connection

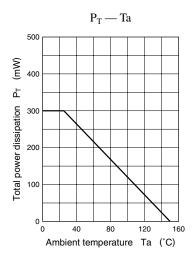


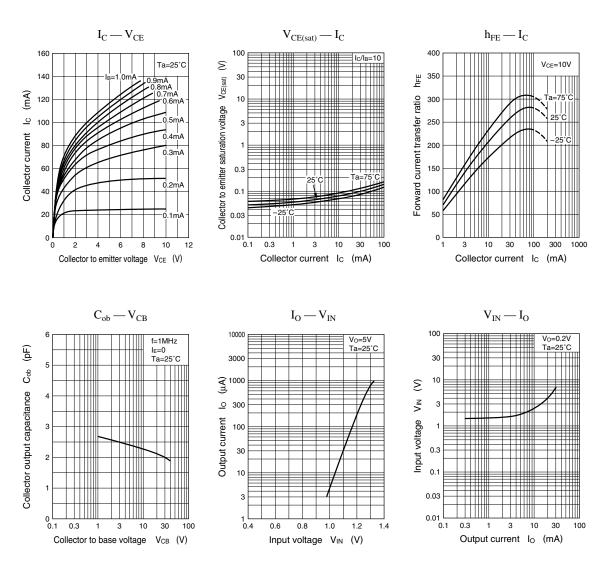
Parameter Symbol Conditions Unit min typ max Collector to base voltage $I_{C} = 10 \mu A, I_{E} = 0$ 50 V V_{CBO} Collector to emitter voltage V_{CEO} $I_{C} = 2mA, I_{B} = 0$ 50 V $V_{CB} = 50V, I_E = 0$ 0.1 I_{CBO} μΑ Collector cutoff current $V_{CE} = 50V, I_B = 0$ I_{CEO} 0.5 μΑ Emitter cutoff current I_{EBO} $V_{EB} = 6V, I_C = 0$ 0.1 mA $V_{CE} = 10V, I_C = 5mA$ Forward current transfer ratio h_{FE} 80 $V_{CE} = 10V, I_C = 5mA$ Forward current transfer h_{FE} ratio h_{FE} (small/large)^{*} 0.5 0.99 $I_{C} = 10mA, I_{B} = 0.3mA$ 0.25 v Collector to emitter saturation voltage V_{CE(sat)} Output voltage high level $V_{CC} = 5V, V_B = 0.5V, R_L = 1k\Omega$ 4.9 v VOH Output voltage low level VOL $V_{CC} = 5V, V_B = 3.5V, R_L = 1k\Omega$ 0.2 V $V_{CB} = 10V, I_E = -2mA, f = 200MHz$ Transition frequency \mathbf{f}_{T} 150 MHz -30% 47 kΩ Input resistance R_1 +30%Resistance ratio R_1/R_2 0.8 1.0 1.2

Electrical Characteristics (Ta=25°C)

*1 Ratio between 2 elements

Note) The Part number in the Parenthesis shows conventional part number.





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