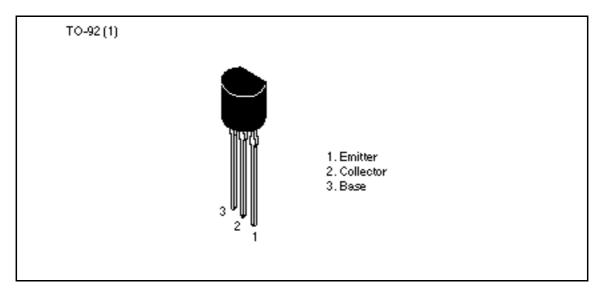
Silicon PNP Epitaxial

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Application

Low frequency amplifier

Outline





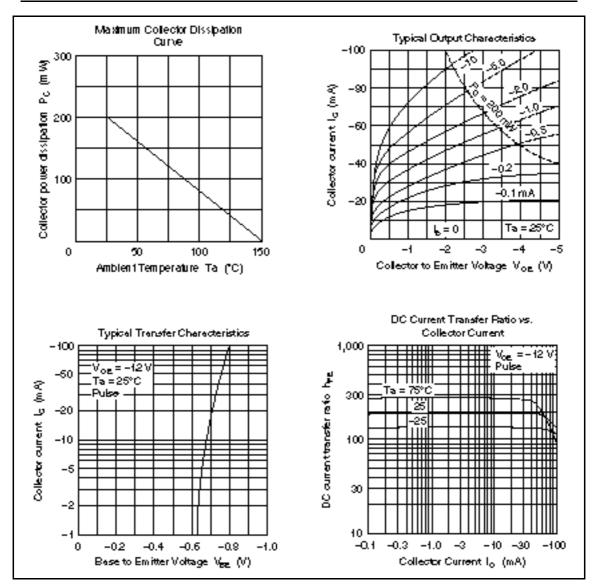
Absolute Maximum Ratings (Ta = 25° C)

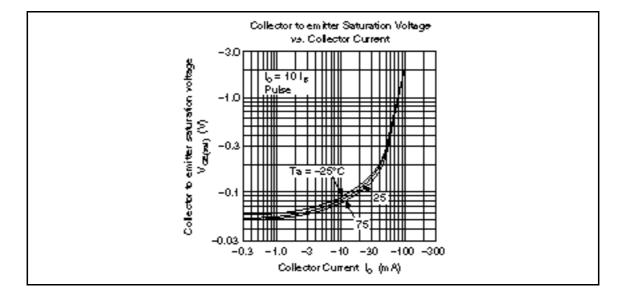
Item	Symbol	Ratings	Unit
Collector to base voltage	V _{CBO}	-200	V
Collector to emitter voltage	V _{CEO}	-200	V
Emitter to base voltage	V _{EBO}	-5	V
Collector current	I _c	-100	mA
Collector power dissipation	P _c	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	–55 to +150	°C

Electrical Characteristics (Ta = 25° C)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(BR)CBO}$	-200	_	—	V	$I_{c} = -10 \ \mu A, \ I_{E} = 0$
Collector to emitter breakdown voltage	$V_{(\text{BR})\text{CEO}}$	-200	_	_	V	$I_c = -0.5$ mA, $R_{BE} =$
Emitter to base breakdown voltage	$V_{(\text{BR})\text{EBO}}$	-5	_	_	V	$I_{\rm E} = -10 \ \mu A, \ I_{\rm C} = 0$
Collector cutoff current	I _{CEO}	_	—	-500	μA	$V_{ce} = -200 \text{ V}, \text{ R}_{be} =$
DC current transfer ratio	h _{FE}	100	—	250		$V_{ce} = -12 \text{ V}, I_c = -2 \text{ mA}^{*1}$
Collector to emitter saturation voltage	$V_{\text{CE(sat)}}$	_	_	-0.5	V	$I_{c} = -30 \text{ mA}, I_{B} = -3 \text{ mA}^{*1}$
Base to emitter voltage	V_{BE}	—	—	-1.0	V	$V_{ce} = -12 \text{ V}, \text{ I}_{c} = -2 \text{ mA}^{*1}$
Noto: 1 Dulas test						

Note: 1. Pulse test





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