

# □ MN158418

<b>Type</b>		<b>MN158418</b>	
<b>ROM (x8-bit)</b>		4K	
<b>RAM (x4-bit)</b>		256	
<b>Number of Instructions</b>		94	
<b>Minimum Instruction Execution Time</b>		2.0μs (at 4.5 to 5.5V, 4MHz)	
<b>Interrupts</b>		• RESET • External • Timer • Serial	
<b>Timer Counter</b>		<b>Timer Counter : 8-bit x 1</b> Clock Source .....1/2, 1/8, 1/32, 1/128 of System Clock Interrupt Source .....Overflow of Timer Counter	
<b>Serial Interface</b>		<b>Serial : 8-bit x 1 (Synchronous Type)</b> Clock Source .....System Clock, SBT Pin Input	
<b>I/O Pins</b>	<b>High Voltage I/O</b>	<b>12</b>	• Pch Open-drain (Breakdown Voltage -30V) : 12 • Specified pull-down Resistor available : 12 (Mask Option)
	<b>Input</b>	<b>7</b>	• Common use : 4 • Specified pull-up Resistor available : 2 • Specified pull-down Resistor available : 2 • Specified pull-up Resistor available : 3 (Software Programmable)
	<b>High Voltage Output</b>	<b>29</b>	• Pch Open-drain available (Breakdown Voltage -30V) : 29 • Specified pull-down Resistor available : 29 (MaskOption)
	<b>Output</b>	<b>8</b>	
<b>FLP</b>		41	
<b>Notes</b>		Crystal / CR Oscillation Selectable	
<b>Package</b>		SDIP064-P-0750	

## Electrical Characteristics

### Supply Current

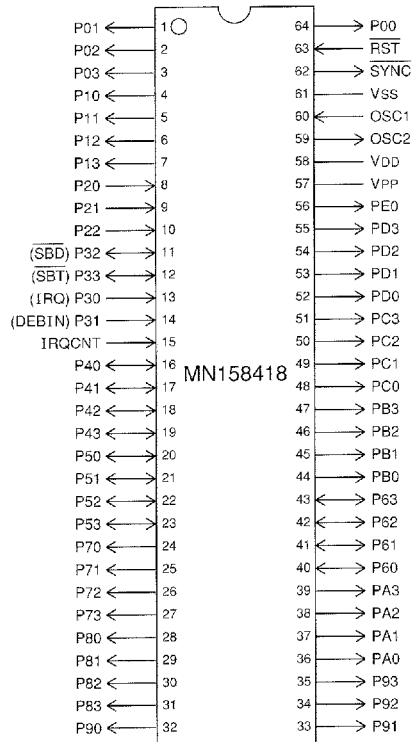
Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Operating Supply Current	IDD1	fosc=4.0MHz		2.5	6.0	mA
Supply Current at STOP	IDD3	VPP=0V			20	μA
Supply Current at HALT	IDD2	fosc=4.0MHz, VPP=0V		0.6	1.5	mA

(Ta= -10 to +70°C, VDD=5.0V, VSS=0V)

## Support Tool

<b>In-Circuit Emulator</b>	PX-ICE1500 + PX-PRB158418
<b>Piggyback</b>	Use <b>EP158418</b> as piggy in SDIP064-P-0750 package.

Pin Assignment



SDIP064-P-0750