

BUL805

High voltage fast-switching NPN Power Transistor

Preliminary Data

General features

- NPN Transistor
- High voltage capability
- Low spread of dynamic parameters
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed
- In compliance with the 2002/93/EC European Directive

Description

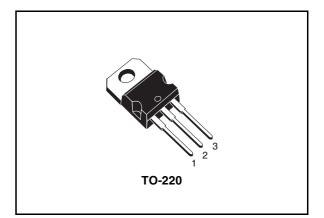
The device is manufactured using high voltage Multi-Epitaxial Planar technology for high switching speeds and medium voltage capability.

It uses a Cellular Emitter structure with planar edge termination to enhance switching speeds.

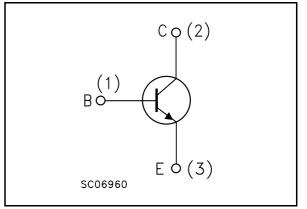
The device is designed for use as PFC in high frequency ballast half Bridge voltage fed topology.

Applications

- Electronic ballast for fluorescent lighting
- Dedicated for PFC solution in half-bridge voltage fed topology.



Internal schematic diagram



Order codes

Part Number	Marking	Package	Packing
BUL805	BUL805	TO-220	Tube

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Electrical ratings

Table 1. Absolute maximum rating

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage (V _{BE} = 0)	800	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	450	V
V _{EBO}	Emitter-base voltage (I _C = 0)	9	V
Ι _C	Collector current	5	А
I _{CM}	Collector peak current (t _P < 5ms)	10	А
Ι _Β	Base current	2	А
I _{BM}	Base peak current (t _P < 5ms)	4	А
P _{tot}	Total dissipation at $T_c = 25^{\circ}C$	80	W
T _{stg}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 2. Thermal data

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case max	1.56	°C/W
R _{thj-amb}	Thermal resistance junction-amb max	62.5	°C/W



2 Electrical characteristics

($T_{case} = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
ICES	Collector cut-off current (V _{BE} =-1.5V)	V _{CE} =800V V _{CE} =800V	T _j =125°C			100 500	μΑ μΑ
I _{CEO}	Collector cut-off current (I _B =0)	V _{CE} =450V				250	μA
V _{EBO}	Emitter-base voltage (I _C = 0)	I _E =10mA		9			v
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage (I _B = 0)	I _C =100mA	L =25mH	450			v
(1)	Collector-emitter saturation voltage	I _C =1A	I _B =0.2A			0.4	V
V _{CE(sat)} ⁽¹⁾		I _C =2A	I _B =0.4A			0.6	V
		I _C =3A	I _B =0.6A			0.8	V
		I _C =1A	I _B =0.2A			1.1	V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	I _C =2A	I _B =0.4A			1.3	V
	Vollage	I _C =3A	I _B =0.6A			1.5	V
h _{FE}	DC ourrent agin	I _C =10mA	V _{CE} =5V	10			
	DC current gain	I _C =2A	V _{CE} =5V	10		20	

Table 3. Electrical characteristics

Note (1) Pulsed duration = $300\mu s$, duty cycle $\leq 1.5\%$



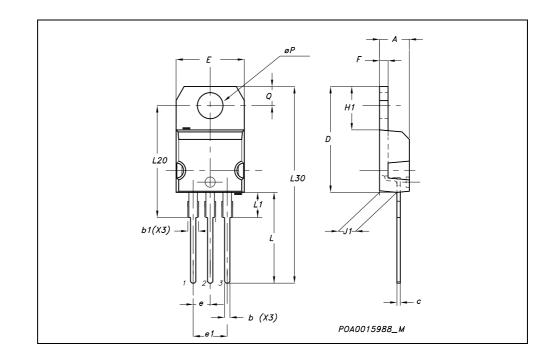
3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



DIM.		mm.			inch			
	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.		
А	4.40		4.60	0.173		0.181		
b	0.61		0.88	0.024		0.034		
b1	1.15		1.70	0.045		0.066		
С	0.49		0.70	0.019		0.027		
D	15.25		15.75	0.60		0.620		
Е	10		10.40	0.393		0.409		
е	2.40		2.70	0.094		0.106		
e1	4.95		5.15	0.194		0.202		
F	1.23		1.32	0.048		0.052		
H1	6.20		6.60	0.244		0.256		
J1	2.40		2.72	0.094		0.107		
L	13		14	0.511		0.551		
L1	3.50		3.93	0.137		0.154		
L20		16.40			0.645			
L30		28.90			1.137			
øP	3.75		3.85	0.147		0.151		
Q	2.65		2.95	0.104		0.116		





4 Revision history

Table 4. Revision history

Date	Revision	Changes
19-May-2006	1	Initial release.



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