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Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

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Keep safety first in your circuit designs!

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2SK2085

Silicon N-Channel MOS FET

RENESAS

ADE-208-1343 (Z)
1st. Edition
Mar. 2001

Application

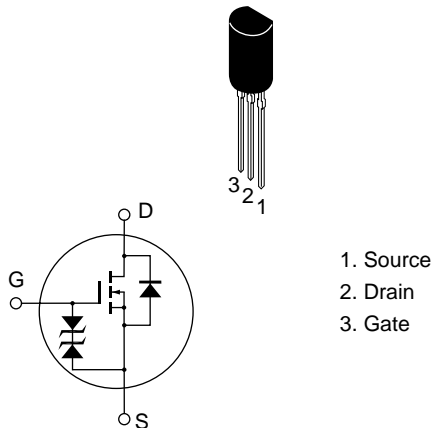
High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device can be driven from 5 V source
- Suitable for Switching regulator, DC - DC converter

Outline

TO-92 Mod



Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	100	V
Gate to source voltage	V_{GSS}	± 20	V
Drain current	I_{D}	1.0	A
Drain peak current	$I_{\text{D(pulse)}}^{*1}$	4.0	A
Body to drain diode reverse drain current	I_{DR}	1.0	A
Channel dissipation	P_{ch}^{*2}	0.9	W
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

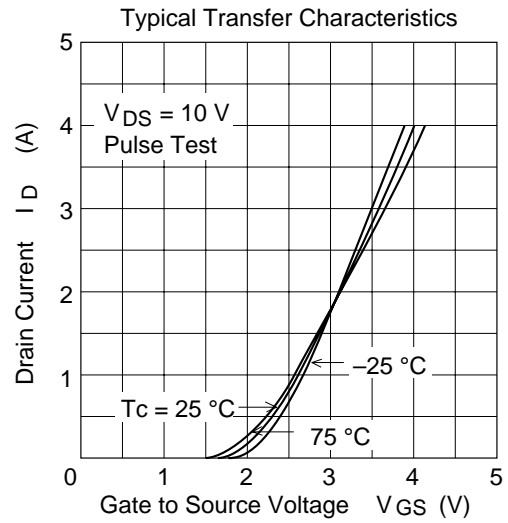
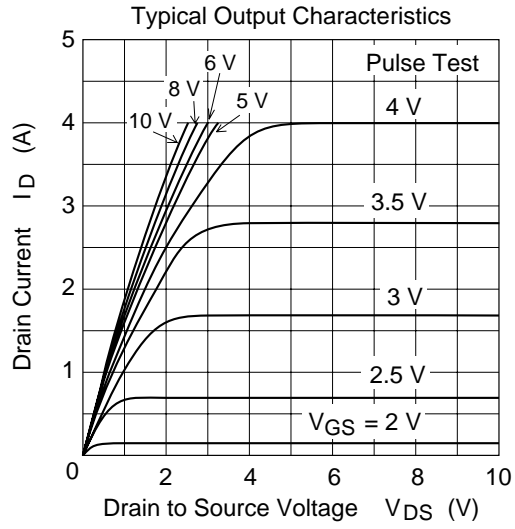
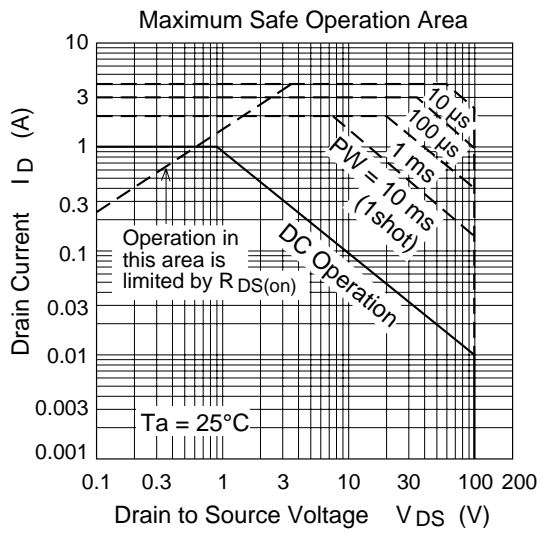
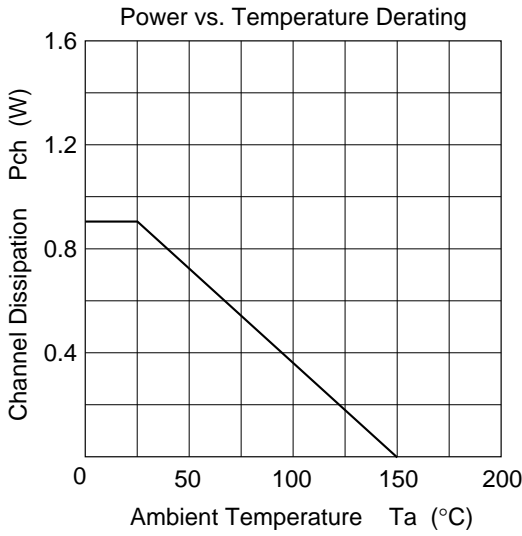
Notes 1. PW 10 μs , duty cycle 1 %

2. Value at $T_c = 25^\circ\text{C}$

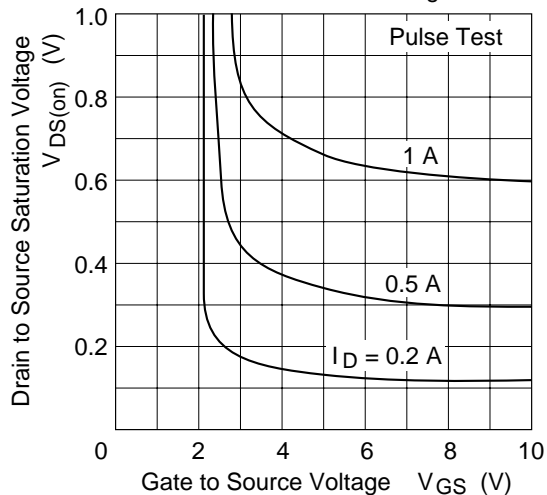
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	100	—	—	V	$I_D = 10 \text{ mA}$, $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	± 20	—	—	V	$I_G = \pm 100 \text{ }\mu\text{A}$, $V_{DS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 16 \text{ V}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	100	μA	$V_{DS} = 80 \text{ V}$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	—	2.0	V	$I_D = 1 \text{ mA}$, $V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	0.6	0.9		$I_D = 0.5 \text{ A}$ $V_{GS} = 10 \text{ V}^{*1}$
		—	0.75	1.35		$I_D = 0.5 \text{ A}$ $V_{GS} = 4 \text{ V}^{*1}$
Forward transfer admittance	$ y_{fs} $	0.7	1.2	—	S	$I_D = 0.5 \text{ A}$ $V_{DS} = 10 \text{ V}^{*1}$
Input capacitance	C_{iss}	—	130	—	pF	$V_{DS} = 10 \text{ V}$
Output capacitance	C_{oss}	—	50	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	C_{rss}	—	12	—	pF	$f = 1 \text{ MHz}$
Turn-on delay time	$t_{d(on)}$	—	7	—	ns	$I_D = 0.5 \text{ A}$
Rise time	t_r	—	6.5	—	ns	$V_{GS} = 10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	55	—	ns	$R_L = 60$
Fall time	t_f	—	20	—	ns	
Body to drain diode forward voltage	V_{DF}	—	0.85	—	V	$I_F = 1.0 \text{ A}$, $V_{GS} = 0$
Body to drain diode reverse recovery time	t_{rr}	—	80	—	ns	$I_F = 1.0 \text{ A}$, $V_{GS} = 0$, $dI_F / dt = 50 \text{ A} / \mu\text{s}$

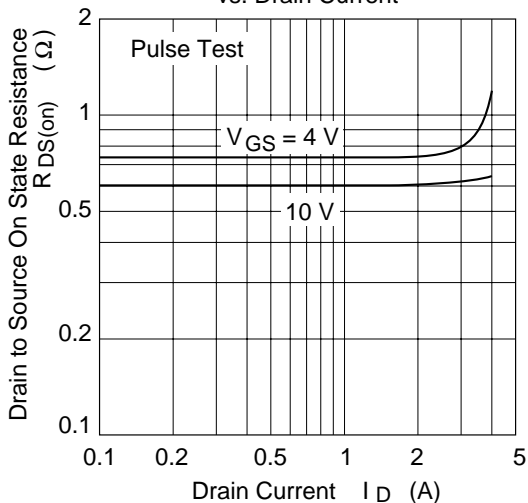
Note 1. Pulse Test



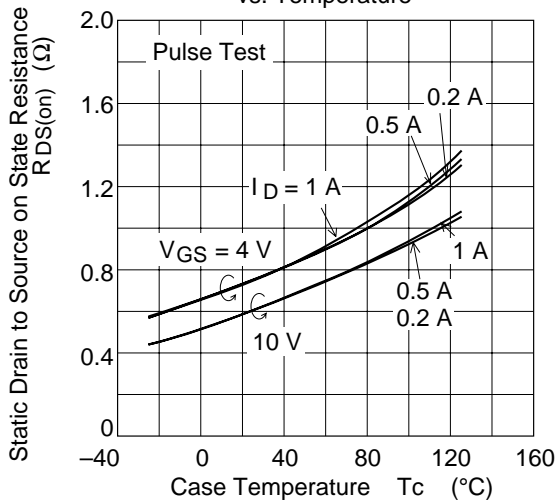
Drain to Source Saturation Voltage vs. Gate to Source Voltage



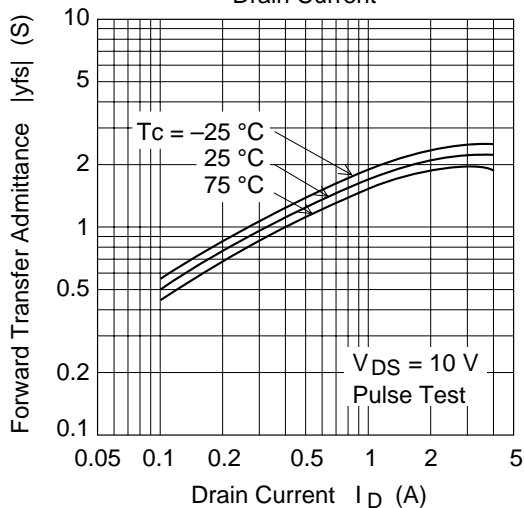
Static Drain to Source State Resistance vs. Drain Current



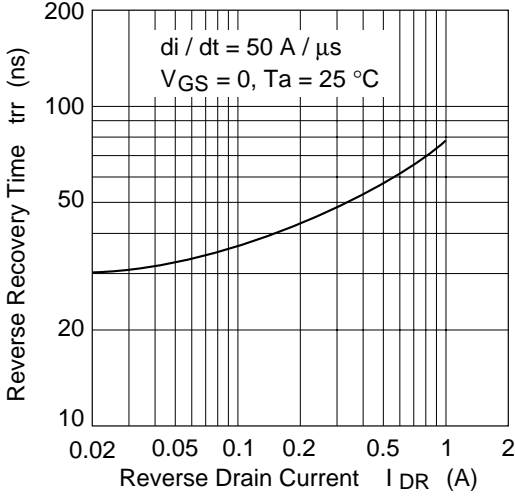
Static Drain to Source on State Resistance vs. Temperature



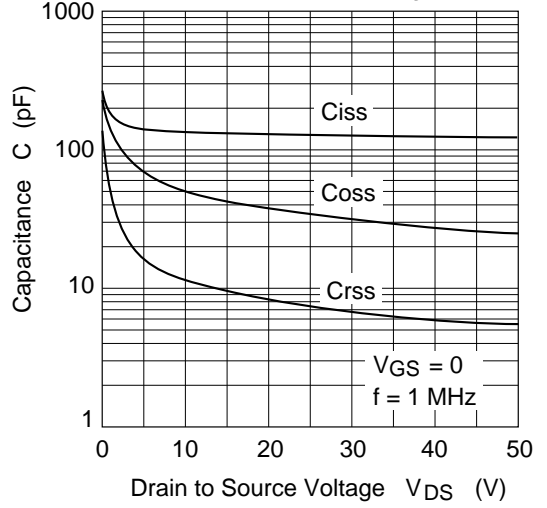
Forward Transfer Admittance vs. Drain Current



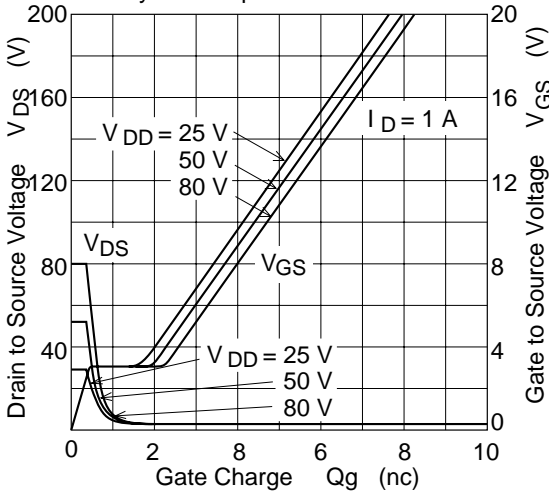
Body to Drain Diode Reverse Recovery Time



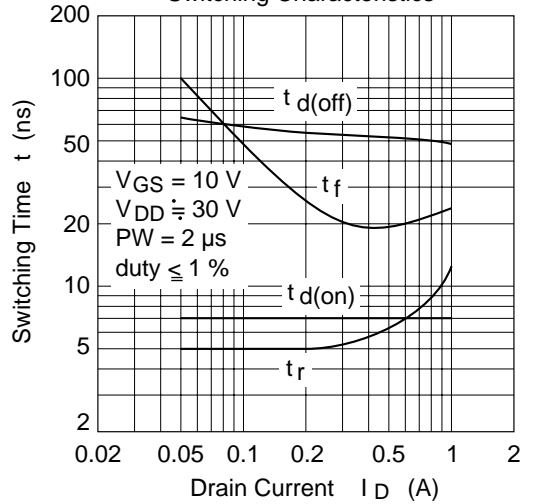
Typical Capacitance vs. Drain to Source Voltage

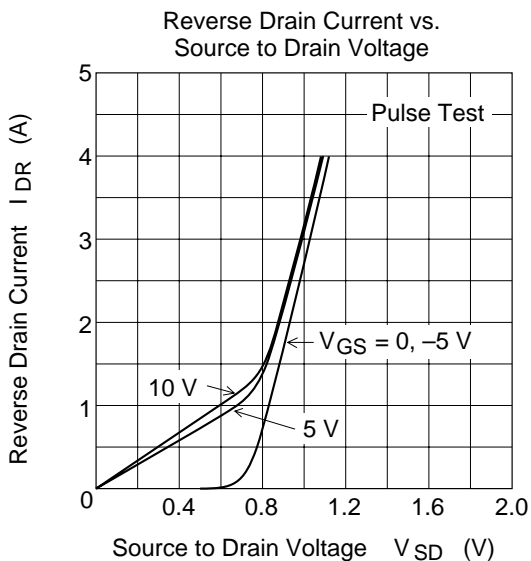


Dynamic Input Characteristics

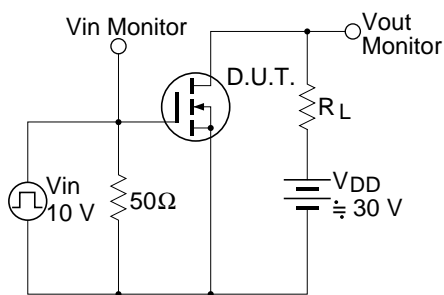


Switching Characteristics

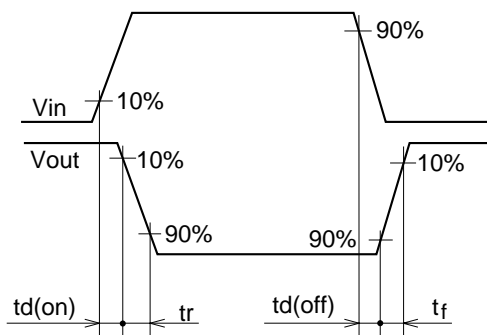




Switching Time Test Circuit

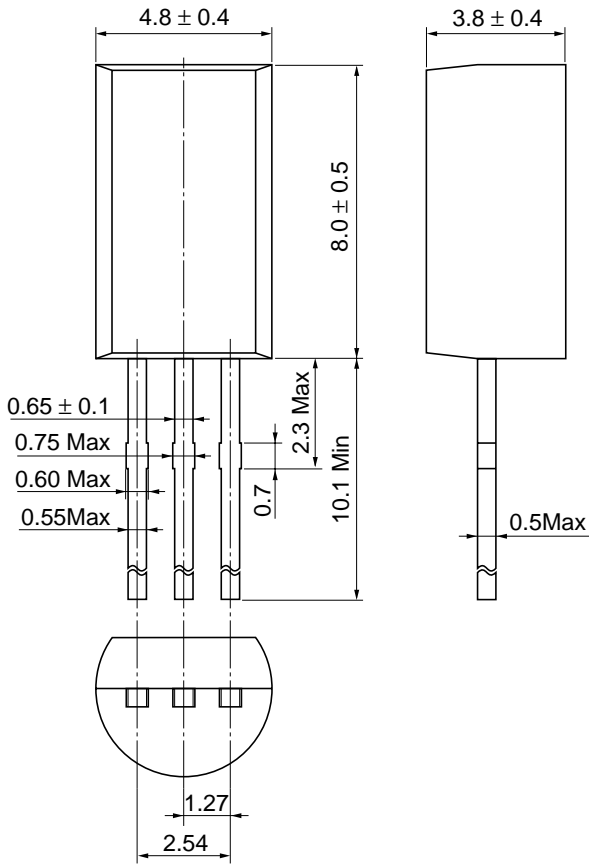


Waveform



Package Dimensions

As of January, 2001
Unit: mm



Hitachi Code	TO-92 Mod
JEDEC	—
EIAJ	Conforms
Mass (reference value)	0.35 g

Cautions

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