

# MB4207

## Frequency-to-Voltage Converter

### DESCRIPTION

The Fujitsu MB4207 is a single power supply frequency-to-voltage converter with a comparator. The MB4207 can stabilize a noisy signal using the charge pump driven by a Schmitt trigger and flip-flop circuit.

The comparator provides precise hysteresis output due to clamping at the reference voltage with Zener diode.

### FEATURES

- An RC pair provides the coefficient of conversion:

$$V_O = \frac{2}{3} \cdot V_Z \cdot C_T \cdot R_T \cdot f_{IN}$$

- Output is clamped at the built-in reference voltage (High-level).
- Positive-edge trigger frequency input.

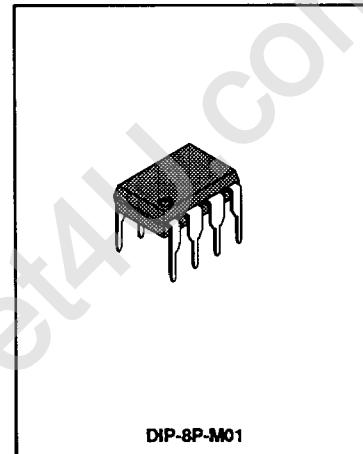
### ABSOLUTE MAXIMUM RATING ( $T_A = 25^\circ\text{C}$ )

Rating	Symbol	Value	Unit
Supply Voltage	$V_{CC}$	24 <sup>1</sup>	V
Surge Voltage at $V_{CC}$	$V_{CCS}$	100 <sup>2</sup>	V
Zener Current	$I_Z$	30	mA
Power Dissipation	$P_D$	300 <sup>3</sup>	mW
Operating Temperature	$T_{OP}$	-30 to +85	°C
Storage Temperature	$T_{STG}$	-55 to +125	°C

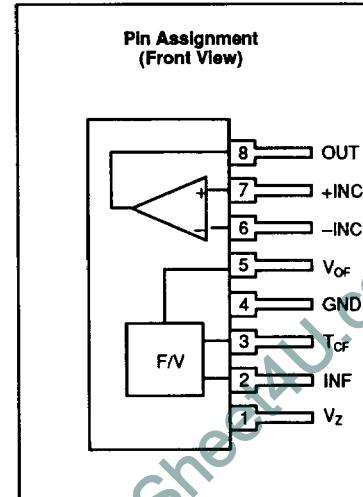
Notes:  
<sup>1</sup> $R_s = 680 \Omega$

<sup>2</sup> $t < 50 \text{ msec}, R_s = 680 \Omega$

<sup>3</sup> $T_A < 85^\circ\text{C}$



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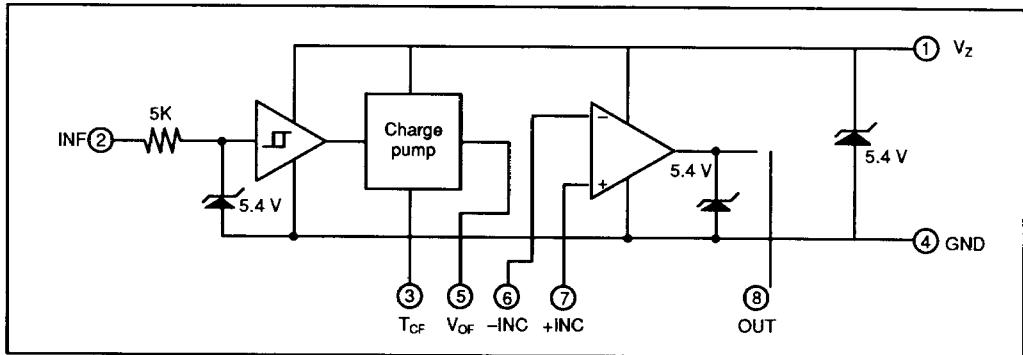


Pin Assignment  
(Front View)

**Note:** Permanent device damage may occur if absolute maximum ratings are exceeded. Functional operation should be restricted to the conditions as detailed in the operation sections of this data sheet. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

This device contains circuitry to protect the inputs against damage due to high static voltages or electric fields. However, it is advised that normal precautions be taken to avoid application of any voltage higher than maximum rated voltages to this high impedance circuit.

## BLOCK DIAGRAM



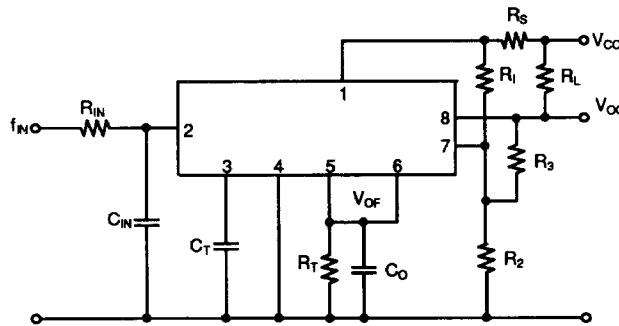
## ELECTRICAL CHARACTERISTICS

(T<sub>A</sub> = 25 °C, V<sub>CC</sub> = 12V, R<sub>S</sub> = 680Ω)

Parameter	Conditions	Symbol	Value			Unit
			Min.	Typ.	Max.	
Supply Current	R <sub>S</sub> =0Ω, V <sub>CC</sub> =4.8V	I <sub>CC</sub>	—	3.0	5.0	mA
Reference Voltage	See Figure 1	V <sub>Z</sub>	5.0	5.4	5.8	V
	V <sub>CC</sub> = 10 to 16 V	ΔV <sub>Z</sub>	—	0.05	0.1	V
V / F	Input High Level	V <sub>IH</sub>	2.4	—	24	V
	Input Low Level	V <sub>IL</sub>	0	—	1.2	V
C O N V E R T E R	Positive-edge		1	—	—	V/ms
	Negative-edge		0.1	—	—	V/ms
I N P U T	Input Current	I <sub>INF</sub>	—	4	8 0.1	mA mA
O U T P U T	Output Current	I <sub>OF</sub>	0.26	0.4	0.58	mA
F/V Coefficient	C <sub>T</sub> = 0.1 μF, R <sub>T</sub> = 47k Ω f = 100 Hz	K <sup>1</sup>	0.9	1.0	1.1	
L I N E A R I T Y	Linearity Error	V <sub>T</sub> = 0.1 μF, R <sub>T</sub> = 47k Ω <sup>2</sup>	—	±0.3	—	%
C O M P A R A T O R	Input Offset Voltage	V <sub>IOC</sub>	—	2	10	mA
	Input Bias Current	I <sub>IBC</sub>	—	0.5	3	μA
	Common Mode Input Voltage	V <sub>CM</sub>	0	—	3	V
V O L T A G E	Voltage Gain	A <sub>V</sub>	—	100	—	dB
G A I N	I <sub>SINK</sub> = 3 mA	V <sub>OLC</sub>	—	0.1	0.2	V
O U T P U T	I <sub>L</sub> = 0.5 mA	V <sub>OLC</sub>	5.0	5.4	5.8	V
I N T E R F A C E	Sink Current	I <sub>SINK</sub>	8	20	—	mA

Notes: <sup>1</sup>V<sub>O</sub> = (2/3) · K · V<sub>Z</sub> · C<sub>T</sub> · R<sub>T</sub> · f<sub>IN</sub><sup>2</sup>At 50/100 Hz on the basis at 100 Hz.<sup>3</sup>The current flows outward from the IC.

## APPLICATION EXAMPLE

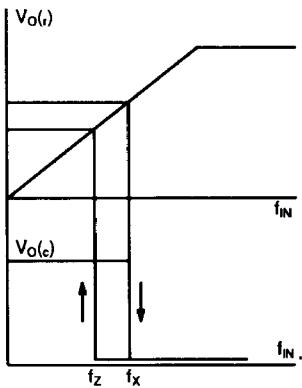


Operating frequency of the comparator  
is provided by the following equations.

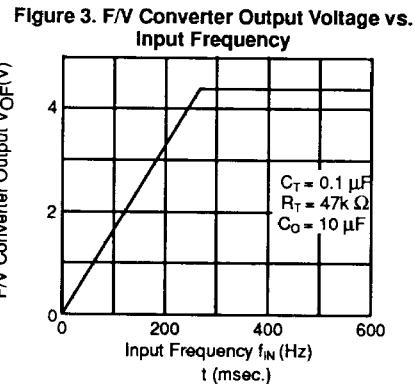
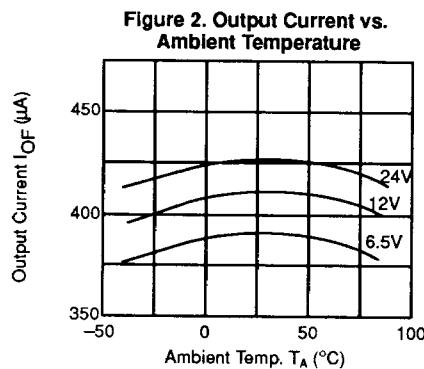
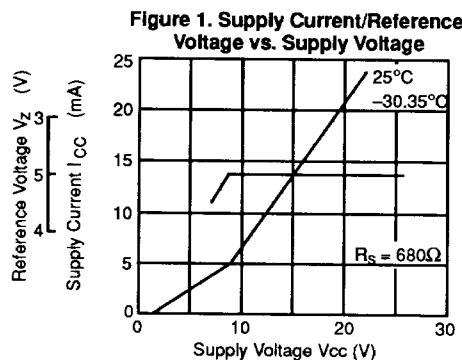
$$f_X \approx \frac{3}{2C_T R_T} \cdot \frac{R_Z}{R_2 + R_1 // R_3}$$

$$f_Z \approx \frac{3}{2C_T R_T} \cdot \frac{R_2 // R_3}{R_I + R_2 // R_3}$$

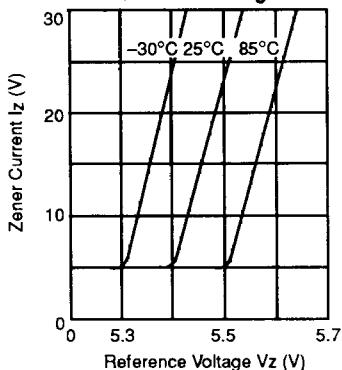
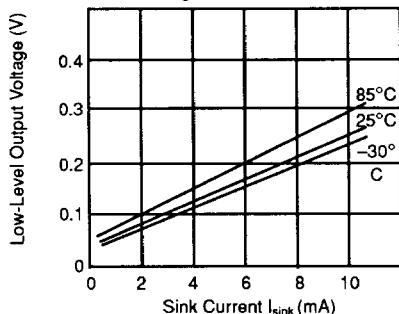
$R_{IN}$  and  $C_{IN}$  are needed when the input  
has chattering noise.



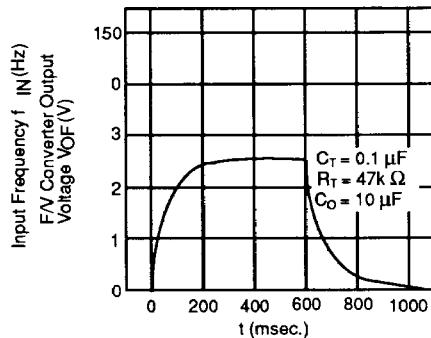
## TYPICAL CHARACTERISTIC CURVES



## TYPICAL CHARACTERISTIC CURVES (Continued)

**Figure 4. Zener Current vs. Reference Voltage****Figure 5. Low-Level Output Voltage vs. Sink Current**

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**Figure 6. F/V Converter Transition Characteristics**

**PACKAGE DIMENSIONS**