General purpose (dual digital transistors) IMH20

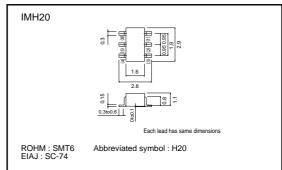
● Features

- 1) Two DTC323T chips in a SMT package.
- 2) Mounting possible with SMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.

●Structure

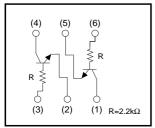
Epitaxial planar type NPN silicon transistor

●External dimensions (Unit : mm)



The following characteristics apply to both DTr1 and DTr2.

●Equivalent circuit



Packaging specifications

	Package	Taping			
	Code	T110			
Type	Basic ordering unit (pieces)	3000			
IMH20		0			

● Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit	
Collector-base voltage	Vсво	30	V	
Collector-emitter voltage	Vceo	15	V	
Emitter-base voltage	VEBO	5	V	
Collector current	Ic	600	mA	
Collector power dissipation	Pc	300 (TOTAL)	mW *	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

st 200mW per element must not be exceeded.

●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	30	_	_	V	Ic=50μA
Collector-emitter breakdown voltage	BVceo	15	-	_	V	Ic=1mA
Emitter-base breakdown voltage	ВVево	5	_	_	V	Iε=50μA
Collector cutoff current	Ісво	_	-	0.5	μА	Vcb=20V
Emitter cutoff current	ІЕВО	_	_	0.5	μА	V _{EB} =4V
Collector-emitter saturation voltage	VCE(sat)	_	40	80	mV	Ic/Iв=50mA/2.5mA
DC current transfer ratio	hfe	100	250	600	_	Ic=50mA , VcE=5V
Input resistance	R ₁	1.64	2.2	2.86	kΩ	-
Transition frequency	f⊤	_	200	_	MHz	Vc=10V, I==-50mA, f=100MHz *
Output on resistoance	Ron	_	0.65	_	Ω	Vce=7V, Ie=1kΩ, f=1KHz

^{*} Transition frequency of the device

•Electrical characteristic curves

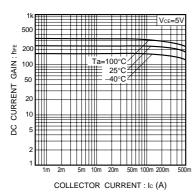


Fig.1 DC current gain vs. collector current

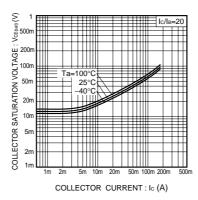


Fig.2 Collector-emitter saturation voltage vs. collector current

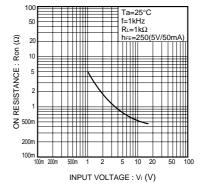


Fig.3 Output on resistance vs. input voltage

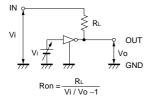


Fig.4 Output on resistance test circuit

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