

# SANYO Semiconductors DATA SHEET

# 2SJ662 — General-Purpose Switching Device Applications

#### **Features**

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 4V drive.
- · Motor drive, DC / DC converter.
- · Avalanche resistance guarantee.

# **Specifications**

#### **Absolute Maximum Ratings** at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		-60	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		-50	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-200	Α
Allowable Power Dissipation	D-		1.65	W
	PD	Tc=25°C	75	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Enargy (Single Pulse) *1	EAS		435	mJ
Avalanche Current *2	IAV		-50	Α

Note: \*1 V<sub>DD</sub>=30V, L=200μH, I<sub>AV</sub>=-50A

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			11.3
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =-1mA, V <sub>G</sub> S=0V	-60			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V			-1	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> = ±16V, V <sub>DS</sub> =0V			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-1.2		-2.6	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =-10V, I <sub>D</sub> =-25A	25	42		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =-25A, V <sub>G</sub> S=-10V		20	26	mΩ
	R <sub>DS</sub> (on)2	I <sub>D</sub> =-25A, V <sub>G</sub> S=-4V		27	38	mΩ

Marking: J662 Continued on next page.

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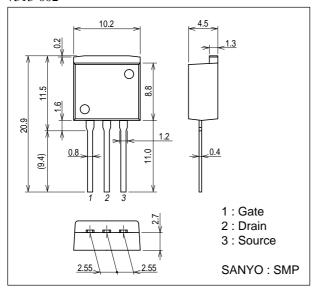
<sup>\*2</sup> L≤200μH, Single pulse

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Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	- Unit
Input Capacitance	Ciss	V <sub>DS</sub> =-20V, f=1MHz		6500		pF
Output Capacitance	Coss	V <sub>DS</sub> =-20V, f=1MHz		700		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =-20V, f=1MHz		500		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		53		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit.		330		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		480		ns
Fall Time	tf	See specified Test Circuit.		270		ns
Total Gate Charge	Qg	V <sub>DS</sub> =-30V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-50A		120		nC
Gate-to-Source Charge	Qgs	VDS=-30V, VGS=-10V, ID=-50A		22		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =-30V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-50A		22		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-50A, V <sub>G</sub> S=0V		-1.03	-1.2	V

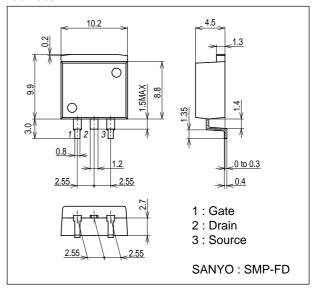
# **Package Dimensions**

unit : mm 7513-002

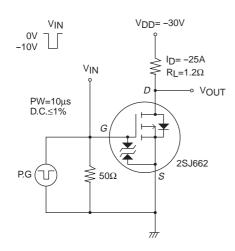


# **Package Dimensions**

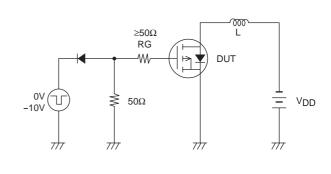
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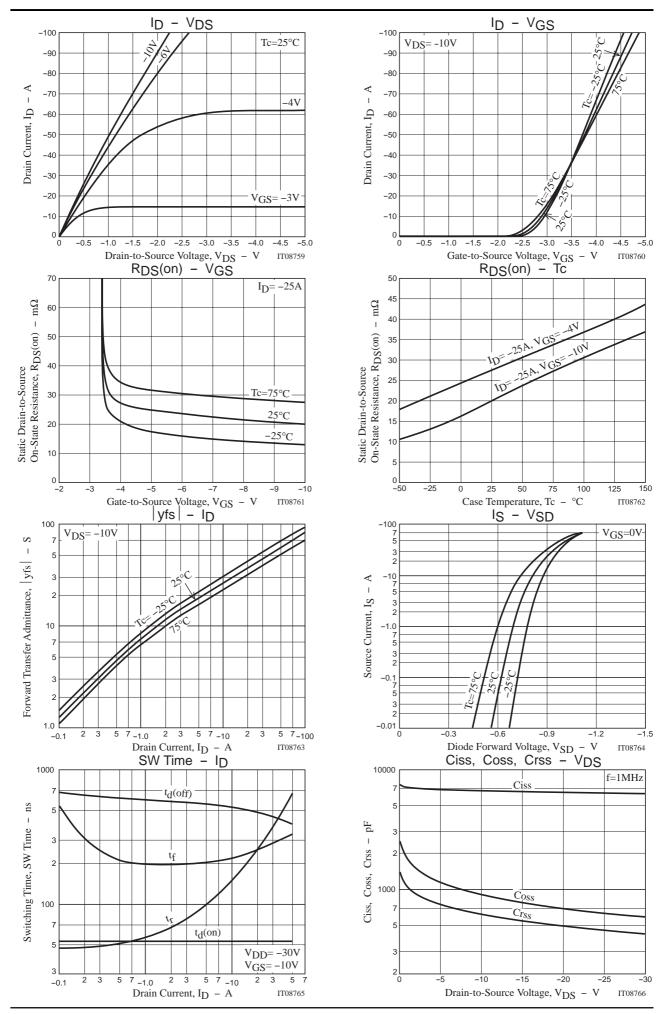


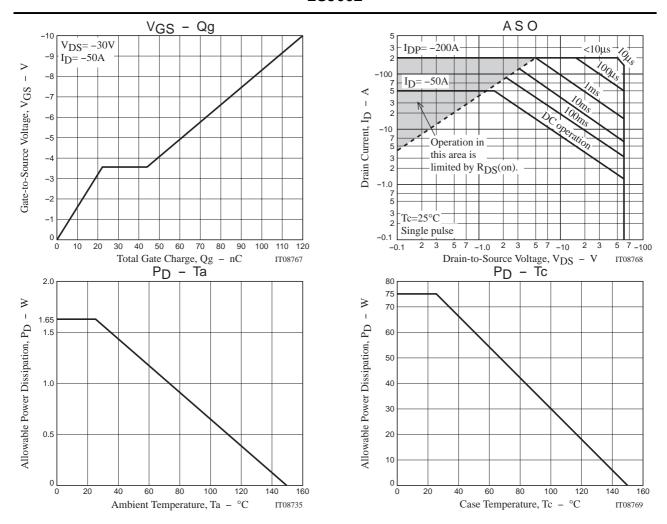
# **Switching Time Test Circuit**



### **Avalanche Resistance Test Circuit**







Note on usage: Since the 2SJ662 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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