

STK0290P

Advanced Power MOSFET

SWITCHING REGULATOR APPLICATIONS

Features

• High Voltage: BV_{DSS}=900V(Min.)

• Low C_{rss} : $C_{rss}=5.0F(Typ.)$

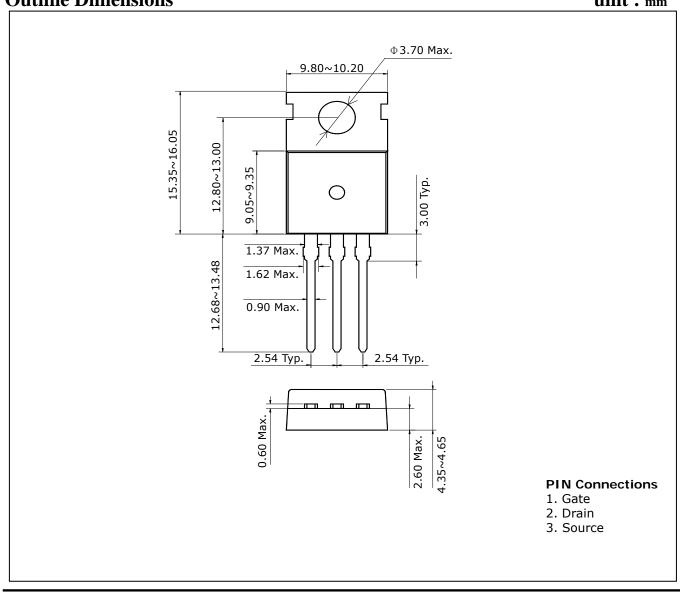
• Low gate charge : Qg=18.2nC(Typ.)

• Low $R_{DS(on)}$: $R_{DS(on)} = 7.2\Omega(Max.)$

Ordering Information

Type NO.	Marking	Package Code		
STK0290P	STK0290	TO-220AB-3L		

Outline Dimensions unit: mm



Preliminary

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Absolute maximum ratings

(Tc=25°C)

Characteristic		Symbol		Rating	Unit		
Drain-source voltage		V_{DSS}		900	V		
Gate-source voltage		V_{GSS}		±30	V		
Drain current (DC)		I _D	T _C =25℃	2.2	А		
			T _C =100 ℃	1.39	Α		
Drain current (Pulsed) *		I_{DM}		8.8	Α		
Drain power dissipation		P_{D}		85	W		
Avalanche current (Single)	2	I _{AS}		2.2	Α		
Single pulsed avalanche energy	2		E _{AS}	170	mJ		
Avalanche current (Repetitive)	1	I_{AR}		8.8	Α		
Repetitive avalanche energy	1	E _{AR}		E _{AR}		8.5	mJ
Junction temperature		T ₁		150	°C		
Storage temperature range		T_{stg}		-55~150	°C		

^{*} Limited by maximum junction temperature

Characteristic		Symbol	Typ.	Max	Unit
Thermal	Junction-case	$R_{th(\mathtt{J-C})}$	-	1.47	°C/W
resistance	Junction-ambient	$R_{th(\mathtt{J-A})}$	-	62.5	C/ W

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Electrical Characteristics

(Tc=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	$V_{(BR)DSS}$	I _D =250 μA, V _{GS} =0V	900	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$I_D=250 \mu A$, $V_{GS}=V_{DS}$	3.0	-	5.0	V
Drain-source cut-off current	I_{DSS}	V _{DS} =900V, V _{GS} =0V	-	-	10	μA
Gate leakage current	I_{GSS}	V _{DS} =0V, V _{GS} =±30V	-	-	±100	nA
Drain-source on-resistance 4	R _{DS(on)}	V _{GS} =10V, I _D =1.1A	-	5.6	7.2	Ω
Forward transfer conductance 4	g _{fs}	V_{DS} =50V, I_{D} =1.1A	-	2.0	-	S
Input capacitance	C _{iss}		-	560	675	
Output capacitance	C _{oss}	V_{GS} =0V, V_{DS} =25V f=1 MHz	-	50	65	рF
Reverse transfer capacitance	C_{rss}		-	5.0	7.0	
Turn-on delay time	t _{d(on)}	V _{DD} =450V, I _D =2.2A	-	15	40	
Rise time	t _r	$R_{G} = 25\Omega$	-	35	80	ns
Turn-off delay time	t _{d(off)}	34	-	20	50	110
Fall time	t _f	34	-	30	70	
Total gate charge	Q_g	V _{DS} =720V, V _{GS} =10V	-	18.2	21.5	
Gate-source charge	Q_{gs}	I _D =2.2A	-	3.8	-	nC
Gate-drain charge	Q_{gd}	34	-	6.0	-	

Source-Drain Diode Ratings and Characteristics

(Tc=25°C)

Characteristic	Symbol	Test Condition	Min	Тур	Max	Unit
Source current (DC)	I_S	Integral reverse diode	-	-	2.2	А
Source current (Pulsed)	I_{SP}	in the MOSFET	-	-	8.8	
Forward voltage) V _{SD}	V_{GS} =0V, I_S =2.2A	-	-	1.4	V
Reverse recovery time	t _{rr}	I _S =2.2A, V _{GS} =0V	-	400	-	ns
Reverse recovery charge	Q_{rr}	$dI_S/dt=100A/\mu S$	-	1.6	-	μС

Note;

① Repetitive rating: Pulse width limited by maximum junction temperature

② L=65mH, I_{AS} =2.2A, V_{DD} =50V, R_{G} =25 Ω

③ Pulse Test: Pulse width≤ 300 \(\mu \sigma \), Duty cycle≤ 2%

4 Essentially independent of operating temperature

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