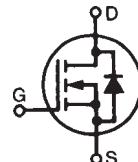


PolarHV™ Power MOSFET

IXTP 1R6N50P IXTY 1R6N50P

V_{DSS} = 500 V
 I_{D25} = 1.6 A
 $R_{DS(on)}$ ≤ 6.5 Ω

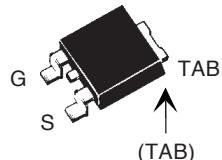
N-Channel Enhancement Mode



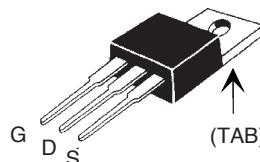
Symbol	Test Conditions	Maximum Ratings		
V_{DSS}	$T_J = 25^\circ\text{C}$ to 150°C	500		V
V_{DGR}	$T_J = 25^\circ\text{C}$ to 150°C ; $R_{GS} = 1 \text{ M}\Omega$	500		V
V_{GS}	Continuous	± 30		V
V_{GSM}	Transient	± 40		V
I_{D25}	$T_c = 25^\circ\text{C}$	1.6		A
I_{DM}	$T_c = 25^\circ\text{C}$, pulse width limited by T_{JM}	2.5		A
I_{AR}	$T_c = 25^\circ\text{C}$	1.6		A
E_{AR}	$T_c = 25^\circ\text{C}$	5		mJ
E_{AS}	$T_c = 25^\circ\text{C}$	75		mJ
dv/dt	$I_s \leq I_{DM}$, $di/dt \leq 100 \text{ A}/\mu\text{s}$, $V_{DD} \leq V_{DSS}$, $T_J \leq 150^\circ\text{C}$, $R_G = 50 \Omega$	10		V/ns
P_D	$T_c = 25^\circ\text{C}$	43		W
T_J		-55 ... +150		$^\circ\text{C}$
T_{JM}		150		$^\circ\text{C}$
T_{stg}		-55 ... +150		$^\circ\text{C}$
T_L	1.6 mm (0.062 in.) from case for 10s Maximum tab temperature for soldering TO-252 package for 10s	300 260		$^\circ\text{C}$
M_d	Mounting torque (TO-220)	1.13/10	Nm/lb.in.	
Weight	TO-252 TO-220	0.8 4		g

Symbol	Test Conditions	Characteristic Values		
($T_J = 25^\circ\text{C}$, unless otherwise specified)		Min.	Typ.	Max.
V_{DSS}	$V_{GS} = 0 \text{ V}$, $I_D = 250 \mu\text{A}$	500		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu\text{A}$	3.0		5.5 V
I_{GSS}	$V_{GS} = \pm 30 \text{ V}_{DC}$, $V_{DS} = 0$		± 100	nA
I_{DSS}	$V_{DS} = V_{DSS}$ $V_{GS} = 0 \text{ V}$		5 50	μA
		$T_J = 125^\circ\text{C}$		
$R_{DS(on)}$	$V_{GS} = 10 \text{ V}$, $I_D = 0.5 I_{D25}$ Pulse test, $t \leq 300 \mu\text{s}$, duty cycle $d \leq 2 \%$		6.5	Ω

TO-252 (IXTY)



TO-220 (IXTP)



G = Gate D = Drain
 S = Source TAB = Drain

Features

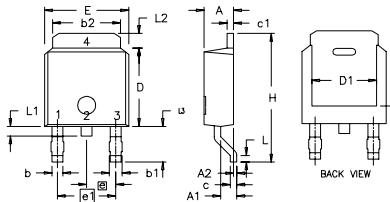
- International standard packages
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
 - easy to drive and to protect

Advantages

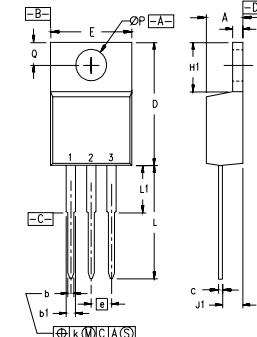
- Easy to mount
- Space savings
- High power density

Symbol	Test Conditions	Characteristic Values			
		(T _J = 25°C, unless otherwise specified)	Min.	Typ.	Max.
g_{fs}	V _{DS} = 20 V; I _D = 0.5 I _{D25} , pulse test	0.7	1.4	S	
C_{iss} C_{oss} C_{rss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz	140	pF		
		20	pF		
		2.6	pF		
t_{d(on)} t_r t_{d(off)} t_f	V _{GS} = 10 V, V _{DS} = 0.5 V _{DSS} , I _D = 0.5 I _{D25} R _G = 20 Ω (External)	10	ns		
		16	ns		
		25	ns		
		16	ns		
Q_{g(on)} Q_{gs} Q_{gd}	V _{GS} = 10 V, V _{DS} = 0.5 V _{DSS} , I _D = 0.5 I _{D25}	3.9	nC		
		1.4	nC		
		1.3	nC		
R_{thJC}				2.9 K/W	

Symbol	Test Conditions	Characteristic Values			
		(T _J = 25°C, unless otherwise specified)	Min.	typ.	Max.
I_s	V _{GS} = 0 V			1.6	A
I_{SM}	Repetitive			2.5	A
V_{SD}	I _F = I _s , V _{GS} = 0 V, Pulse test, t ≤ 300 μs, duty cycle d ≤ 2 %			1.5	V
t_{rr}	I _F = 1.6 A, -di/dt = 100 A/μs V _R = 100V	400		ns	

TO-252 AA Outline


Dim.	Millimeter Min.	Millimeter Max.	Inches Min.	Inches Max.
A	2.19	2.38	0.086	0.094
A1	0.89	1.14	0.035	0.045
A2	0	0.13	0	0.005
b	0.64	0.89	0.025	0.035
b1	0.76	1.14	0.030	0.045
b2	5.21	5.46	0.205	0.215
c	0.46	0.58	0.018	0.023
c1	0.46	0.58	0.018	0.023
D	5.97	6.22	0.235	0.245
D1	4.32	5.21	0.170	0.205
E	6.35	6.73	0.250	0.265
E1	4.32	5.21	0.170	0.205
e	2.28 BSC		0.090 BSC	
e1	4.57 BSC		0.180 BSC	
H	9.40	10.42	0.370	0.410
L	0.51	1.02	0.020	0.040
L1	0.64	1.02	0.025	0.040
L2	0.89	1.27	0.035	0.050
L3	2.54	2.92	0.100	0.115

TO-220 Outline


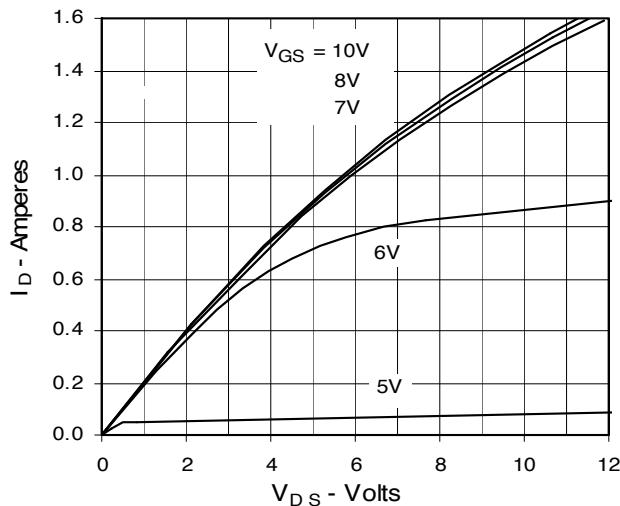
Pins: 1 - Gate 2 - Drain

SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.170	.190	4.32	4.83
b	.025	.040	0.64	1.02
b1	.045	.065	1.15	1.65
c	.014	.022	0.35	0.56
D	.580	.630	14.73	16.00
E	.390	.420	9.91	10.66
e	.100 BSC		2.54 BSC	
F	.045	.055	1.14	1.40
H1	.230	.270	5.85	6.85
J1	.090	.110	2.29	2.79
k	0	.015	0	0.38
L	.500	.550	12.70	13.97
L1	.110	.230	2.79	5.84
ØP	.139	.161	3.53	4.08
Q	.100	.125	2.54	3.18

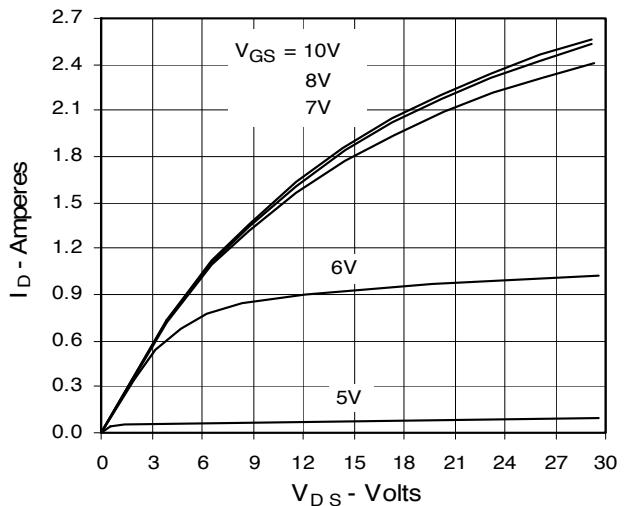
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**Fig. 1. Output Characteristics
@ 25°C**



**Fig. 2. Extended Output Characteristics
@ 25°C**



**Fig. 3. Output Characteristics
@ 125°C**

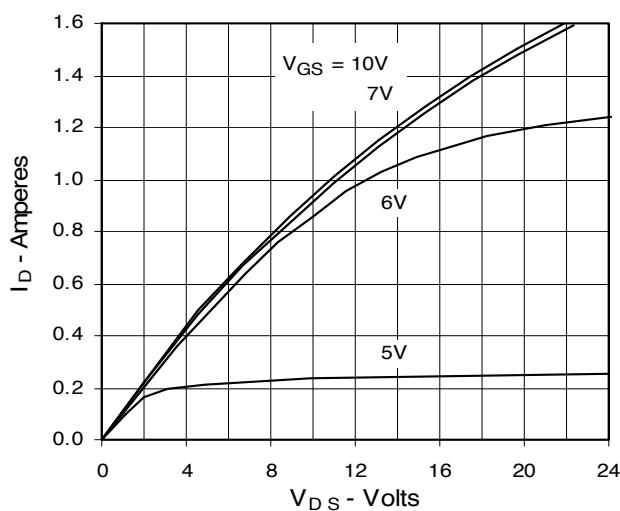


Fig. 4. $R_{DS(on)}$ Normalized to 0.5 I_{D25} Value vs. Junction Temperature

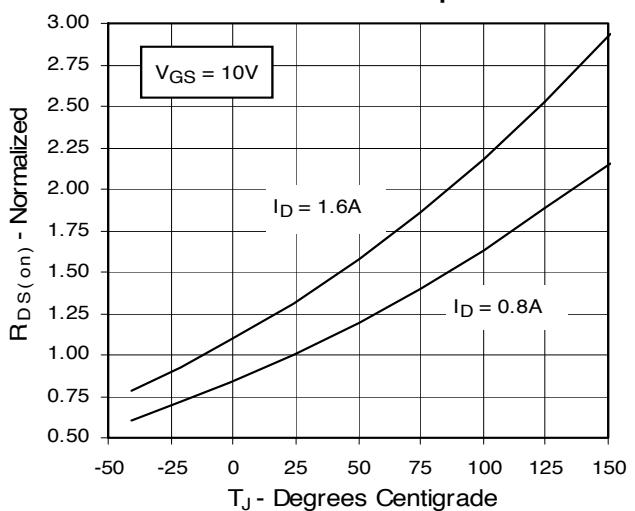


Fig. 5. $R_{DS(on)}$ Normalized to 0.5 I_{D25} Value vs. I_D

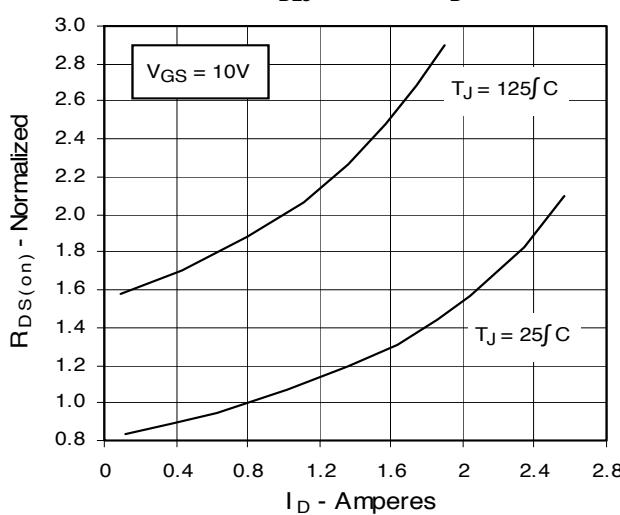


Fig. 6. Drain Current vs. Case Temperature

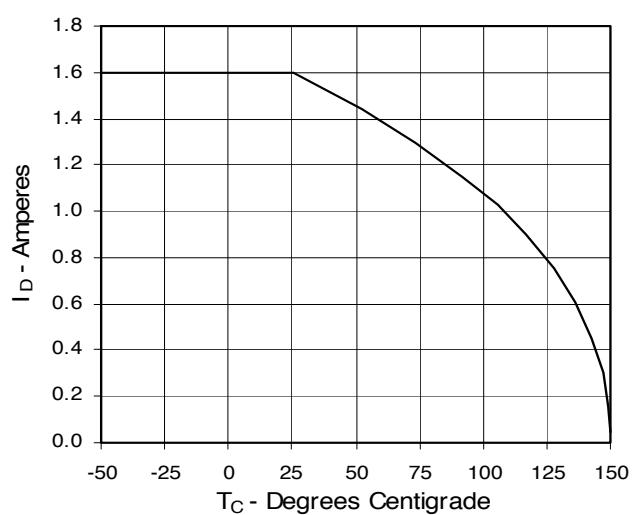
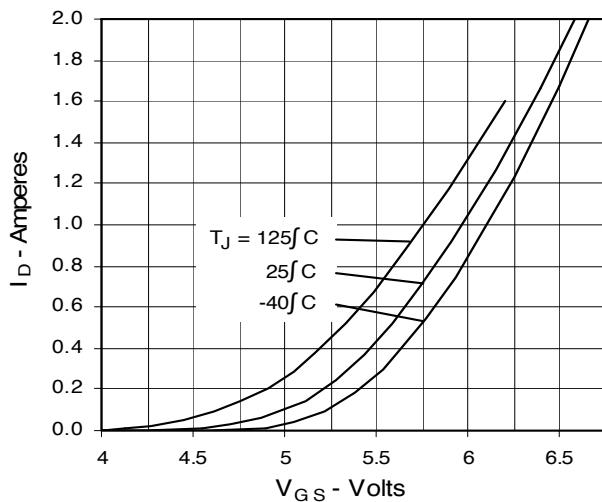
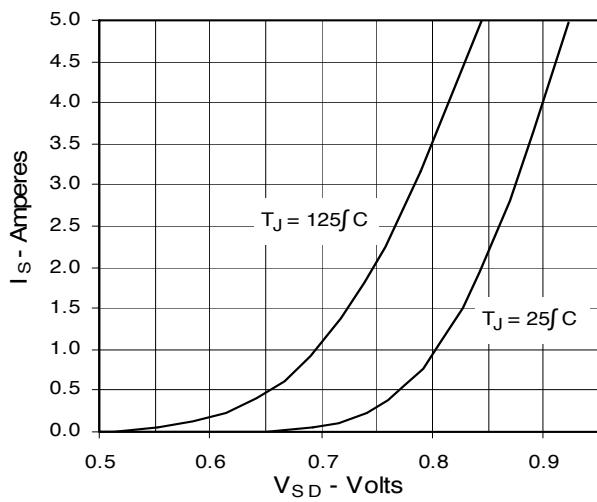
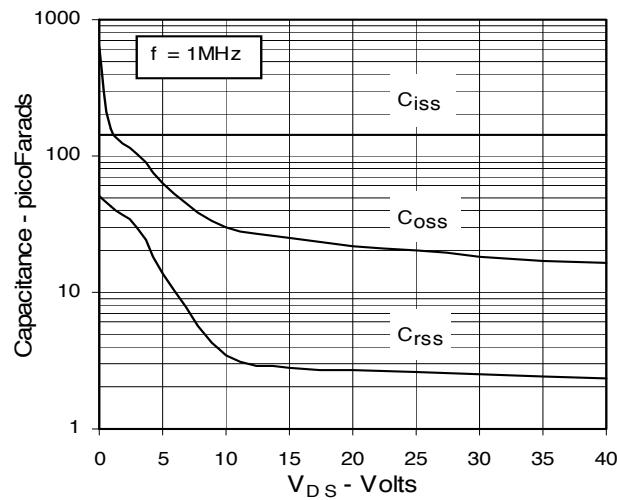
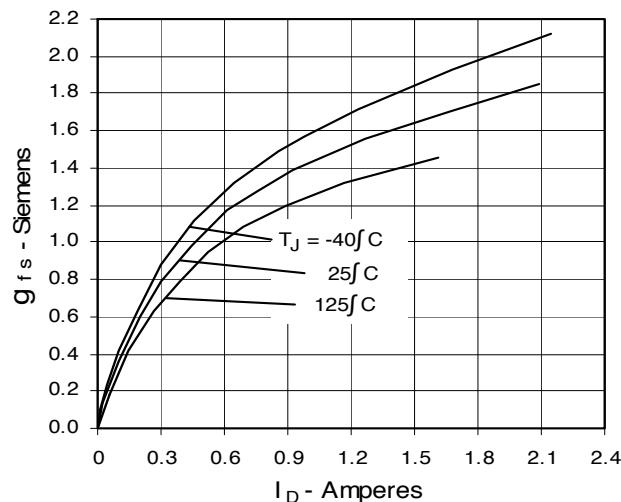
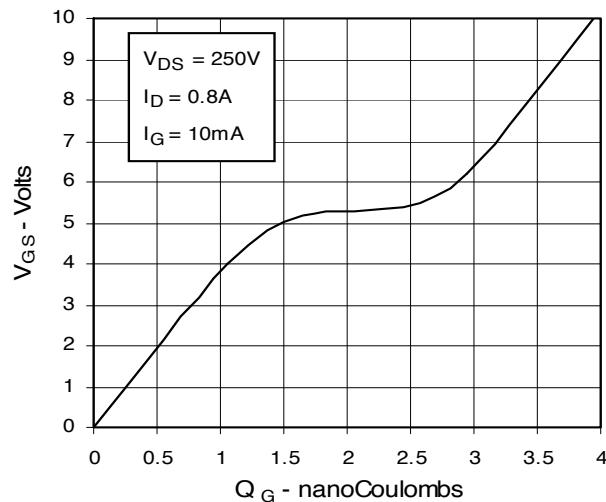
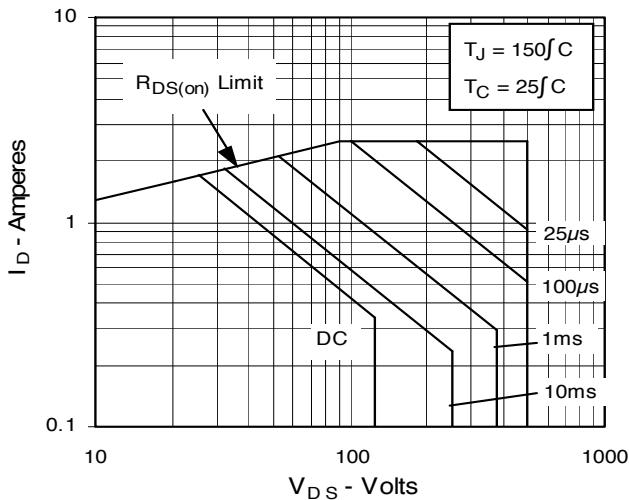


Fig. 7. Input Admittance

Fig. 9. Source Current vs. Source-To-Drain Voltage

Fig. 11. Capacitance

Fig. 8. Transconductance

Fig. 10. Gate Charge

Fig. 12. Forward-Bias Safe Operating Area


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