

Description

- High speed switching application.

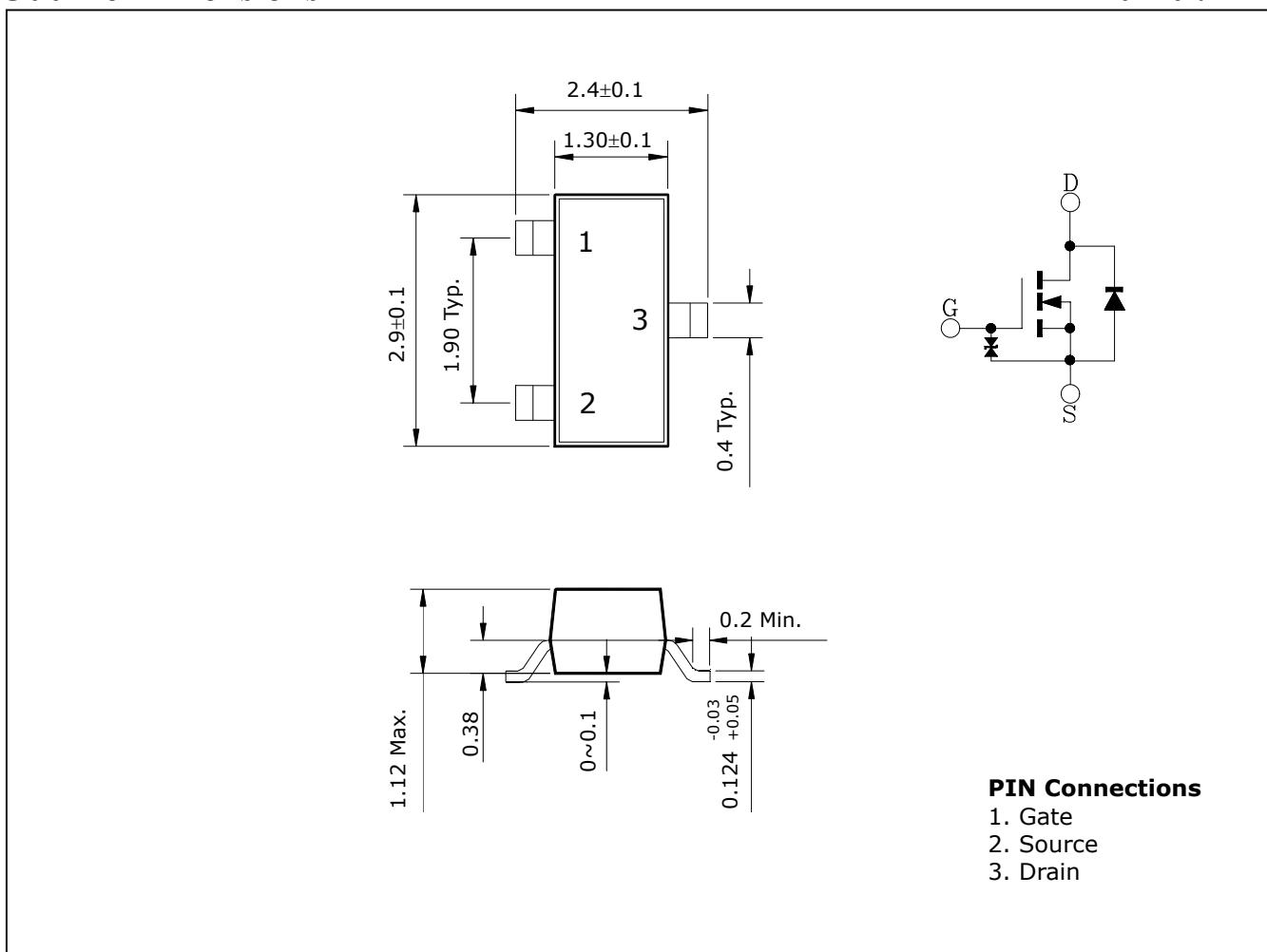
Features

- High density cell design for low $R_{DS(ON)}$.
- Voltage controlled small signal switch
- Include Zener protection for ESD ruggedness.

Ordering Information

Type NO.	Marking	Package Code
STK0602	K602	SOT-23

Outline Dimensions

unit : mm


Absolute maximum ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Drain-Source voltage	V _{DSS}	60	V
Gate-Source voltage	V _{GS}	±8	V
Maximum Drain current	I _D	200	mA
Pulsed Drain Current	I _{DP}	800	mA
Drain Power dissipation	P _D	200	mW
Operating Junction and Storage temperature range	T _J , T _{stg}	-55~150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Drain-Source breakdown voltage	BV _{DSS}	I _D =10μA, V _{GS} =0	60	-	-	V
Gate-Threshold voltage	V _{GS(th)}	I _D =1μA, V _{DS} =5V	0.8	-	1.8	V
Zero Gate voltage drain current	I _{DSS}	V _{DS} =60V, V _{GS} =0	-	-	1.0	μA
Gate-body leakage	I _{GSS}	V _{DS} =0V, V _{GS} =±6V	-	-	±1.0	μA
Drain-Source on-resistance	R _{DS(ON)}	V _{GS} =5V, I _D =10mA	-	2.5	6.0	Ω
		V _{GS} =10V, I _D =10mA	-	2.0	4.0	
Forward transconductance	g _{fs}	V _{DS} =5V, I _D =20mA	20	65	-	mS
Input capacitance	C _{iss}	V _{DS} =5V, V _{GS} =0, f=1MHz	-	26	-	pF
Output capacitance	C _{oss}		-	20	-	
Reverse Transfer capacitance	C _{rss}		-	10	-	
Turn-on delay time	t _{d(on)}	V _{DD} =5V, I _D =10mA V _{GS} =5V R _L =500Ω		150		ns
Rise time	t _r			240		
Turn-off delay time	t _{d(off)}		-	200	-	
Fall time	t _f		-	300	-	

Electrical Characteristic Curves

Fig. 1 I_D - V_{DS}

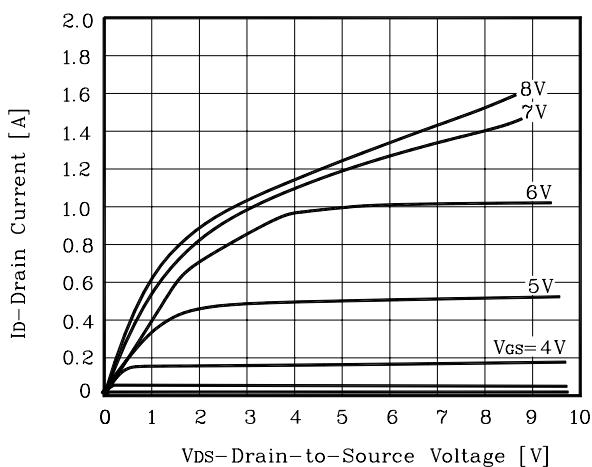


Fig. 2 I_D - V_{DS}

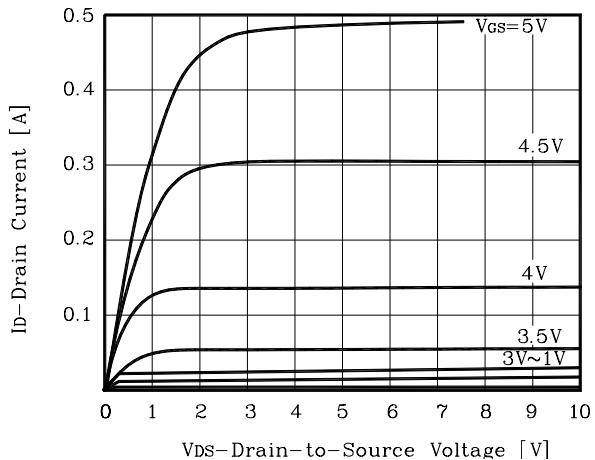


Fig. 3 I_D - V_{GS}

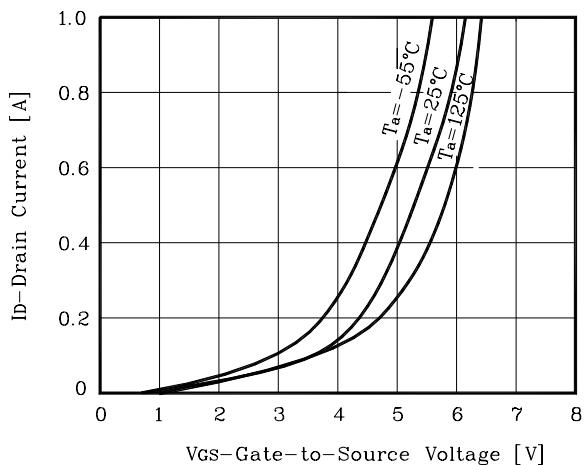


Fig. 4 $r_{DS(on)}$ - I_D

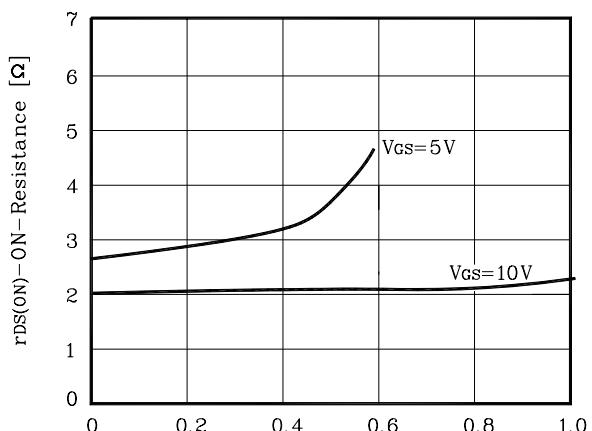


Fig. 5 C - V_{DS}

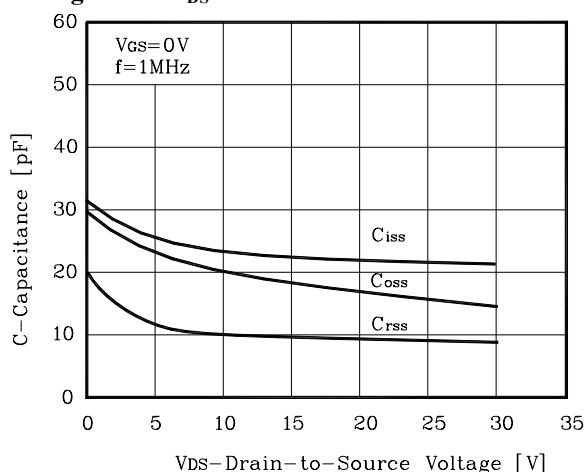
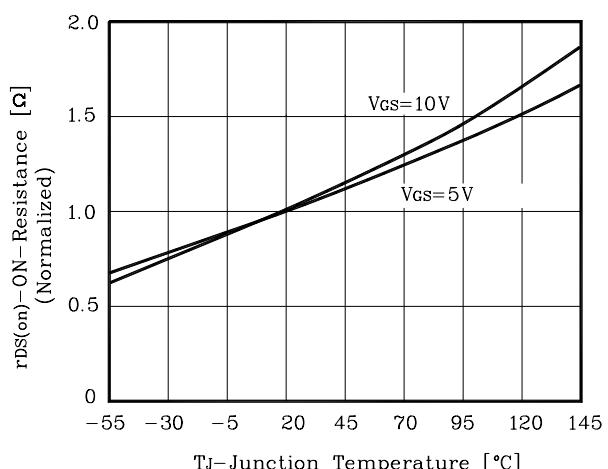


Fig. 6 $r_{DS(on)}$ - T_J



Electrical Characteristic Curves

Fig. 7 $r_{DS(on)}$ - V_{GS}

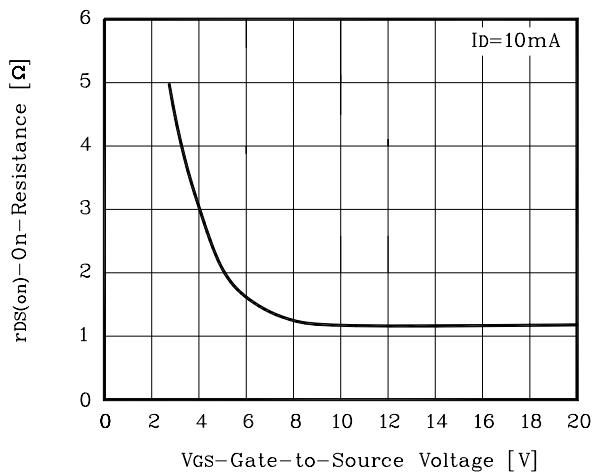


Fig. 8 I_S - V_{SD}

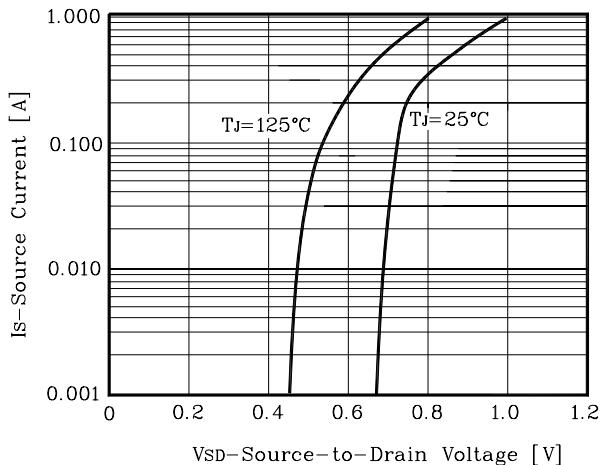
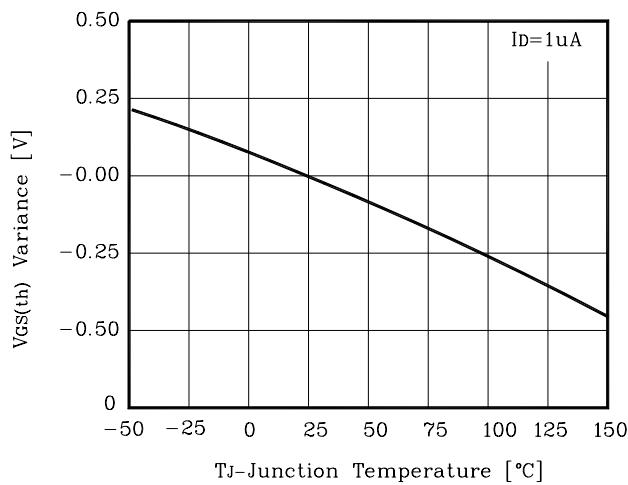


Fig. 9 $V_{GS(th)}$ - T_J



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