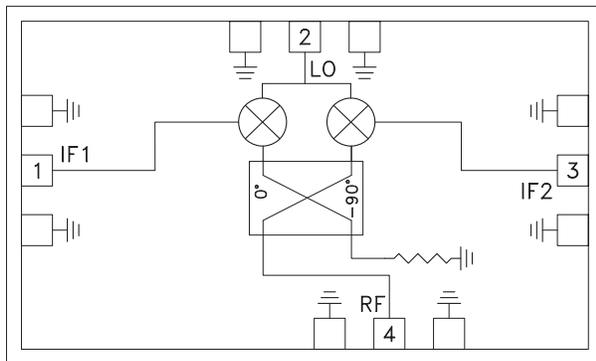


### Typical Applications

This HMC-MDB207 is ideal for:

- Point-to-Point Radios
- Military Radar, ECM & EW
- Test & Measurement Equipment
- SATCOM
- Sensors

### Functional Diagram



### Features

- Wide IF Bandwidth: DC - 3 GHz
- High Image Rejection: 30 dB
- High LO to RF Isolation: 30 dB
- Passive: No DC Bias Required
- Die Size: 2.0 x 1.2 x 0.1 mm

### General Description

The HMC-MDB207 is a monolithic I/Q Mixer which can be used as either an image reject mixer (IRM) or a single sideband upconverter. This passive MMIC mixer utilizes two double balanced mixer cells and a 90 degree hybrid. It is fabricated with GaAs Heterojunction Bipolar Transistor (HBT) Shottky diode technology. For downconversion applications, an external quadrature hybrid can be used to select the desired sideband while rejecting image signals. All bond pads and the die backside are Ti/Au metallized and the Shottky devices are fully passivated for reliable operation. The HMC-MDB207 I/Q MMIC Mixer is compatible with conventional die attach methods, as well as thermocompression and thermosonic wire bonding, making it ideal for MCM and hybrid microcircuit applications. This compact MMIC is a much smaller and more consistent alternative to hybrid style Image Reject Mixers and Single Sideband Upconverter assemblies. All data shown herein is measured with the chip in a 50 Ohm environment and contacted with RF probes.

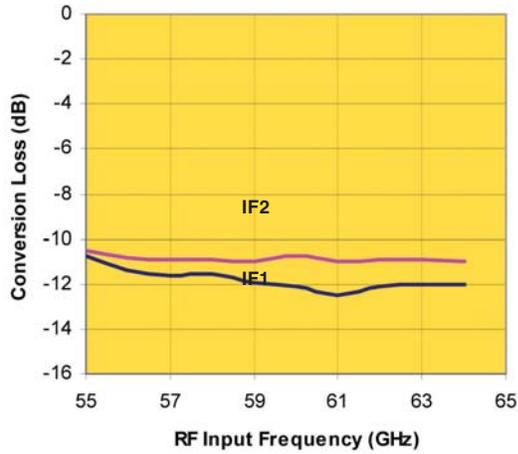
### Electrical Specifications,<sup>[1]</sup> $T_A = 25\text{ }^\circ\text{C}$ , $IF = 2\text{ GHz}$ , $LO = +16\text{ dBm}$

Parameter	Min.	Typ.	Max.	Units
Frequency Range, RF & LO		55 - 64		GHz
Frequency Range, IF		DC - 3		GHz
Conversion Loss <sup>[2]</sup>		9	11	dB
1 dB Compression (Input)		7		dBm
Image Rejection		30		dB
LO to RF Isolation		30		dB
LO to IF Isolation		30		dB
IP3 (Input)		16		dBm
Amplitude Balance		0.7	1.7	dB
Phase Balance		1		Deg

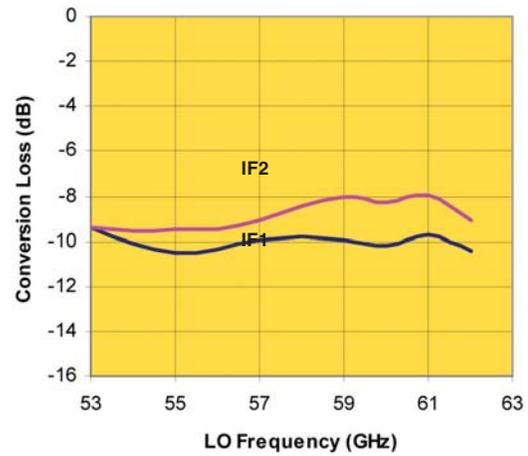
[1] Unless otherwise indicated, all measurements are from probed die

[2] Conversion loss assumed that IF1 & IF2 are combined with an ideal off-chip 90° hybrid

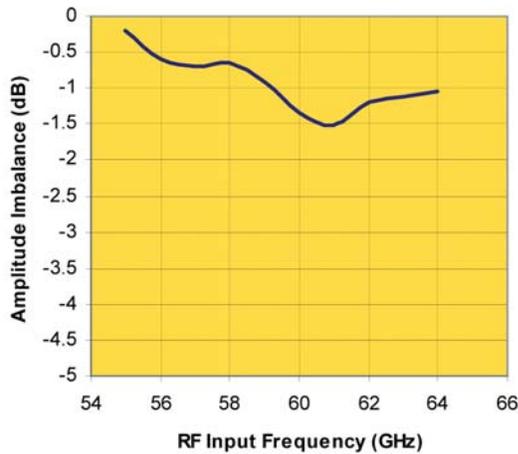
**Downconverter Conversion Loss [1]**



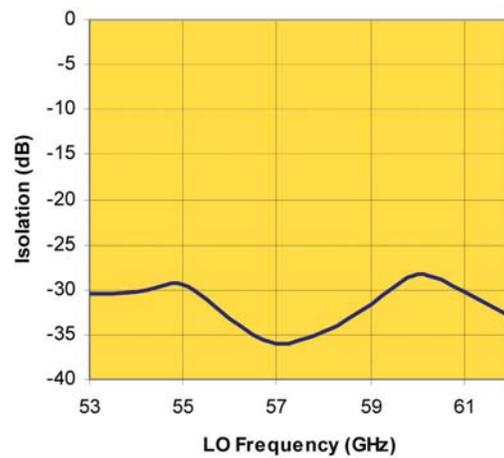
**Upconverter Conversion Loss [2]**



**Downconverter Amplitude Imbalance [1]**



**LO & RF Port Isolation [2]**



[1] IF = RF - LO. RF and LO Tracking, IF = 2 GHz

[2] RF = LO + IF. RF and LO Tracking, IF = 2 GHz

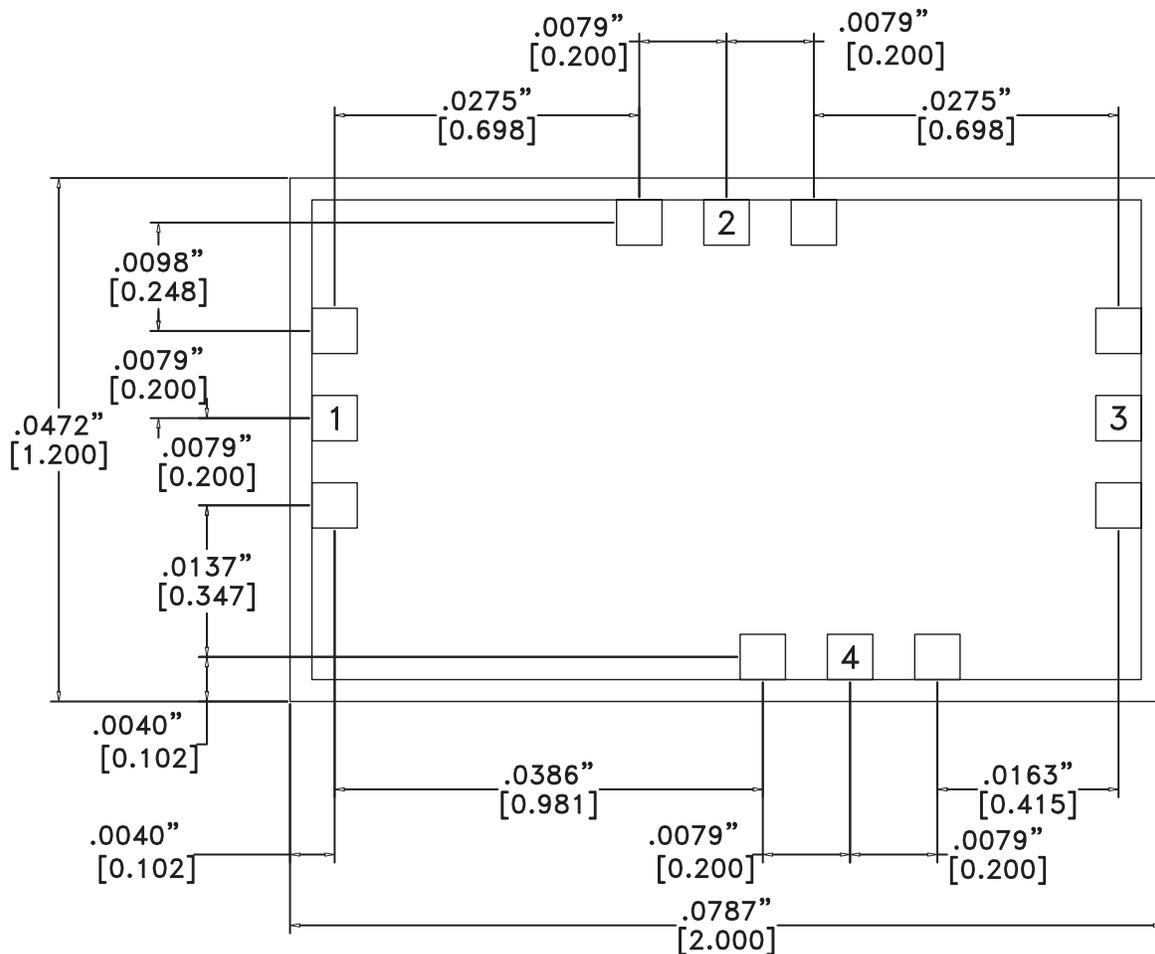
**Absolute Maximum Ratings**

LO Drive	20 dBm
Storage Temperature	-65 °C to 150 °C
Operating Temperature	-55 °C to 85 °C



**ELECTROSTATIC SENSITIVE DEVICE  
OBSERVE HANDLING PRECAUTIONS**

**Outline Drawing**



**Die Packaging Information** [1]

Standard	Alternate
WP-21	[2]

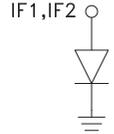
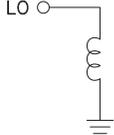
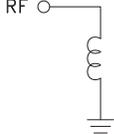
[1] Refer to the "Packaging Information" section for die packaging dimensions.

[2] For alternate packaging information contact Hittite Microwave Corporation.

**NOTES:**

1. ALL DIMENSIONS ARE IN INCHES [MM].
2. TYPICAL BOND PAD IS .004" SQUARE.
3. BACKSIDE METALLIZATION: GOLD.
4. BACKSIDE METAL IS GROUND.
5. BOND PAD METALLIZATION: GOLD.
6. CONNECTION NOT REQUIRED FOR UNLABELED BOND PADS.
7. OVERALL DIE SIZE ±.002"

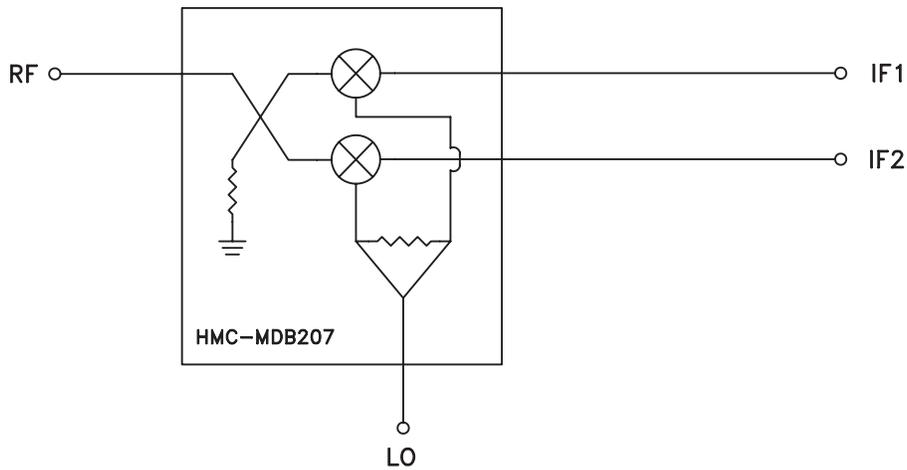
**Pad Descriptions**

Pad Number	Function	Pin Description	Interface Schematic
1, 3	IF1, IF2	This pad is DC coupled.	
2	LO	This pad is DC coupled and matched to 50 Ohms.	
4	RF	This pad is DC coupled and matched to 50 Ohms.	

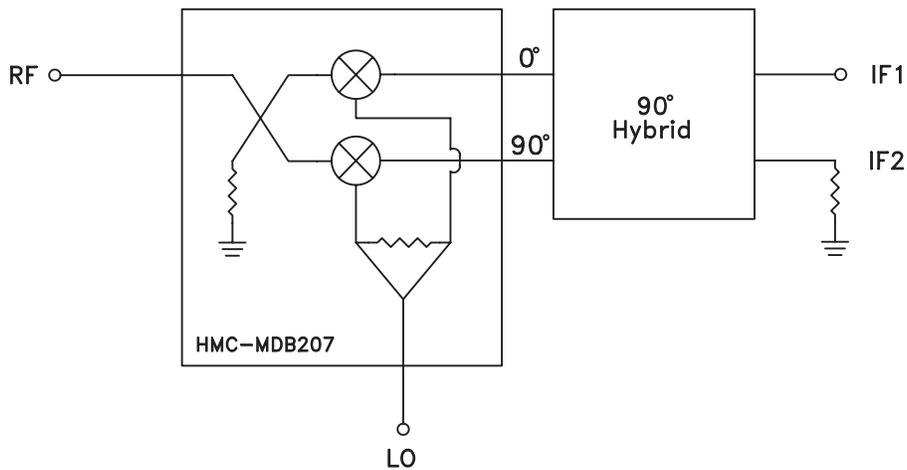
**Application Circuits**

Application circuit 1 shows the mixer equivalent circuit. Application Circuit 2 depicts the mixer with a 90° hybrid used to achieve signal image rejection. All RF parameters are specified with an ideal 90° hybrid on IF output ports. Conversion loss is measured (on wafer) at IF1 and/or IF2 (Application Circuit 1) with the second IF port terminated into 50 ohms. Three dB is then added to compensate for an ideal hybrid. The IP3 is stated as an input IP3 number and is obtained via a two-tone measurement.

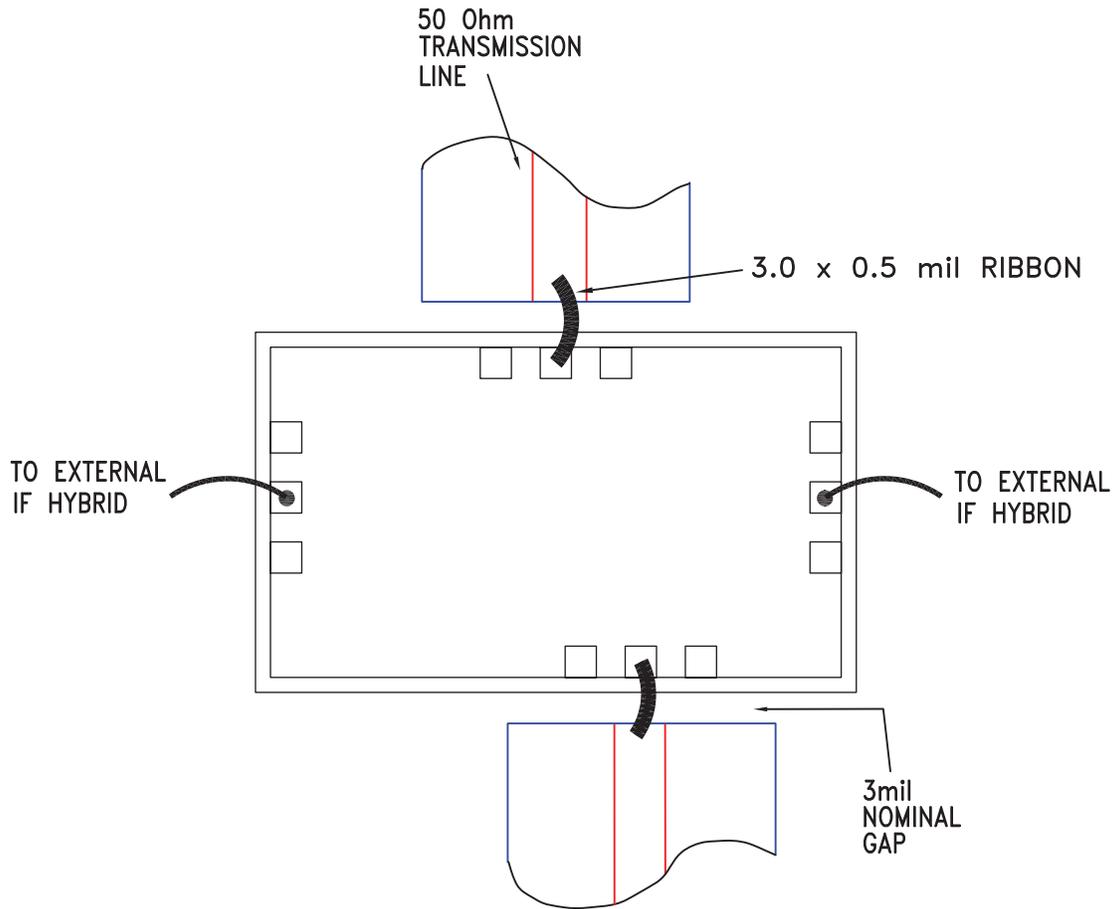
**Application Circuit 1**



**Application Circuit 2**



**Assembly Diagram**

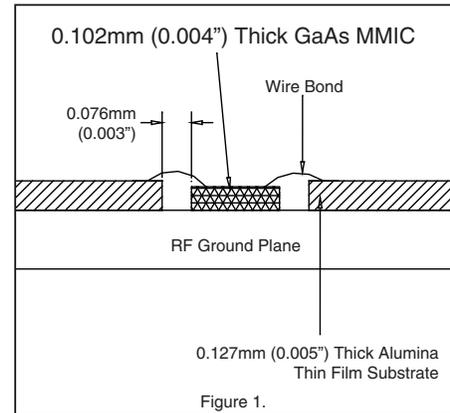


### Mounting & Bonding Techniques for Millimeterwave GaAs MMICs

The die should be attached directly to the ground plane eutectically or with conductive epoxy (see HMC general Handling, Mounting, Bonding Note).

50 Ohm Microstrip transmission lines on 0.127mm (5 mil) thick alumina thin film substrates are recommended for bringing RF to and from the chip (Figure 1). If 0.254mm (10 mil) thick alumina thin film substrates must be used, the die should be raised 0.150mm (6 mils) so that the surface of the die is coplanar with the surface of the substrate. One way to accomplish this is to attach the 0.102mm (4 mil) thick die to a 0.150mm (6 mil) thick molybdenum heat spreader (moly-tab) which is then attached to the ground plane (Figure 2).

Microstrip substrates should be placed as close to the die as possible in order to minimize bond wire length. Typical die-to-substrate spacing is 0.076mm to 0.152 mm (3 to 6 mils).



### Handling Precautions

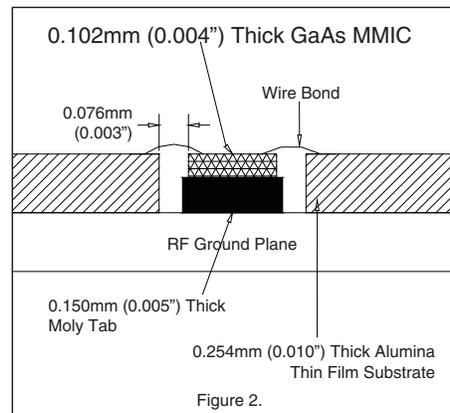
Follow these precautions to avoid permanent damage.

**Storage:** All bare die are placed in either Waffle or Gel based ESD protective containers, and then sealed in an ESD protective bag for shipment. Once the sealed ESD protective bag has been opened, all die should be stored in a dry nitrogen environment.

**Cleanliness:** Handle the chips in a clean environment. DO NOT attempt to clean the chip using liquid cleaning systems.

**Static Sensitivity:** Follow ESD precautions to protect against ESD strikes.

**Transients:** Suppress instrument and bias supply transients while bias is applied. Use shielded signal and bias cables to minimize inductive pick-up.



**General Handling:** Handle the chip along the edges with a vacuum collet or with a sharp pair of bent tweezers. The surface of the chip has fragile air bridges and should not be touched with vacuum collet, tweezers, or fingers.

### Mounting

The chip is back-metallized and can be die mounted with AuSn eutectic preforms or with electrically conductive epoxy. The mounting surface should be clean and flat.

**Eutectic Die Attach:** A 80/20 gold tin preform is recommended with a work surface temperature of 255 °C and a tool temperature of 265 °C. When hot 90/10 nitrogen/hydrogen gas is applied, tool tip temperature should be 290 °C. DO NOT expose the chip to a temperature greater than 320 °C for more than 20 seconds. No more than 3 seconds of scrubbing should be required for attachment.

**Epoxy Die Attach:** Apply a minimum amount of epoxy to the mounting surface so that a thin epoxy fillet is observed around the perimeter of the chip once it is placed into position. Cure epoxy per the manufacturer's schedule.

### Wire Bonding

RF bonds made with 0.003" x 0.0005" ribbon are recommended. These bonds should be thermosonically bonded with a force of 40-60 grams. DC bonds of 0.001" (0.025 mm) diameter, thermosonically bonded, are recommended. Ball bonds should be made with a force of 40-50 grams and wedge bonds at 18-22 grams. All bonds should be made with a nominal stage temperature of 150 °C. A minimum amount of ultrasonic energy should be applied to achieve reliable bonds. All bonds should be as short as possible, less than 12 mils (0.31 mm).

**Notes:**