TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

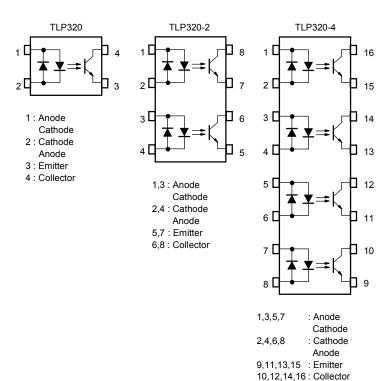
TLP320, TLP320-2, TLP320-4

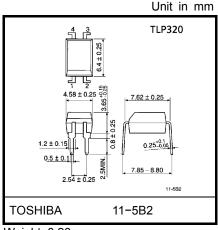
Telecommunication Office Machine Telephone Use Equipment

The TOSHIBA TLP320, -2 and -4 consists of a photo–transistor optically coupled to a gallium arsenide infrared emitting diode. The TLP320–2 offers two isolated channels in an eight lead plastic DIP package, while the TLP320–4 provides four isolated channels in a sixteen plastic DIP package. This is suitable for application of AC input current up to $150 \mathrm{mA}$.

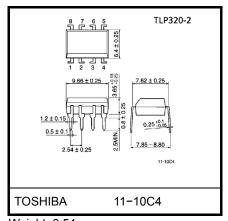
- IF maximum rating: ±150mA
- Collector-emitter voltage: 55V (min.)
- Current transfer ratio: 25% (min.) (I_F = 20mA)
- Isolation voltage: 5000V_{rms} (min.)
- UL recognized: file No. E67349
- BSI approved: BS EN60065:2002, certificate no.7426
 BS EN60950-1:2002, certificate no.7427

Pin Configurations (top view)

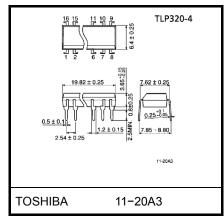




Weight: 0.26g



Weight: 0.54g



Weight: 1.1g



Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Rat		
		Symbol	TLP320	TLP320-2 TLP320-4	Unit
Forward current		lF	±150		mA
Ω	Forward current derating	ΔI _F /°C	–1.5 (Ta ≥ 25°C)		mA / °C
LED	Pulse forward current	IFP	±1 (100µs pulse, 100pps)		Α
	Junction temperature	Tj	12	25	°C
	Collector-emitter voltage	V _{CEO}	5	5	V
	Emitter-collector voltage	V _{ECO}	7	7	V
'n	Collector current	Ic	8	0	mA
Detector	Collector power dissipation (1 circuit)	PC	150	100	mW
	Collector power dissipation derating (1 circuit, Ta ≥ 25°C)	ΔP _C / °C	-1.5	-1.0	mW / °C
	Junction temperature	Tj	125		°C
Sto	rage temperature range	T _{stg}	−55~125		°C
Оре	erating temperature range	T _{opr}	−55~100		°C
Lead soldering temperature		T _{sol}	260 (10s)		°C
Total package power dissipation		P _T	250	200	mW
Total package power dissipation derating (Ta≥25°C)		ΔP _T / °C	-2.5	2.0	mW / °C
Isol	ation voltage (Note 1)	BVS	5000 (AC, 1min., R.H. ≤ 60%)		V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) Device consider a two terminal: LED side pins shorted together and detector side pins shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V _{CC}	_	5	24	V
Forward current	lF	_	20	120	mA
Collector current	IC	_	1	10	mA
Operating temperature	T _{opr}	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

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Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
LED	Forward voltage	V _F	I _F = ±100 mA	_	1.4	1.7	V
	Forward current	l _F	V _F = ±0.7 V	_	2.5	20	μΑ
	Capacitance	C _T	V = 0, f = 1 MHz	-	60	_	pF
Detector	Collector–emitter breakdown voltage	V _(BR) CEO	I _C = 0.5 mA	55	_	-	V
	Emitter-collector breakdown voltage	V _(BR) ECO	I _E = 0.1 mA	7	_	-	V
	Collector dark current	lana	V _{CE} = 24 V	_	10	100	nA
	Collector dark current	ICEO	V _{CE} = 24 V, Ta = 85°C	_	2	50	μA
	Capacitance collector to emitter	C _{CE}	V = 0, f = 1 MHz	_	10	_	pF

Coupled Electrical Characteristics (Ta = 25°C)

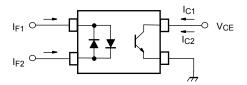
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
	I _C / I _F	I _F = ±20 mA, V _{CE} = 1 V	25	_	-	
Current transfer ratio	IC / IF (high)	I _F = ±100 mA, V _{CE} = 1 V	20	_	80	%
Collector–emitter saturation voltage	V _{CE (sat)}	I _C = 2.4 mA, I _F = ±20 mA	_	_	0.4	V
		I _C = 2.4 mA, I _F = ±100 mA	_	_	0.4	
Off-state collector current	I _{C(off)}	V _F = ± 0.7V, V _{CE} = 24 V	_	1	10	μΑ
CTR symmetry (Note)	I _C (ratio)	$I_{C} (I_{F} = -20 \text{mA}) / I_{C} (I_{F} = +20 \text{mA})$ (Note)	0.5	1	2	_

Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Capacitance input to output	Cs	V _S = 0, f = 1 MHz	_	8.0	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≤ 60%	5×10 ¹⁰	10 ¹⁴	_	Ω
	BV _S AC, 1	AC, 1 minute	5000	_	_	V
Isolation voltage		AC, 1 second, in oil	_	10000	_	V _{rms}
		DC, 1 minute, in oil	_	10000	_	V _{dc}

(Note)

$$I_{C(ratio)} = \frac{I_{C2}(I_F = I_{F2}, V_{CE} = 1V)}{I_{C1}(I_F = I_{F1}, V_{CE} = 1V)}$$

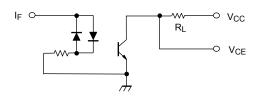


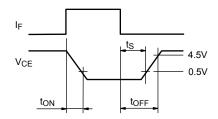


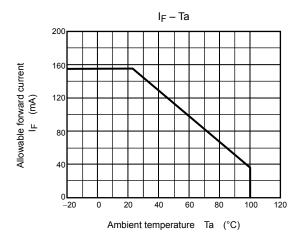
Switching Characteristics (Ta = 25°C)

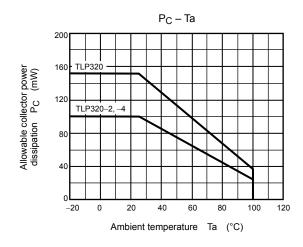
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Rise time	t _r		_	2	_	
Fall time	t _f	V _{CC} = 10 V, I _C = 2 mA	_	3	_	110
Turn-on time	t _{on}	$R_L = 100\Omega$	_	3	_	μs
Turn-off time	t _{off}		_	3	_	
Turn-on time	ton	D. = 1.0 kO (Fig.1)	_	2	_	
Storage time	ts	$R_L = 1.9 \text{ k}\Omega$ (Fig.1) $V_{CC} = 5 \text{ V}, I_F = \pm 16 \text{ mA}$	_	15	_	μs
Turn-off time	t _{OFF}	VCC - 3 V, IF - ± 10 IIIA	_	25	_	

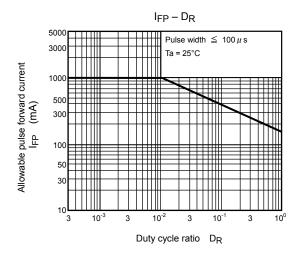
Fig. 1 Switching time test circuit

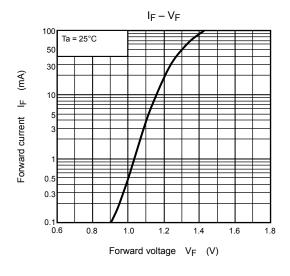


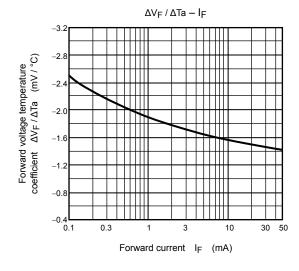


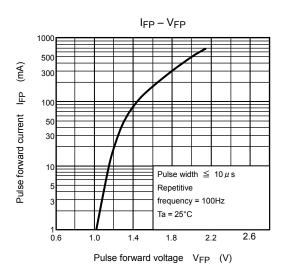




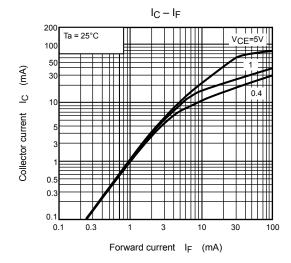


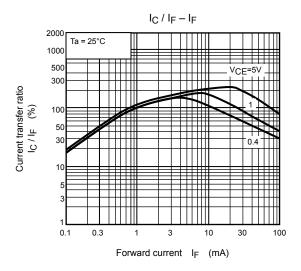


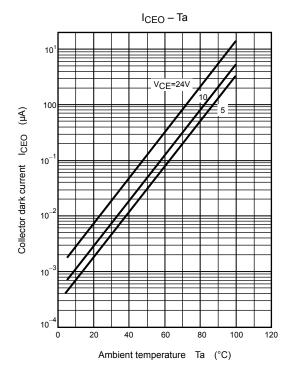




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