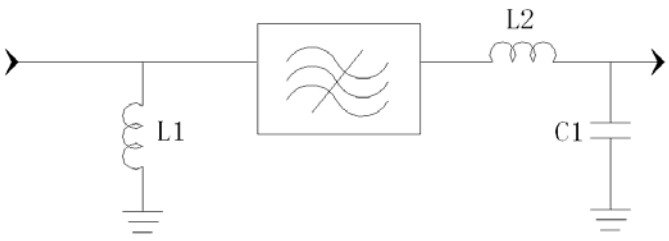
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	114.92	115	115.08
Insertion Loss	dB		24	25.5
3 dB Bandwidth	MHz	4.92	4.93	
30 dB Bandwidth	MHz		5.72	5.82
40 dB Bandwidth	MHz		5.93	6.02
50 dB Bandwidth	MHz		6.22	6.32
Passband Variation	dB		0.7	1
Absolute Delay	usec		2.96	4
Group Delay Variation(f0±1.96MHz)	nsec		150	300
Ultimate Rejection(outside of f0±4MHz)	dB	45	46	
Subatrate Material			112LT	
Ambient Temperature	°C		25	
Package Size			DIP2712 (27.0x12.8x4.7mm^3)	

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



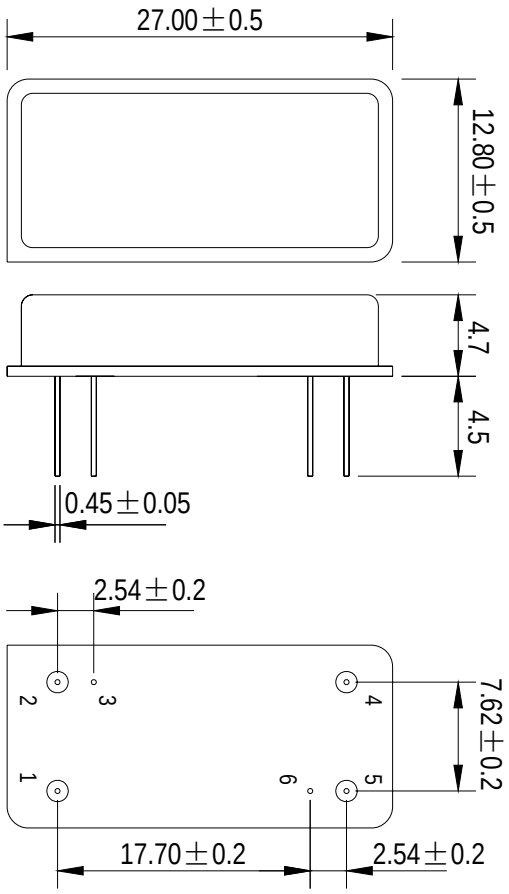
L1=56nH L2=22nH

C1=39pF

Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

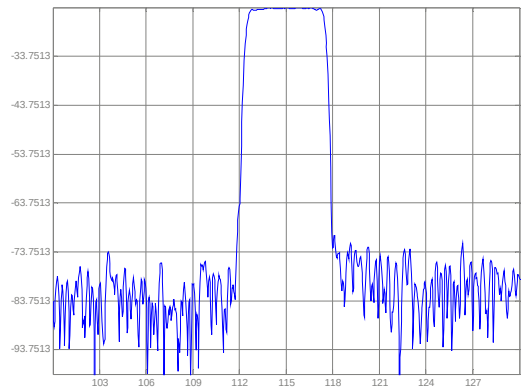
Package Dimension



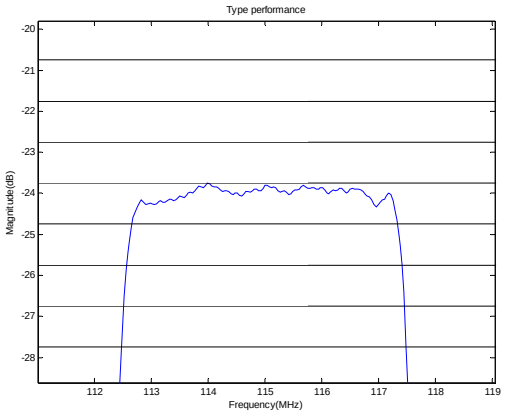
Pin 1:input
Pin 5:output
Others:Grounded

Typical Performance

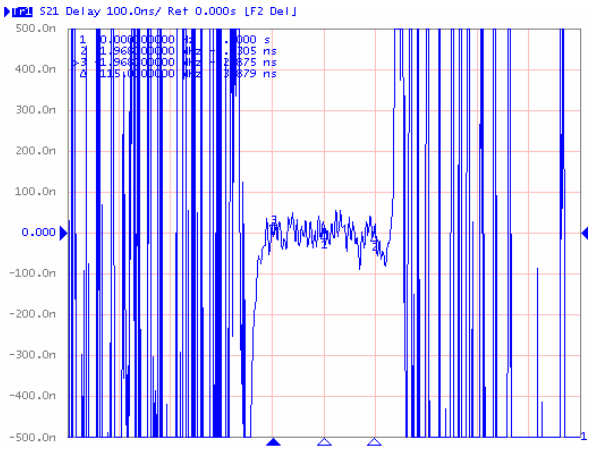
Frequency Respond



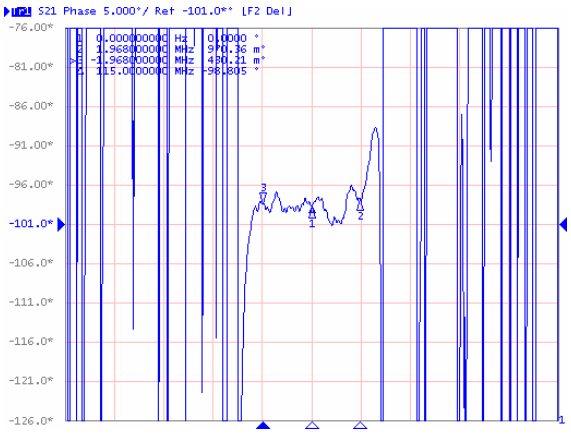
Passband Respond



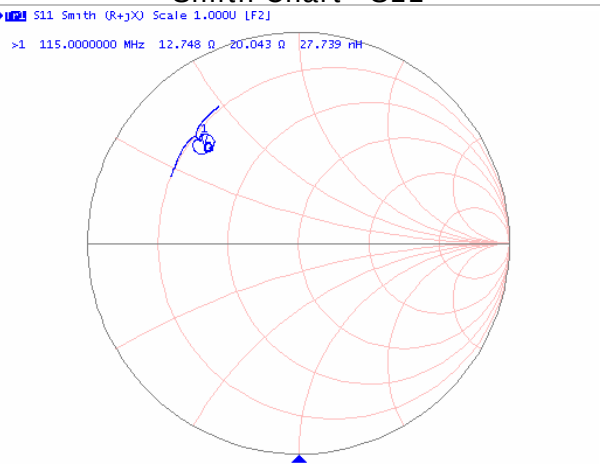
Group Delay Variation(f0±1.968MHz)



Phase Linearity(f0±1.968MHz)



Simth Chart S11



Simth Chart S22