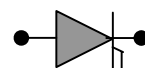


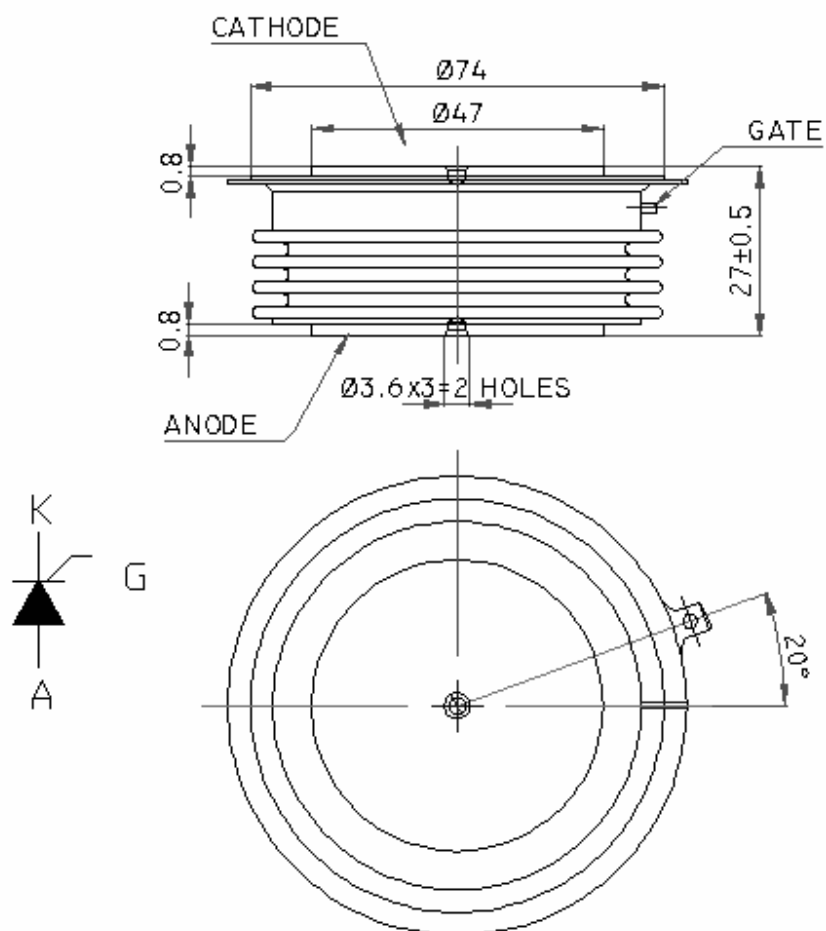
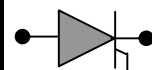
PHASE CONTROL THYRISTOR H1450CHXX



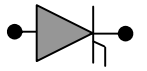
Symbol	Characteristics	Conditions	T_J ($^{\circ}\text{C}$)	Value	Unit
BLOCKING PARAMETERS					
V_{RRM}	Repetitive peak reverse voltage		125	200-2800	V
V_{DRM}	Repetitive peak off-stage voltage		125	200-2800	V
I_{RRM}	Repetitive peak reverse current	$V = V_{RRM}$	125	70	mA
I_{DRM}	Repetitive peak off-state current	$V = V_{RRM}$	125	70	mA
CONDUCTING PARAMETERS					
$I_{F(AV)}$	Average on-state current	180 sine, 50Hz, $T_C = 82^{\circ}\text{C}$		1450	A
I_{RMS}	RMS on-state current			2280	A
I_{TSM}	Surge on-state current	Sine wave, 10mS without reverse voltage	125	30	kA
I^2t	I^2t			4500	kA^2S
V_T	Peak on-state voltage drop	On-state current = 3kA	125	2	V
V_0	Threshold voltage		125	0.97	V
R_0	On-state slope resistance		125	0.27	$\text{m}\Omega$
TRIGGERING PARAMETERS					
I_{GT}	Gate trigger current	$V_D = 5\text{V}$	25	300	mA
V_{GT}	Gate trigger voltage		25	3.50	V
I_L	Latching Current	$V_D = 5\text{V}$	25	1000	mA
P_{G-PEAK}	Maximum Peak Gate Power	Pulse width 100 μSec		150	W
di/dt	Repetitive rate of rise of current			200	$\text{A}/\mu\text{Sec}$
V_{FGM}	Maximum forward gate voltage			12	V
I_{FGM}	Maximum forward gate current			50	A
THERMAL & MECHANICAL PARAMETERS					
$R_{TH(J-C)}$	Thermal impedance, 180 conduction, Sine	Junction to case		0.015	$^{\circ}\text{C}/\text{W}$
$R_{TH(C-HK)}$	Thermal impedance	Case to heatsink		0.005	$^{\circ}\text{C}/\text{W}$
T_J	Maximum Permissible junction temperature			125	$^{\circ}\text{C}$
T_{STG}	Storage temperature range			-40 - 125	$^{\circ}\text{C}$
F	Mounting Torque			23	KN
W	Weight			550	gms



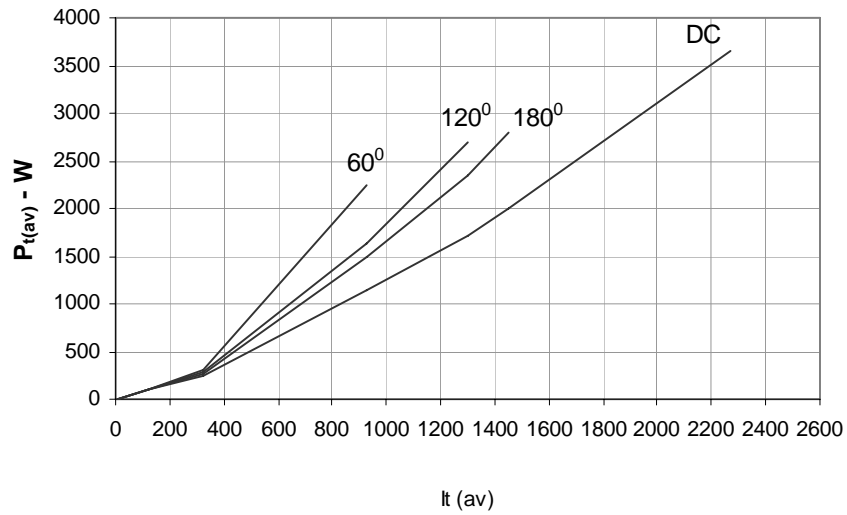
PHASE CONTROL THYRISTOR H1450CHXX



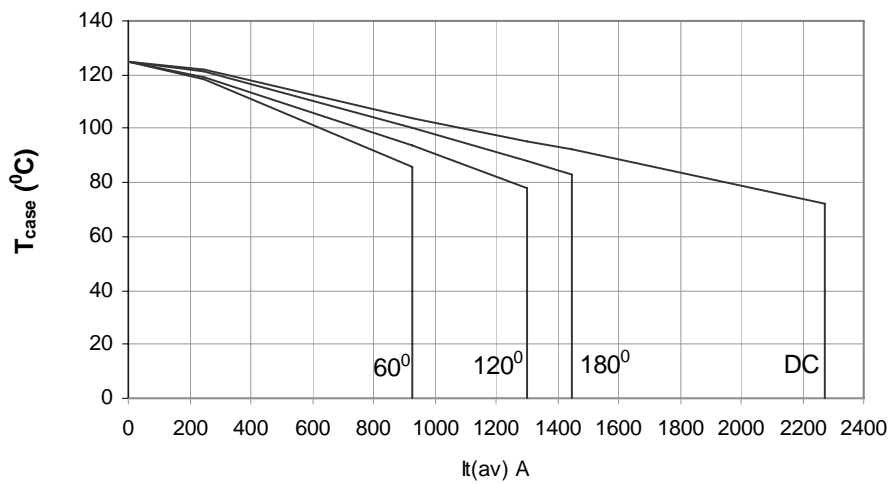
All dimensions in mm

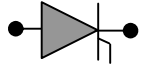


On State Power Loss

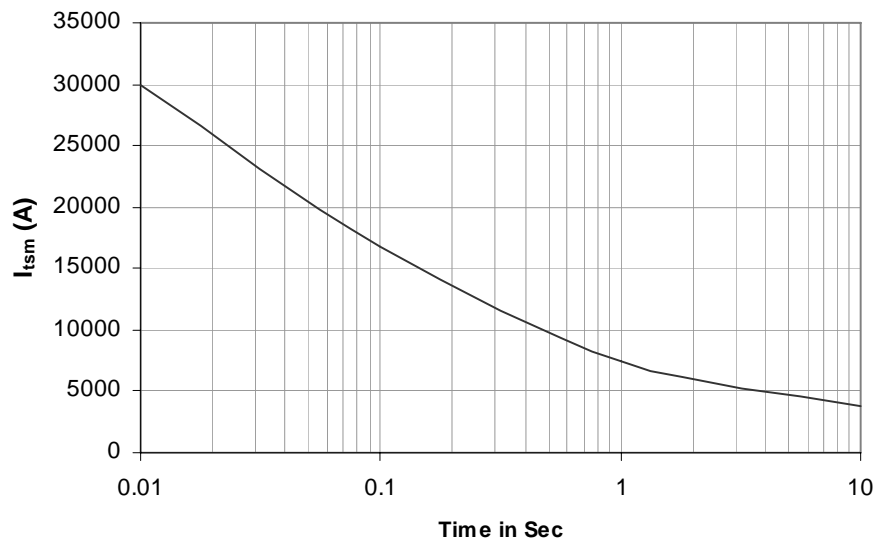


Maximum Permissible Case Temp

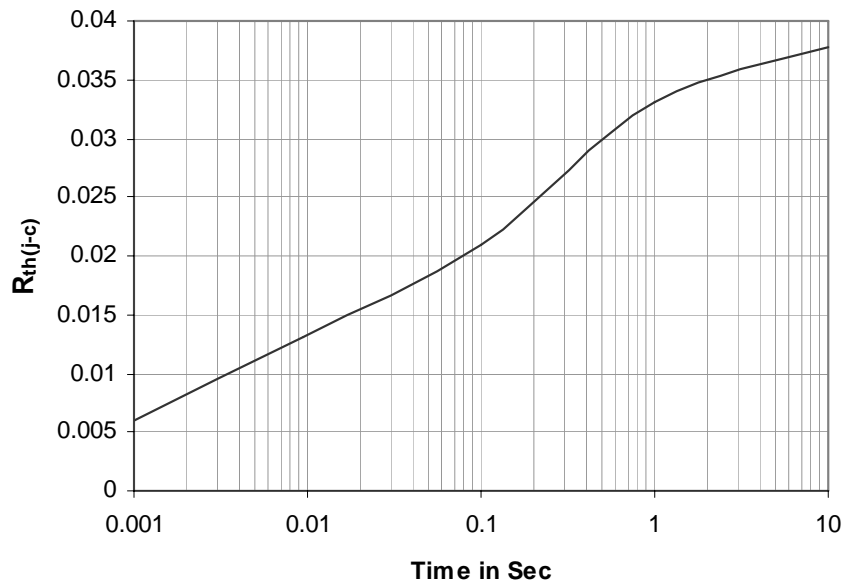


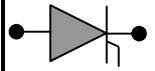


Max non repetitive Surge Current

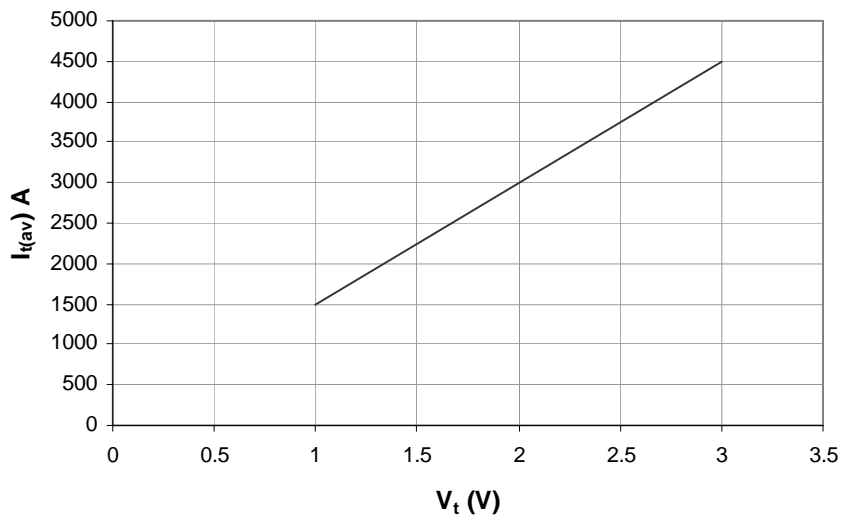


Transient Thermal Impedance Junction to Case

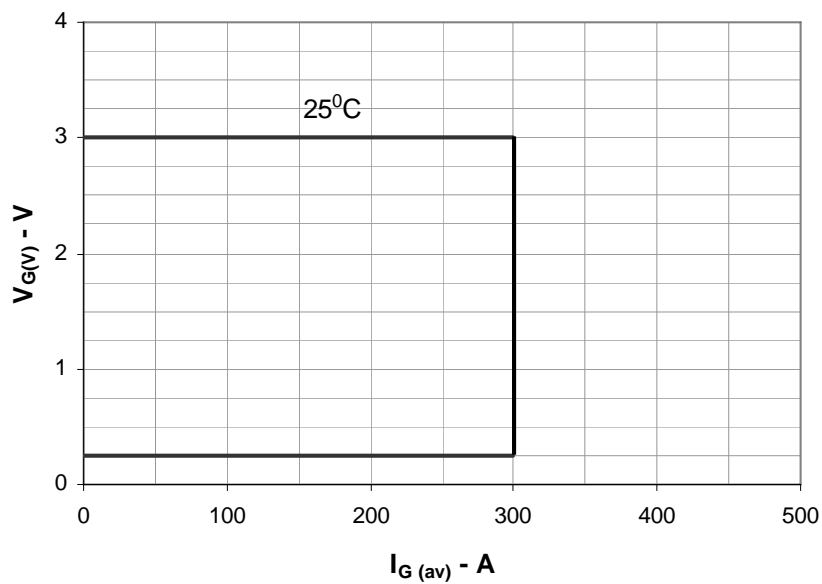




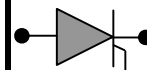
On State Characteristics



Gate Trigger Characteristics



PHASE CONTROL THYRISTOR H1450CHXX



Ordering Information: -

H	1450	CH	XX
Hirect make Thyristor	$I_{F(AV)} = 1450A$	Capsule Thyristor	$V_{RRM} = XX * 100$ e.g. 28 * 100 = 2800V

Hind Rectifiers Ltd reserves the right to change the specifications without notice.

This datasheet specifies technical information for semiconductor devices but promises no characteristics. No warranty or guarantee expressed or implied is made regarding delivery, performance or suitability.

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