

To all our customers

Regarding the change of names mentioned in the document, such as Mitsubishi Electric and Mitsubishi XX, to Renesas Technology Corp.

The semiconductor operations of Hitachi and Mitsubishi Electric were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Mitsubishi Electric, Mitsubishi Electric Corporation, Mitsubishi Semiconductors, and other Mitsubishi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Note : Mitsubishi Electric will continue the business operations of high frequency & optical devices and power devices.

Renesas Technology Corp.
Customer Support Dept.
April 1, 2003

M51403FP/GP

PAL VIDEO CHROMA SIGNAL PROCESSOR

DESCRIPTION

The M51403FP/GP is a semiconductor integrated circuit for processing video signals in a PAL system color liquid crystal (LC) television set.

This IC incorporates ACC, APC, VCXO, RGB matrix amplifier, color signal demodulator, picture quality control, killer circuit and ID circuit.

FEATURES

- Low power dissipation
- PAL/SECAM dual system can be configured by combination with M51404AFP (SECAM chroma IC)
- 32-pin flat package

APPLICATION

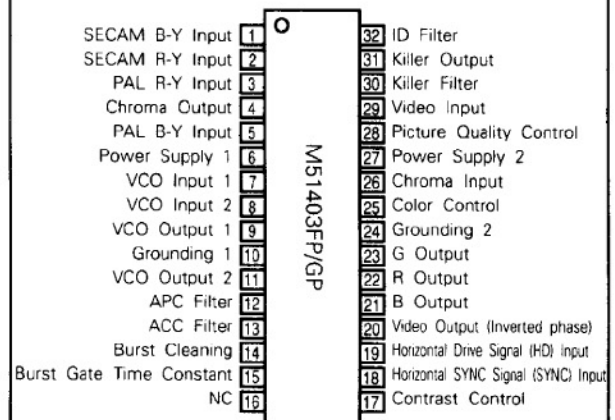
LCD, TV

RECOMMENDED OPERATING CONDITION

Supply voltage range.....4.0 ~ 5.0V

Rated supply voltage.....4.5V

PIN CONFIGURATION (TOP VIEW)

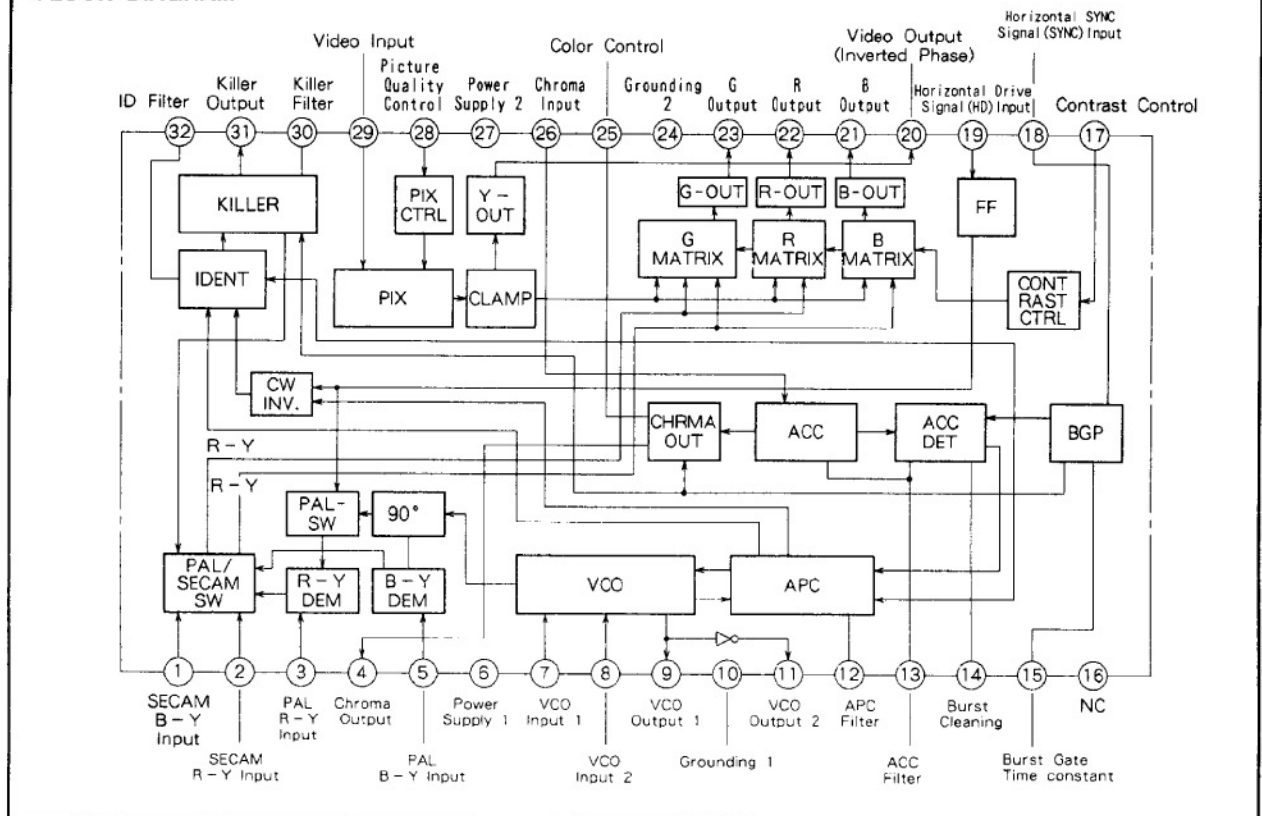


Outline 32P2W-A (FP)

32P2U-B (GP)

NC: No connection

BLOCK DIAGRAM



M51403FP/GP

PAL VIDEO CHROMA SIGNAL PROCESSOR

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Unit
V _{CC}	Supply voltage	5.0	V
Surge	Surge withstand voltage (200pF)	± 300*	V
V _{IN⑧}	Input amplitude at pin ⑧	500	mV _{P-P}
I _{OUT⑪}	Outflow current at pin ⑪	800	mA
I _{OUT⑫}	Outflow current at pin ⑫	900	mA
V _{IN⑬}	Input voltage at pin ⑬	V _{CC} + 0.3	V
V _{IN⑭}	Input voltage at pin ⑭	V _{CC} + 0.3	V
I _{OUT⑰} ⑱ ⑲	Outflow current at ⑰, ⑱, ⑲	900	mA
I _{OUT⑳}	Outflow current at pin ⑳	450	mA
P _d	Internal power dissipation	600 (FP) 430 (GP)	mW
T _{opr}	Operating temperature	-20~75	°C
T _{stg}	Storage temperature	-40~125	°C

* : The voltage on pins ①, ②, ⑤ is ± 200 V.

ELECTRICAL CHARACTERISTICS (T_a = 25 °C, unless otherwise noted)

Symbol	Parameter	Test No.	Test method	Limits			Unit
				Min.	Typ.	Max.	
I _{CC}	circuit current	1	Measure the inflow current when 4.5 V _{DC} of voltage is applied to pin ⑧, ⑫.	15	20	27	mA
Video Section							
Y _{max}	Maximum output	2	Input 100kHz sine wave of 0.3 V _{p-p} to pin ⑧, and measure the output amplitude at pin ⑪ when the voltage at pin ⑭ is 4.5 V _{DC} .	1.8	2.2	2.7	V _{P-P}
G _Y	Video amplifier gain	3	Input 100kHz sine wave of 0.3 V _{p-p} to pin ⑧ and measure the output amplitude at pin ⑫ when the voltage at pin ⑭ is 1.0 V _{DC} to calculate the ratio between the measured amplitude and input amplitude.	11	14.3	16	dB
Y _{CRST(2.0)}	Contrast control characteristics	4	Input 100kHz sine wave of 0.3 V _{p-p} to pin ⑧, and when the voltage at pin ⑭ is changed to 2.0, 0.5 and 0 V _{DC} , measure each output amplitude at pin ⑫ to calculate the measured amplitude to Test 3 output amplitude ratio.	0.5	3.0	5.0	dB
Y _{CRST(2.5)}				-8.0	-4.8	-3.0	dB
Y _{CRST(0)}				-50	-19	-14	dB
Y _{TONE(4.5)}	Picture quality control characteristics	5	Input 1.5 MHz sine wave of 0.3 V _{p-p} to pin ⑧, and when the voltage at pin ⑭ is 1.0 V _{DC} , and the voltage at pin ⑫ is changed to 2.25, 4.5 and 0 V _{DC} , measure each output amplitude at pin ⑫ to calculate the ratio of the measured amplitude to the output amplitude when the voltage at pin ⑫ is 2.25V _{DC} .	-4.0	-2.6	-1.5	dB
Y _{TONE(0)}				4.0	5.7	7.0	dB
Y _{FREQ}	Frequency characteristics	6	Input 200kHz and 2 MHz sine waves of 0.3 V _{p-p} to pin ⑧, and when the voltage at pin ⑫ and ⑭ are 2.25 and 1.0 V _{DC} respectively, measure the output amplitude at pin ⑫ to calculate the ratio between the measured amplitude and output amplitude at 2 MHz in relation to 200 kHz.	-9.5	-6.2	-3.5	dB
Chroma Section							
C _{max}	Maximum output	7	Input a sine wave of 0 dB to pin ⑧, and measure the output amplitude at pin ⑫ when the voltage at pin ⑭, ⑫ are 4.5 V _{DC} .	1.5	2.0	2.5	V _{P-P}
G _C	Chroma maximum gain	8	Input a sine wave of -26 dB to pin ⑧, and when the voltage at pins ⑭, ⑫ are 4.5 V _{DC} , measure the output amplitude at pin ⑫ to calculate the ratio between the measured amplitude and input amplitude.	45	52	57	dB
C _{ACC(+6)}	ACC control characteristics	9	Input sine wave of 0, +6, -20 dB to pin ⑧, and when the voltage at pin ⑭, ⑫ are 1.0 and 0.3 V _{DC} respectively, measure each output amplitude at pin ⑫ to calculate the ratio between the measured amplitude and output amplitude at 0 dB input.	0	0.1	2.0	dB
C _{ACC(-20)}				-6.0	-1.8	0	dB
C _{KLR}	Killer operation input	10	Input PAL simple chroma signal of 0 dB to pin ⑧, reduce the amplitude, measure the input amplitude when the voltage at pin ⑭ exceeds 2.9 V, and calculate the ratio of the measured amplitude to the input amplitude at 0 dB.		-48	-40	dB
D _{KLR}	Killer color residual	11	Input a sine wave of 0 dB to pin ⑧, and measure the output amplitude at pin ⑫ when the voltage at pin ⑭, ⑫ are 1.0 and 0.3 V _{DC} respectively.	0	10	30	mV _{P-P}
C _{SAT(4.5)}	Color saturation control characteristics	12	Input a sine wave of 0 dB to pin ⑧, and when the voltage at pin ⑭, ⑫ is 1.0 V _{DC} , and the voltage at pin ⑫ is changed to 4.5, 1.0, 0.5, 0.3 and 0.1 V _{DC} , measure each output amplitude at pin ⑫ to calculate the ratio between the measured amplitude and the output amplitude when the voltage at pin ⑫ is 0.3V _{DC} . (To be continued)	1.5	3.5	6.0	dB
C _{SAT(1.0)}				1.5	3.5	6.0	dB
C _{SAT(0.5)}				1.0	3.0	5.5	dB
C _{SAT(0.1)}				-16	-13	-10	dB

PAL VIDEO CHROMA SIGNAL PROCESSOR

ELECTRICAL CHARACTERISTICS (cont.)

Symbol	Parameter	Test No.	Test method	Limits			Unit
				Min.	Typ.	Max.	
CCTRL(4.5)	Color control characteristics	13	Input a sine wave of 0 dB to pin ⑤, and when the voltage at pin ①, ② are 4.5 V _{cc} , measure the amplitude at pin ④; it shall be CCTRL(4.5). In the same manner, measure the output amplitude at pin ④ when the voltage at pin ② is 2.0 and 0.5 V _{cc} to calculate the ratio between the measured amplitude and CCTRL(4.5); the amplitude shall be CCTRL(2.0) and CCTRL(0.5) respectively.	0.8	1.2	1.6	V _{P-P}
CCTRL(2.0)				-6	-2.8	0	dB
CCTRL(0.5)				-20	-17	-14	dB
Δf _{VCO}	VCO free run frequency	14	Input only SYNC to pin ⑩ and measure the oscillation frequency at pin ① to calculate the difference from 4.433619 MHz. (No input at pin ⑤)	-950	0	+950	Hz
Δf _{VCOpull}	APC pull-in range	15	Input a sine wave of 0 dB to pin ⑤ and change the frequency. Measure the input frequency at which the voltage at pin ⑤ change from "H" to "L." (The center frequency shall be 4.433619MHz.)	+200	+300		Hz
DB	B demodulator sensitivity	16	Input a sine wave of 0 dB to pin ⑤ and measure the output amplitude at pin ④ when the voltages at pin ①, ② are 1.0 and 0.3 V _{cc} respectively.	0.4	0.7	1.2	V _{P-P}
R (R/B)	Demodulated output voltage ratio	17	Input a sine wave of 0 dB to pin ⑤ and measure the output amplitude at pin ④, ② when the voltages at pin ①, ② are 1.0 and 0.3 V _{cc} respectively to calculate the ratio between the measured amplitude and the output amplitude in Test No. 16 above.	0.40	0.57	0.70	-
R (G/B)				0.25	0.35	0.45	-
DleakB	Demodulated output carrier leak	18	Input a sine wave of 0 dB to pin ⑤ and measure the output amplitude at pin ①, ②, ③ for the element of 8.9 MHz when the voltages at pins ①, ② are 1.0 and 0 V _{cc} respectively.	0	8	25	mV _{P-P}
DleakR				0	8	30	mV _{P-P}
DleakG				0	8	25	mV _{P-P}
R _{DC}	PAL/SECAM output DC voltage difference	19	Measure each DC voltage difference at pin ①, ②, ③ when pin ⑤ is grounded with V _{cc} at 10KΩ with noinput at pins ② and ③. * 4.5 V _{cc} for voltage at pin ① and ③	0	19	50	mV _{DC}
G _{DC}				0	6	50	mV _{DC}
B _{DC}				0	10	50	mV _{DC}
V _{31KLR-H}	"H" voltage at killer pin	20	Input only SYNC and measure each DC voltage at pin ① when pin ⑤ is connected through 10kΩ to "GND (L)," and it is connected to "V _{cc} (H)" in the same manner. (No input at pin ⑤)	3.3	3.7	4.5	V _{DC}
V _{31KLR-L}	"L" voltage at killer pin	20		0	0.1	0.3	V _{DC}
V _{CONT}	Voltage at no signal input	21	Measure output DC voltage at pin ② when no signal is input.	2.7	3.1	3.7	V
G _{CO}	Luminance amplifier gain	22	Input 100 kHz sine wave of 0.1 V _{p-p} to pin ⑤ and measure the output amplitude at pin ④ to calculate the ratio between the measured amplitude and input amplitude.	11.0	14.0	17.0	dB
G _{max}	Maximum output	23	Input 100 kHz sine wave of 0.5 V _{p-p} to pin ⑤ and measure the output amplitude at pin ④.	1.7	2.5	3.0	V _{P-P}
SB	SECAM demodulated luminance	24	Input 100 kHz sine wave of 0.3 V _{p-p} to pin ①, ② when pin ⑤ is connected through 10 KΩ to "V _{cc} " with pin ① = 1.0 V _{cc} and pin ② = 0 V _{cc} , and measure the output amplitude at pin ④.	0.7	1.0	1.3	V _{P-P}
SR (R/B)	SECAM demodulated output voltage ratio	25	Measure the output amplitude at pin ④, ② under the conditions in Test No. 24 above, and calculate the ratio between the measured amplitude and output amplitude in Test.	0.55	0.67	0.80	-
SR (G/B)				0.45	0.55	0.65	-

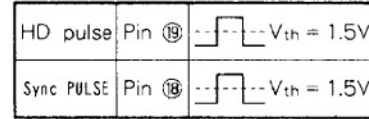
PAL VIDEO CHROMA SIGNAL PROCESSOR

SWITCH CONDITIONS IN TEST CIRCUIT

Test No.	Pin Voltage (V _{DC})						S										W			
	⑰	⑳	㉑	㉒	㉓	㉔	S ₁	S ₃	S ₁₇	S ₁₉	S ₂₅	S ₂₆	S ₂₈	S ₂₉₋₁	S ₂₉₋₂	S ₃₀				
1	2.7	2.25	4.5	2.25			OFF	OFF	OFF	OFF	OFF	ON	OFF	ON	OFF	OFF				
2	4.5	0	4.5	2.25			OFF	ON	OFF	ON	ON	ON	OFF	ON	OFF	OFF				
3	1.0	0	4.5	2.25			OFF	ON	OFF	ON	ON	ON	OFF	ON	OFF	OFF				
4	2.0 0.5 0	0	4.5	2.25			OFF	ON	OFF	ON	ON	ON	OFF	ON	OFF	OFF				
5	1.0	0	4.5	2.25 4.5 0			OFF	ON	OFF	ON	ON	ON	OFF	ON	OFF	OFF				
6	1.0	0	4.5	2.25			OFF	ON	OFF	ON	ON	ON	OFF	ON	OFF	OFF				
7	4.5	4.5	4.5	2.25	2.9	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON				
8	4.5	4.5	4.5	2.25	2.9	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON				
9	1.0	0.3	4.5	2.25	2.9	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON				
10	1.0	0.3	4.5	2.25	2.9	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON				
11	1.0	0.3	4.5	2.25	2.9	4.5	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON				
13	4.5	4.5 2.0 0	4.5	2.25	2.9	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON				
14	1.0	0.3	4.5	2.25	2.9		OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF				
15	1.0	0.3	4.5	2.25	2.9		OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF				
16	1.0	0.3	4.5	2.25	2.9	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON				
17	1.0	0.3	4.5	2.25	2.9	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON				
18	1.0	0	4.5	2.25	2.9	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON				
19	4.5	4.5	4.5	4.5		4.5 0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON				
20	1.0		4.5	2.25	2.9	4.5 0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON				
21	1.0	0	4.5	2.25			OFF	ON	OFF	ON	ON	ON	OFF	ON	OFF	OFF				
22	1.0	0	4.5	2.25			OFF	ON	OFF	ON	ON	ON	OFF	ON	OFF	OFF				
23	1.0	0	4.5	2.25			OFF	ON	OFF	ON	ON	ON	OFF	ON	OFF	OFF				
24	1.0	0.3	4.5	2.25	2.9	4.5	ON	ON	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON				
25	1.0	0.3	4.5	2.25	2.9	4.5	ON	ON	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON				

* Normally S5 should be OFF.

Input Pulse Pin Threshold



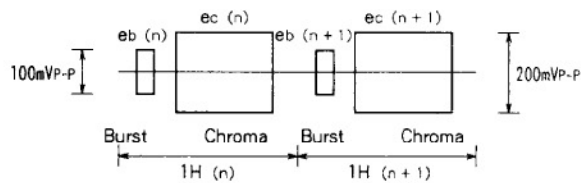
NOTE: When a pulse is input with the HD pulse polarity facing downward "↓":

- Fall position: This position should be within the fly-back period and ahead of the burst signal.
- Rise position: This position should be within the fly-back period and not come into contact with the image.

ELECTRICAL CHARACTERISTICS TEST METHOD

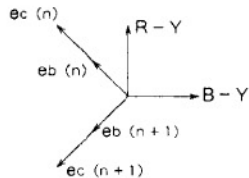
NOTE1. "Sine wave of 0dB" in the chroma section above refers to 4.43MHz, 100mV_{P-P}.

NOTE2. PAL simple chroma signal (0dB)

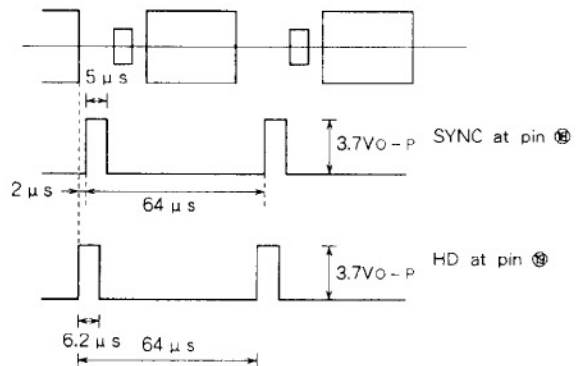


$f_{sb}(n) = f_{sc}(n) = 4.433619\text{MHz}$ (Same phase)

The phase relation of the above signal is as shown on the right. The phase relation of $ec(n)$ and $ec(n+1)$ to the burst may not always be as shown in the figure above: the test items for the phase relation in particular should be variable.



INPUT SIGNAL

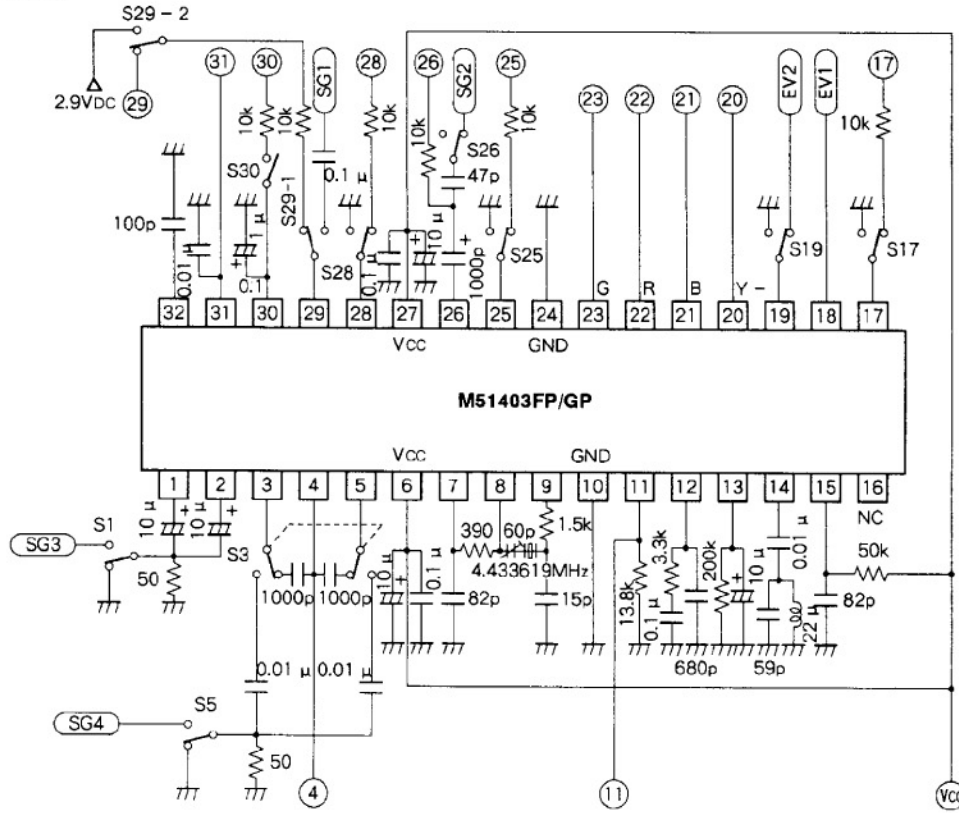


The above timing is standard: there is an allowance of 1.5 μs after SYNC fall and 12 μs before SYNC fall in HD rise, and 2 to 7.5 μs in HD width. The input amplitude should be 1.7V_{0-P}-V_{CC}.

M51403FP/GP

PAL VIDEO CHROMA SIGNAL PROCESSOR

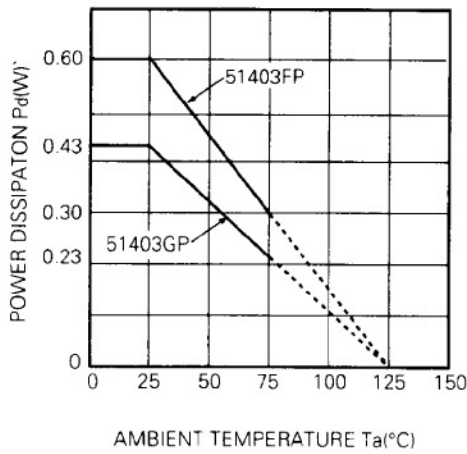
TEST CIRCUIT



Units Resistance: Ω
Capacitance: F

TYPICAL CHARACTERISTICS

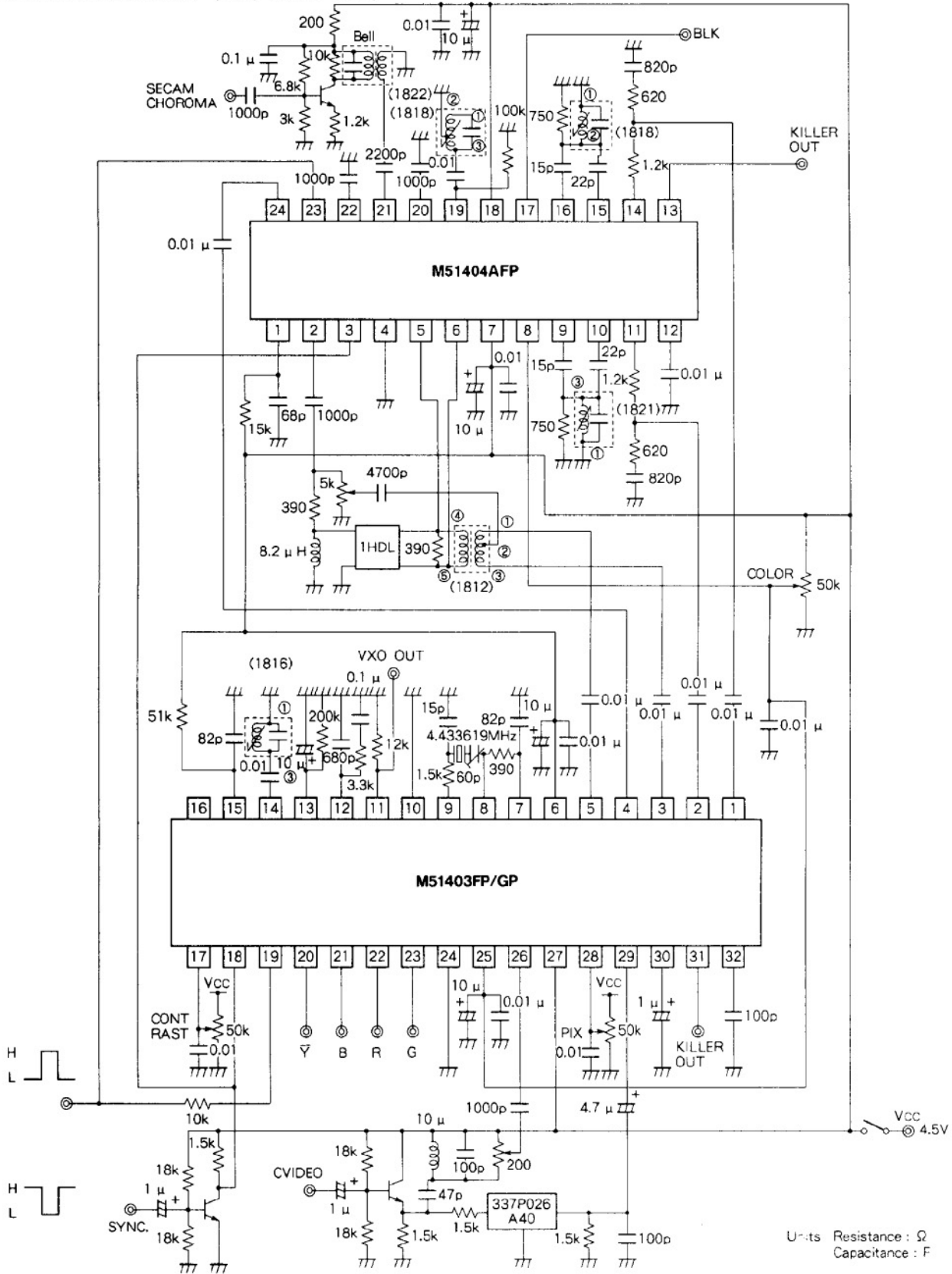
THERMAL DERATING (MAXIMUM RATING)



M51403FP/GP

PAL VIDEO CHROMA SIGNAL PROCESSOR

APPLICATION EXAMPLE (PAL/Secam dual)



PAL VIDEO CHROMA SIGNAL PROCESSOR

DESCRIPTION OF PIN

Pin No.	Name	Peripheral circuit of pins
①	SECAM B-Y Input	
②	SECAM R-Y Input	
③	PAL R-Y Input	
⑤	PAL B-Y Input	
④	Chroma Output	
⑥	Vcc1	
⑦	VCO Input 1	
⑧	VCO Input 2	
⑨	VCO Output 1	

PAL VIDEO CHROMA SIGNAL PROCESSOR

DESCRIPTION OF PIN (cont.)

Pin No.	Name	Peripheral circuit of pins
⑩	GND	
⑪	VCO Output 2	
⑫	APC Filter	
⑬	ACC Filter	
⑭	Burst Cleaning	

PAL VIDEO CHROMA SIGNAL PROCESSOR

DESCRIPTION OF PIN (cont.)

Pin No.	Name	Peripheral circuit of pins
⑮	Burst Gate Time Constant	
⑯	N.C.	—
⑰	Contrast Control	
⑱	Horizontal Sync Signal Input (SYNC)	
⑲	Horizontal Drive Signal Input (HD)	

PAL VIDEO CHROMA SIGNAL PROCESSOR

DESCRIPTION OF PIN (cont.)

Pin No.	Name	Peripheral circuit of pins
⑳	VIDEO Output (Inverted phase)	
㉑	B Output	
㉒	R Output	
㉓	G Output	
㉔	GND	—
㉕	Color Control	
㉖	Chroma Input	
㉗	Vcc2	—
㉘	Picture Quality Control	

DESCRIPTION OF PIN (cont.)

Pin No.	Name	Peripheral circuit of pins
⑳	VIDEO Input	
㉑	Killer Filter	
㉒	Killer Output	
㉓	ID Filter	