

DESCRIPTION

RT3J33M is a composite transistor built with two INJ0003AX chips in SC-88 package.

FEATURE

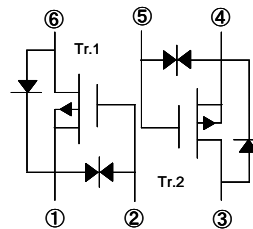
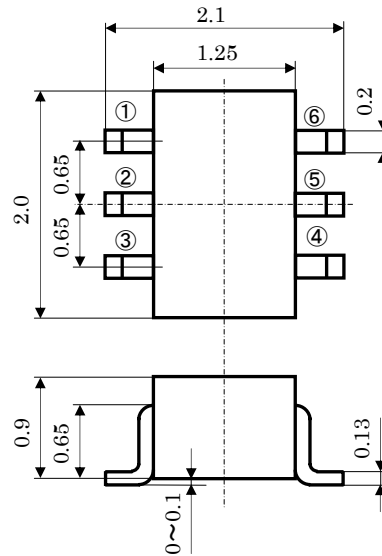
- Input impedance is high, and not necessary to consider a drive electric current.
- Vth is low, and drive by low voltage is possible. Vth=0.6~1.2V
- Low on Resistance. Ron=2Ω (TYP)
- High speed switching.
- Small package for easy mounting.

APPLICATION

high speed switching , Analog switching

OUTLINE DRAWING

Unit:mm



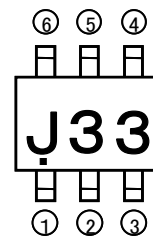
TERMINAL CONNECTOR
①:SOURCE1
②:GATE1
③:DRAIN2
④:SOURCE2
⑤:GATE2
⑥:DRAIN1

JEITA:SC-88

MAXIMUM RATING (Ta=25°C)

SYMBOL	PARAMETER	RATING	UNIT
VDSS	Drain-source voltage	-20	V
VGSS	Gate-source voltage	±8	V
ID	Drain current	-200	mA
PD	Total power dissipation (Ta=25°C)	150	mW
Tch	Channel temperature	+125	°C
Tstg	Range of Storage temperature	-55~+125	°C

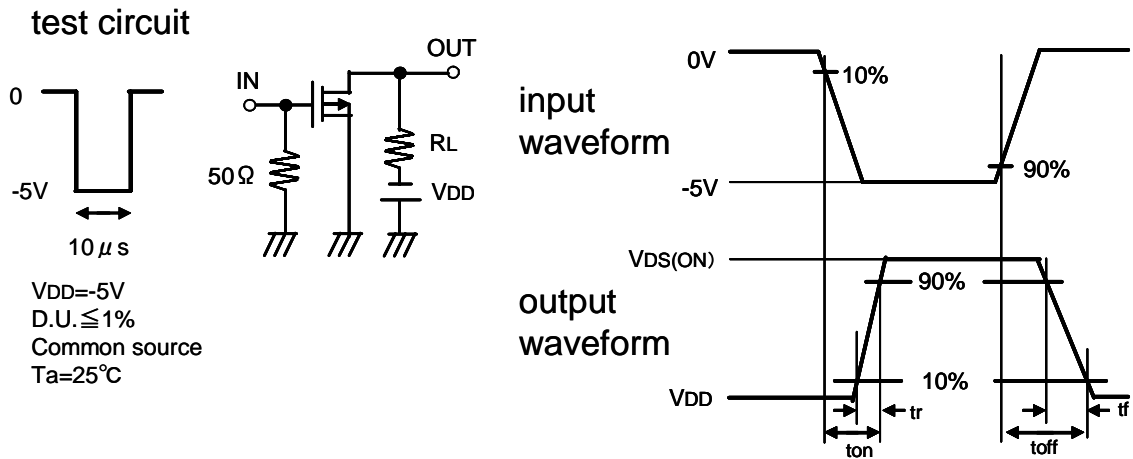
MARKING



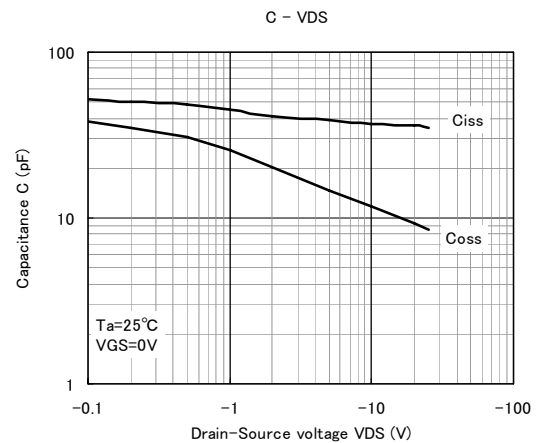
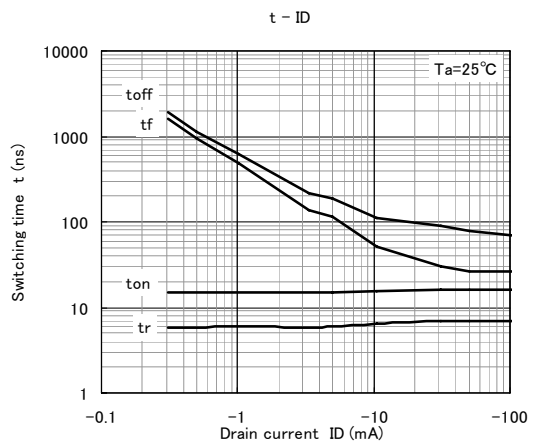
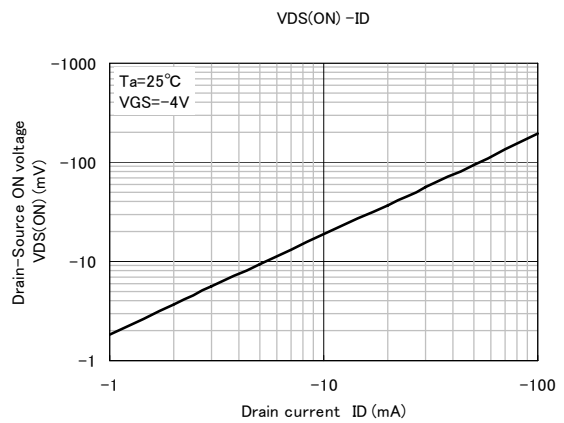
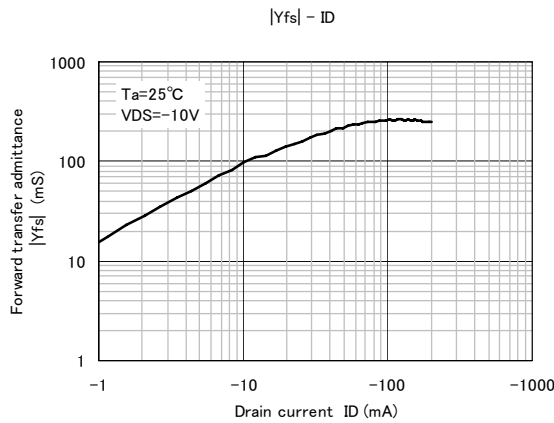
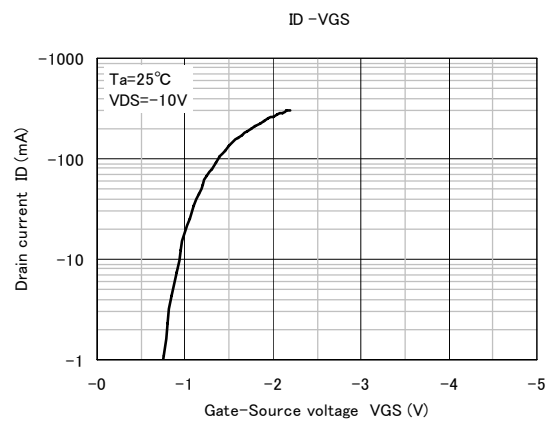
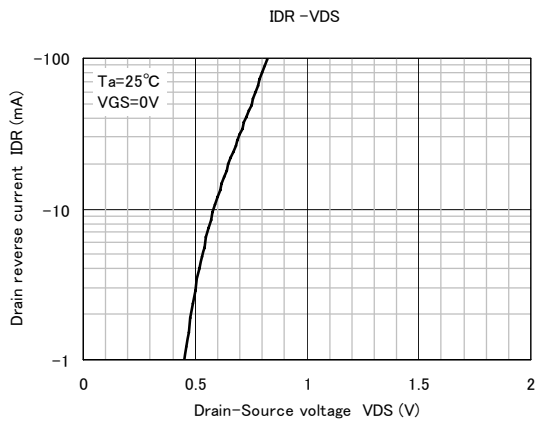
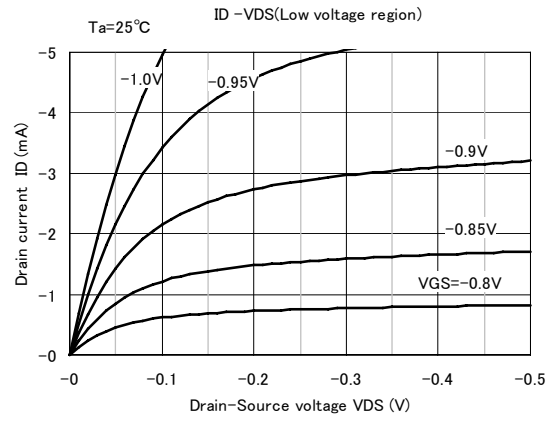
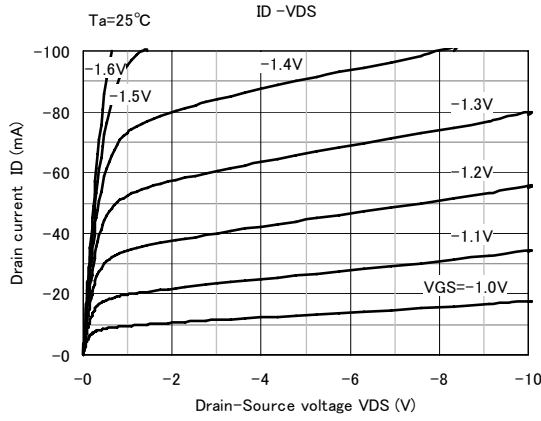
ELECTRICAL CHARACTERISTICS (Ta=25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
$V_{(BR)DSS}$	Drain-source breakdown voltage	$I_D = -100 \mu A, V_{GS} = 0V$	-20	-	-	V
I_{GSS}	Gate-source leak current	$V_{GS} = \pm 5V, V_{DS} = 0V$	-	-	± 0.5	μA
I_{DSS}	Zero gate voltage drain current	$V_{DS} = -20V, V_{GS} = 0V$	-	-	-50	μA
V_{th}	Gate threshold voltage	$I_D = -250 \mu A, V_{DS} = V_{GS}$	-0.6	-	-1.2	V
$ Y_{fs} $	Forward transfer admittance	$V_{DS} = -10V, I_D = -0.1A$	-	280	-	mS
$R_{DS(ON)}$	Static drain-source on-state resistance	$I_D = -100mA, V_{GS} = -4.0V$	-	2	-	Ω
C_{iss}	Input capacitance	$V_{DS} = -10V, V_{GS} = 0V, f = 1MHz$	-	37	-	pF
C_{oss}	Output capacitance	$V_{DS} = -10V, V_{GS} = 0V, f = 1MHz$	-	12	-	pF
t_{ON}	Switching time	$V_{DD} = -5V, I_D = -10mA$	-	16	-	ns
t_{OFF}		$V_{GS} = 0 \sim -5V$	-	110	-	

Switching time test condition



TYPICAL CHARACTERISTICS





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