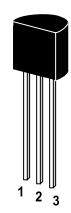
## NPN Silicon Triple Diffused Planar Transistor

for High-Voltage Driver Applications.

The transistor is subdivided into two groups, D and E, according to its DC current gain.

On special request, these transistors can be manufactured in different pin configurations.



1. Emitter 2. Collector 3. Base

TO-92 Plastic Package Weight approx. 0.18g

## Absolute Maximum Ratings (T<sub>a</sub> = 25°C)

	Symbol	Value	Unit
Collector Base Voltage	V <sub>CBO</sub>	400	V
Collector Emitter Voltage	V <sub>CEO</sub>	400	V
Emitter Base Voltage	V <sub>EBO</sub>	5	V
Collector Current	Ι <sub>C</sub>	200	mA
Collector Current (Pulse)	I <sub>CP</sub>	400	mA
Power Dissipation	P <sub>tot</sub>	600	mW
Junction Temperature	Tj	150	°C
Storage Temperature Range	Τs	-55 to +150	°C







Dated : 07/12/2002

## ST 2SC4002

## Characteristics at T<sub>amb</sub>=25 °C

	Symbol	Min.	Тур.	Max.	Unit
DC Current Gain					
at V <sub>CE</sub> =10V, I <sub>C</sub> =50mA					-
Current Gain Group D	h <sub>FE</sub>	60	-	120	-
E	h <sub>FE</sub>	100	-	200	-
Collector Cutoff Current					
at V <sub>CB</sub> =300V	I <sub>CBO</sub>	-	-	0.1	μA
Emitter Cutoff Current					
at V <sub>EB</sub> =4V	I <sub>EBO</sub>	-	-	0.1	μA
Collector Emitter Saturation Voltage					
at $I_{C}$ =50mA, $I_{B}$ =5mA	$V_{\text{CE(sat)}}$	-	-	0.6	V
Base Emitter Saturation Voltage					
at I <sub>C</sub> =50mA, I <sub>B</sub> =5mA	V <sub>BE(sat)</sub>	-	-	1.0	V
Gain Bandwidth Product					
at $V_{CE}$ =30V, I <sub>C</sub> =10mA	f⊤	-	70	-	MHz







Dated : 07/12/2002