



SPN6099

N-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPN6099 is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application , notebook computer power management and other battery powered circuits where high-side switching .

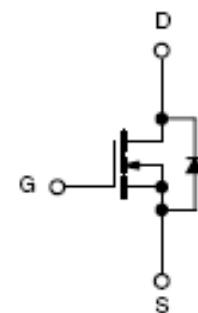
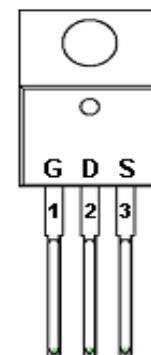
FEATURES

- ◆ 60V/80A,R_{DS(ON)}= 4.0mΩ@V_{GS}= 10V
- ◆ 60V/20A,R_{DS(ON)}= 4.2mΩ@V_{GS}= 6.0V
- ◆ 60V/10A,R_{DS(ON)}= 4.4mΩ@V_{GS}=4.5V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ TO-220-3L package design

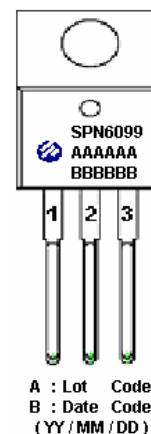
APPLICATIONS

- DC/DC Converter
- Load Switch
- SMPS Secondary Side Synchronous Rectifier

PIN CONFIGURATION(TO-220-3L)



PART MARKING



A : Lot Code
B : Date Code
(YY / MM / DD)



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PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	D	Drain
3	S	Source

ORDERING INFORMATION

Part Number	Package	Part Marking
SPN6099T220TGB	TO-220-3L	SPN6099

※ SPN6099T220TGB: Tube ; Pb – Free; Halogen – Free

ABSOLUTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	VDSS	60	V
Gate –Source Voltage	VGSS	±20	V
Continuous Drain Current(TJ=150°C)	TA=25°C	80	A
	TA=70°C	80	
Pulsed Drain Current	IDM	420	A
Avalanche Current	IAS	75	A
Power Dissipation	TA=25°C	62.5	W
	TA=70°C	3.38	
Avalanche Energy with Single Pulse (TJ=25°C , L = 0.12mH , IAS = 80A , VDD = 48V.)	EAS	380	mJ
Operating Junction Temperature	TJ	-55/150	°C
Storage Temperature Range	TSTG	-55/150	°C
Thermal Resistance-Junction to Ambient	R _{θJA}	2	°C/W



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ELECTRICAL CHARACTERISTICS

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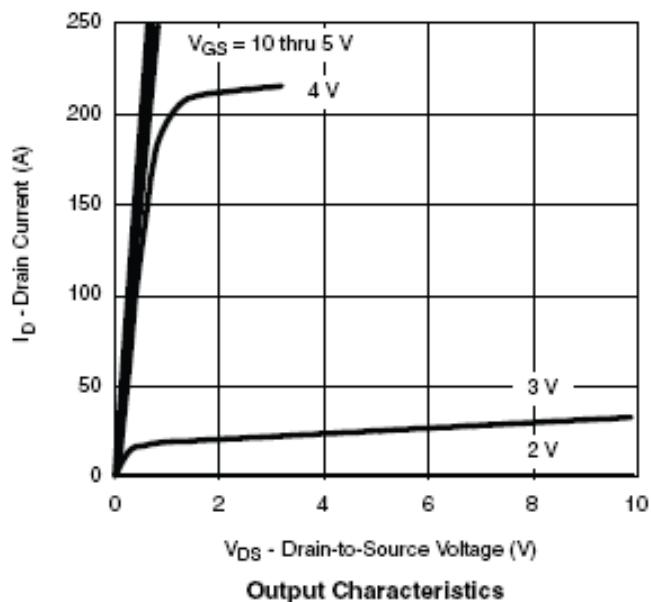
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, ID=250uA	60			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , ID=250uA	1.0		2.5	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			1	uA
		V _{DS} =60V, V _{GS} =0V T _J = 150 °C			250	
On-State Drain Current	I _{D(on)}	V _{DS} ≥5V, V _{GS} =10V	70			A
Drain-Source On-Resistance	R _{DSS(on)}	V _{GS} = 10V, ID=80A		3.6	4.0	mΩ
		V _{GS} = 6.0V, ID=20A		3.8	4.2	
		V _{GS} = 4.5V, ID=10A		4.0	4.4	
Forward Transconductance	g _{fs}	V _{DS} =15V, ID=20A		60		S
Diode Forward Voltage	V _{SD}	I _S =30A, V _{GS} =0V			1.5	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =30V, V _{GS} =10V ID= 80A		100		nC
Gate-Source Charge	Q _{gs}			28		
Gate-Drain Charge	Q _{gd}			25		
Input Capacitance	C _{iss}	V _{DS} =30V, V _{GS} =0V f=1MHz		6000		pF
Output Capacitance	C _{oss}			900		
Reverse Transfer Capacitance	C _{rss}			320		
Turn-On Time	t _{d(on)}	V _{DD} =30V, R _L =0.4Ω ID=80A, V _{GEN} =10V R _G =1.0Ω		25	35	nS
	t _r			15	25	
Turn-Off Time	t _{d(off)}			35	55	
	t _f			8	15	



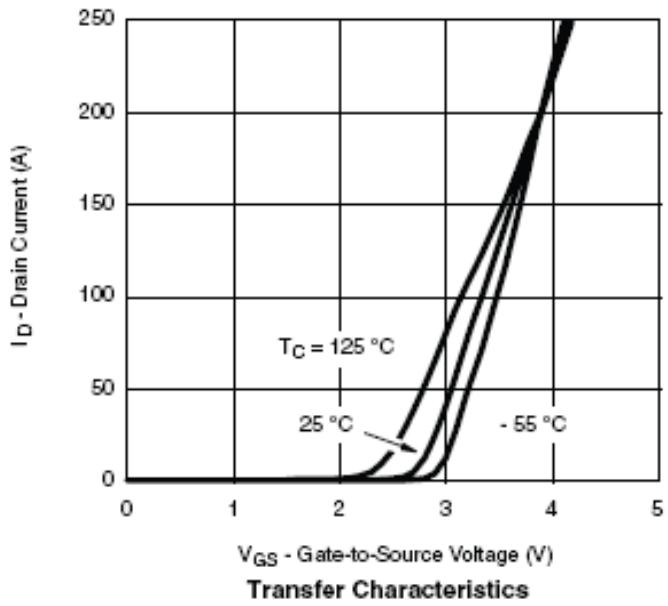
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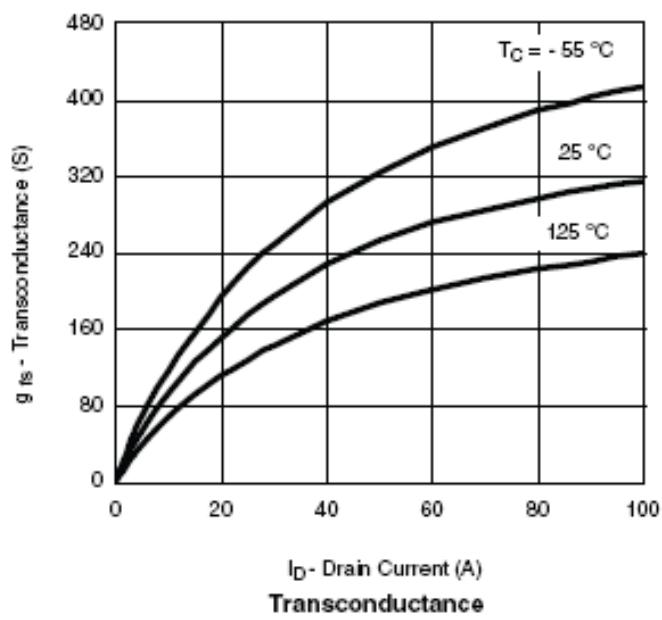
TYPICAL CHARACTERISTICS



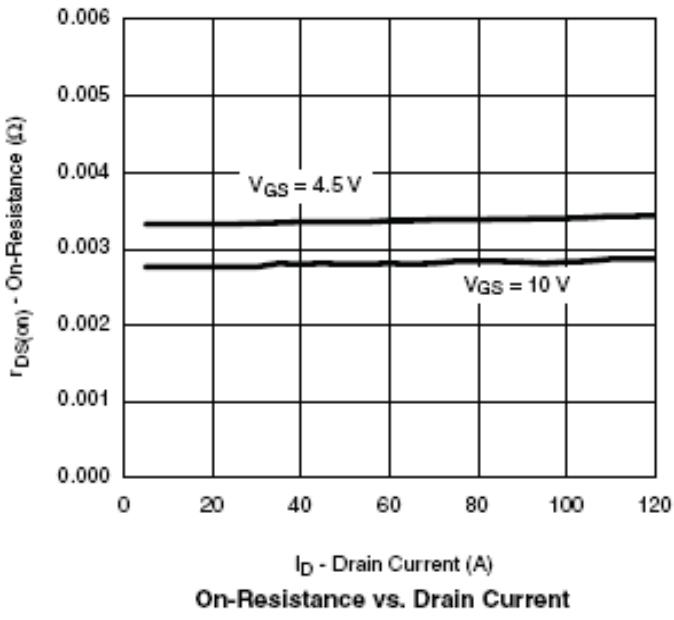
Output Characteristics



Transfer Characteristics



Transconductance



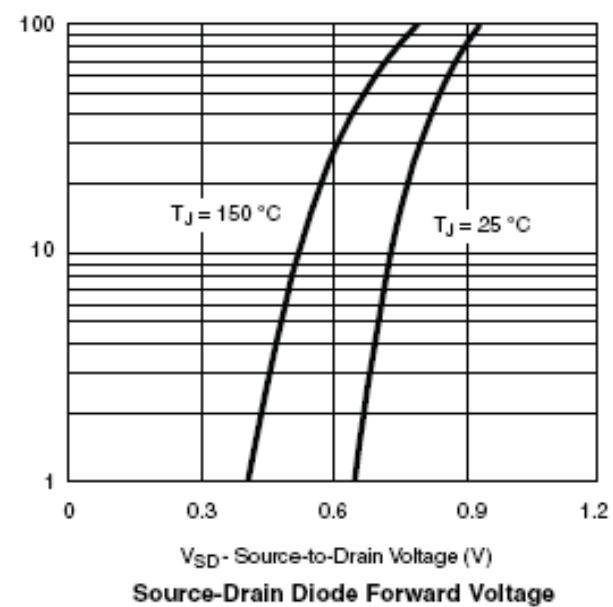
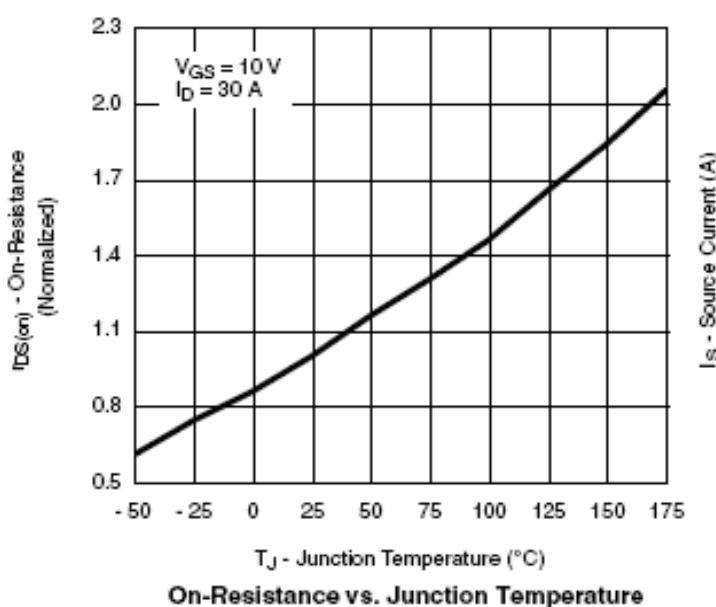
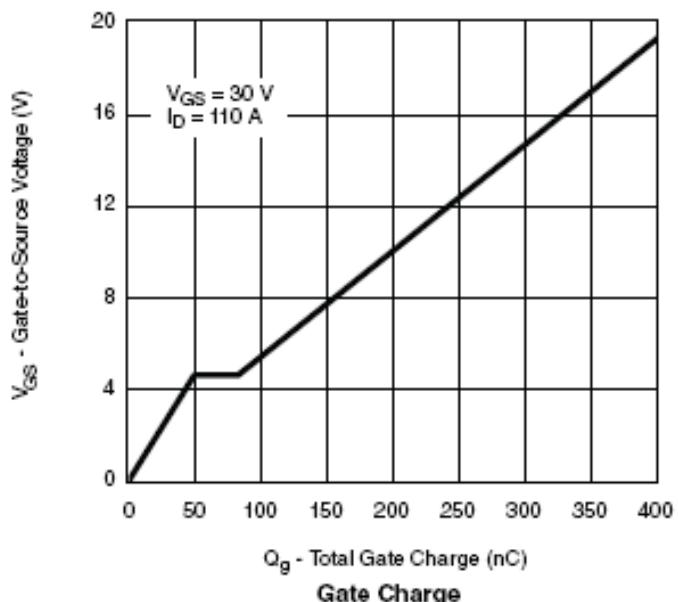
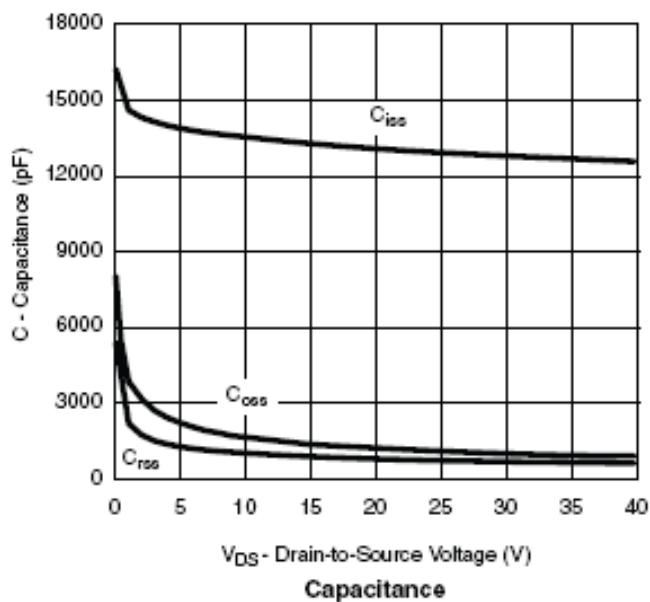
On-Resistance vs. Drain Current



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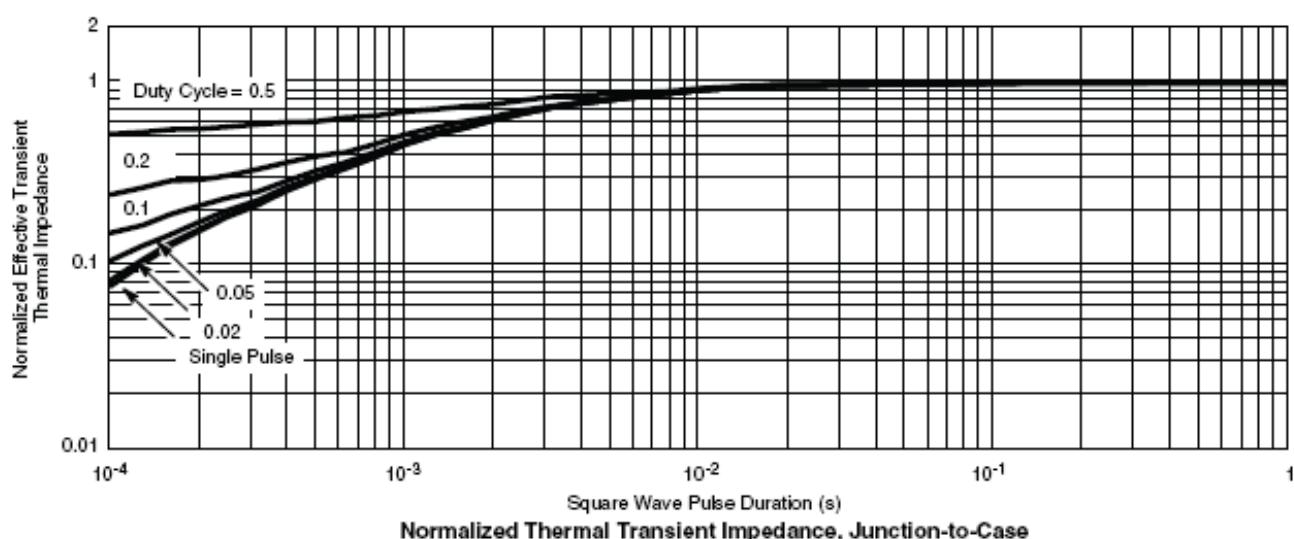
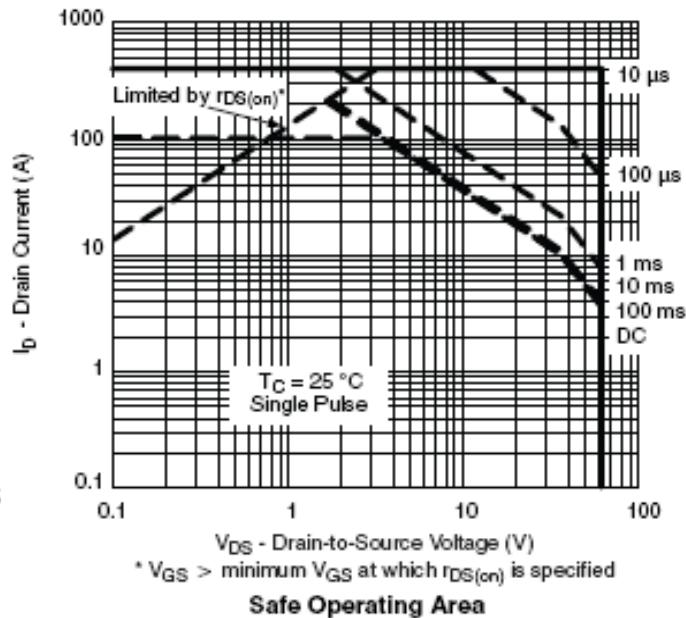
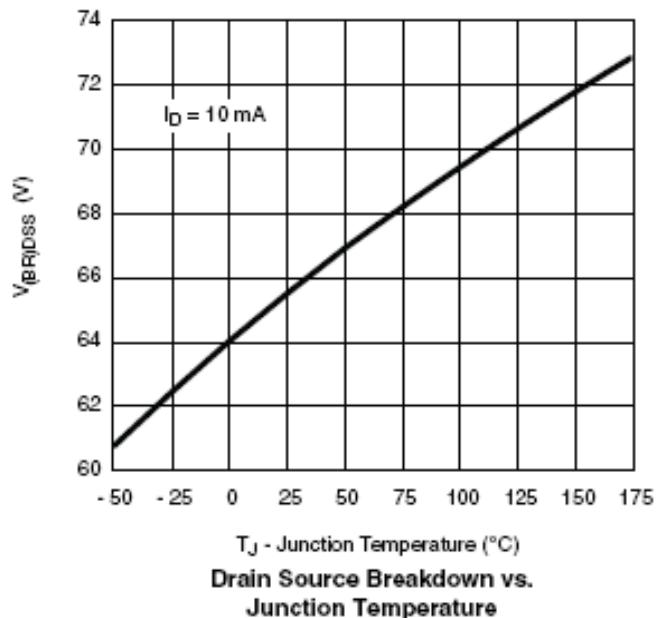




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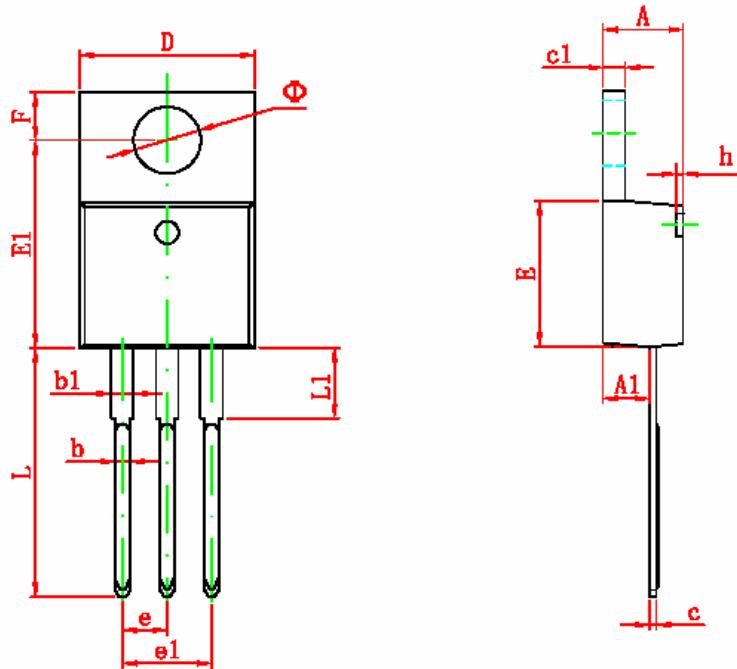




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TO-220-3L PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	4.470	4.670	0.176	0.184
A1	2.520	2.820	0.099	0.111
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
E	8.500	8.900	0.335	0.350
E1	12.060	12.460	0.475	0.491
e	2.540 TYP		0.100 TYP	
e1	4.980	5.180	0.196	0.204
F	2.590	2.890	0.102	0.114
h	0.000	0.300	0.000	0.012
L	13.400	13.800	0.528	0.543
L1	3.560	3.960	0.140	0.156
• •	3.735	3.935	0.147	0.155



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SYNC Power Corporation

7F-2, No.3-1, Park Street

NanKang District (NKSP), Taipei, Taiwan 115

Phone: 886-2-2655-8178

Fax: 886-2-2655-8468

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