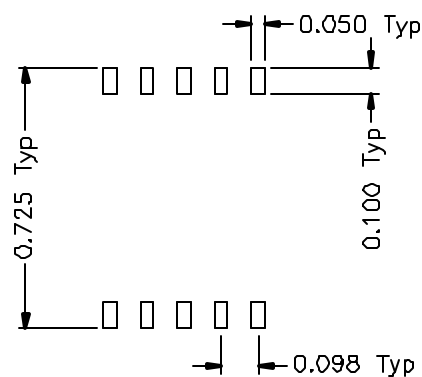
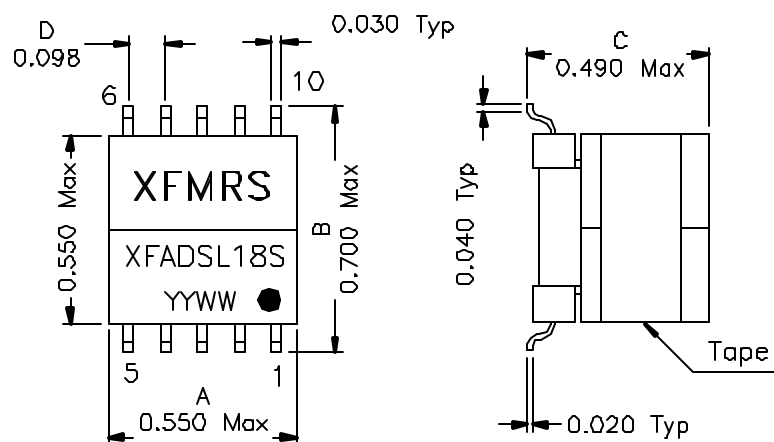
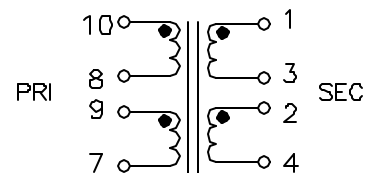


## 1. Dimensions:



Suggested PCB Layout

## 2.Schematic:



## 3.Electrical Specifications: @25°C

Turns Ratio: (P10-7):(P1-4) = 1:1:1 ±2% (TIE P3-2,P8-9)

PIN1-4 OCL: 1.2mH ±10% @10KHz 0.1V TIE P2-3 Ls

PIN1-4 LL: 10uH Max @100KHz 0.1V

(CONNECT PIN3-2, PIN8-9,PIN10-7) Ls

CW/W: PIN1-10 40pF Max. @10KHz 1.0V (TIE P3-2,P8-9) Cs

DC Res. P10-8,P9-7 0.740 Ohms ±15%

P1-3 0.570 Ohms ±15%

P2-4 0.780 Ohms ±15%

Impedance: Designed to reflect 100 Ohms on the PRI  
with 83 Ohms load on SEC

Frequency Response: ±0.5dB 100KHz-1.1MHz, 300KHz ref.

Insertion Loss: 0.5dB Max @300KHz

THD: -80dB Max. @20KHz 4.4V Across PRI 83 Ohms load,  
100 Ohms input, TIE P8-9,P3-2

Longitudinal Balance: 46dB Min.@10KHz-1.1MHz Per ITU Method  
(L->M). with terminal 4 grounded

HIPOT: 2000VAC for 1 second between P10-1(Tie P8-9,P2-3)

Designed to meet UL1950 requirements for Supplementary  
Insulation for a Primary circuit with 250 working volts

<b>XFMRS Inc</b>	Title: TRANSFORMER		
	UNLESS OTHERWISE SPECIFIED		P/N: XFADSL18S
TOLERANCES:	DWN.	廖玉坤	Jul-01-00
.xxx ±0.010	CHK.	李清儿	Jul-01-00
Dimensions in INCH	APP.	Joe Huff	Jul-01-00
SHEET 1 OF 1			

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