Complementary MOSFET

ELM14609AA-N

■General Description

ELM14609AA-N uses advanced trench technology to provide excellent Rds(on) and low gate charge.

■Features

N-channel	P-channel
• Vds=30V	Vds=-30V
• Id=8.5A(Vgs=10V)	Id=-3A(Vgs=-10V)
• Rds(on) < 18mΩ(Vgs=10V)	Rds(on) < 130mΩ(Vgs=-10V)
• Rds(on) < 28mΩ(Vgs=4.5V)	Rds(on) < 180mΩ(Vgs=-4.5V)
• Rds(on)	Rds(on) < 260mΩ(Vgs=-2.5V)

■Maximum Absolute Ratings

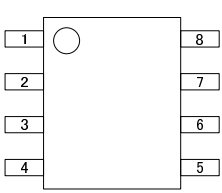
Parameter		Symbol	N-ch (Max.)	P-ch (Max.)	Unit	Note
Drain-source voltage		Vds	30	-30	V	
Gate-source voltage		Vgs	±20	±12	V	
Continuous drain current	Ta=25℃	Id	8.5	-3.0	A	1
	Ta=70℃		6.6	-2.4		
Pulsed drain current		Idm	40	-6	A	2
Power dissipation	Ta=25℃	Pd	2.00	2.00	W	
	Ta=70℃		1.28	1.28		
Junction and storage temperature range		Tj,Tstg	-55 to 150	-55 to 150	℃	

■Thermal Characteristics

Parameter		Symbol	Device	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	t≤10s	Rθja	N-ch	48.0	62.5	℃/W	1
Maximum junction-to-ambient	Steady-state			74.0	110.0	℃/W	
Maximum junction-to-lead	Steady-state	Rθjl		35.0	40.0	℃/W	3
Maximum junction-to-ambient	t≤10s	Rθja	P-ch	56.0	62.5	℃/W	1
Maximum junction-to-ambient	Steady-state			81.0	110.0	℃/W	
Maximum junction-to-lead	Steady-state	Rθjl		40.0	48.0	℃/W	3

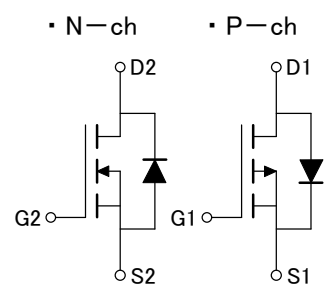
■Pin Configuration

SOP-8 (TOP VIEW)



Pin No.	Pin name
1	SOURCE2
2	GATE2
3	SOURCE1
4	GATE1
5	DRAIN1
6	DRAIN1
7	DRAIN2
8	DRAIN2

■Circuit



■Electrical Characteristics (N-ch)

Ta=25℃

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
STATIC PARAMETERS							
Drain-source breakdown voltage	BVdss	Id=250μA, Vgs=0V		30			V
Zero gate voltage drain current	Idss	Vds=24V, Vgs=0V				1	μA
			Tj=55℃			5	
Gate-body leakage current	Igss	Vds=0V, Vgs=±20V				100	nA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=250μA		1.0	1.8	3.0	V
On state drain current	Id(on)	Vgs=10V, Vds=5V		40			A
Static drain-source on-resistance	Rds(on)	Vgs=10V, Id=8.5A			15.5	18.0	mΩ
			Tj=125℃		22.3	27.0	
		Vgs=4.5V, Id=6A			23.0	28.0	
Forward transconductance	Gfs	Vds=5V, Id=8.5A			23		S
Diode forward voltage	Vsd	Is=1A, Vgs=0V			0.75	1.00	V
Max.body-diode continuous current	Is					3	A
DYNAMIC PARAMETERS							
Input capacitance	Ciss	Vgs=0V, Vds=15V, f=1MHz			1040	1250	pF
Output capacitance	Coss				180		pF
Reverse transfer capacitance	Crss				110		pF
Gate resistance	Rg	Vgs=0V, Vds=0V, f=1MHz			0.70	0.85	Ω
SWITCHING PARAMETERS							
Total gate charge (10V)	Qg	Vgs=10V, Vds=15V, Id=8.5A			19.20	23.00	nC
Total gate charge (4.5V)	Qg				9.36	11.20	nC
Gate-source charge	Qgs				2.60		nC
Gate-drain charge	Qgd				4.20		nC
Turn-on delay time	td(on)	Vgs=10V, Vds=15V, Rl=1.8Ω, Rgen=3Ω			5.2	7.5	ns
Turn-on rise time	tr				4.4	6.5	ns
Turn-off delay time	td(off)				17.3	25.0	ns
Turn-off fall time	tf				3.3	5.0	ns
Body-diode reverse recovery time	trr	If=8.5A, dI/dt=100A/μs			16.7	21.0	ns
Body-diode reverse recovery charge	Qrr	If=8.5A, dI/dt=100A/μs			6.7	10.0	nC

NOTE :

1. The value of Rθja is measured with the device mounted on 1in² FR-4 board of 2oz. Copper, in still air environment with Ta=25℃. The value in any given applications depends on the user's specific board design, The current rating is based on the t≤10s themal resistance rating.
2. Repetitive rating, pulse width limited by junction temperature.
3. The Rθja is the sum of the thermal impedance from junction to lead Rθjl and lead to ambient.
4. The static characteristics in Figures 1 to 6 are obtained using 80μs pulses, duty cycle 0.5%max.
5. These tests are performed with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with Ta=25℃. The SOA curve provides a single pulse rating.

■Typical Electrical and Thermal Characteristics (N-ch)

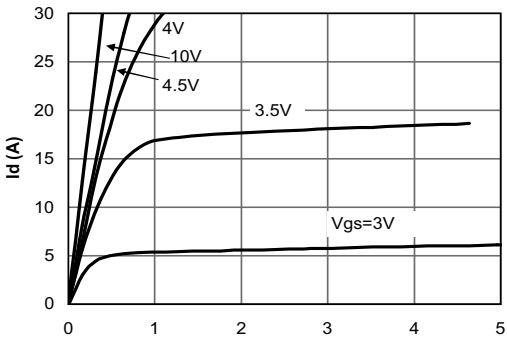


Fig 1: On-Region Characteristics

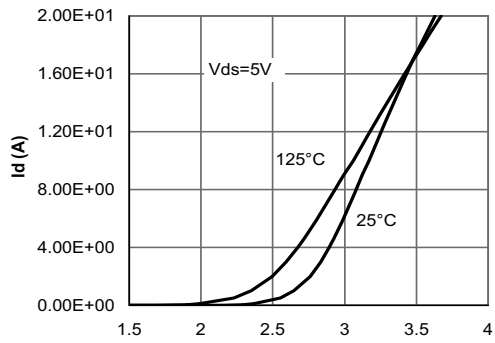


Figure 2: Transfer Characteristics

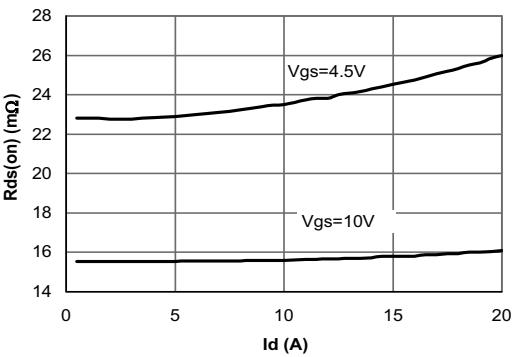


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

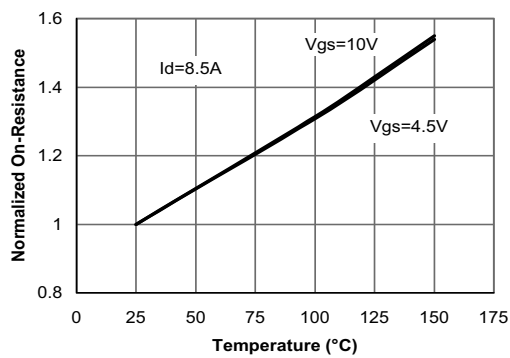


Figure 4: On-Resistance vs. Junction Temperature

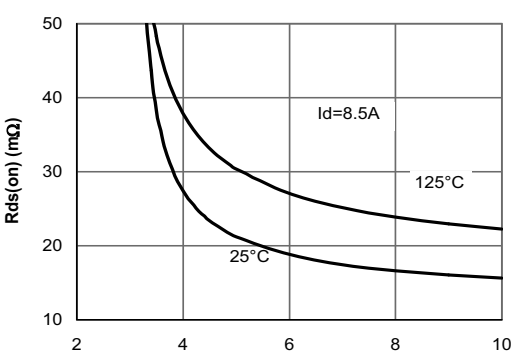


Figure 5: On-Resistance vs. Gate-Source Voltage

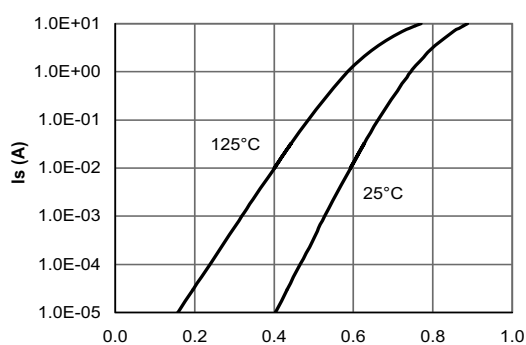


Figure 6: Body-Diode Characteristics

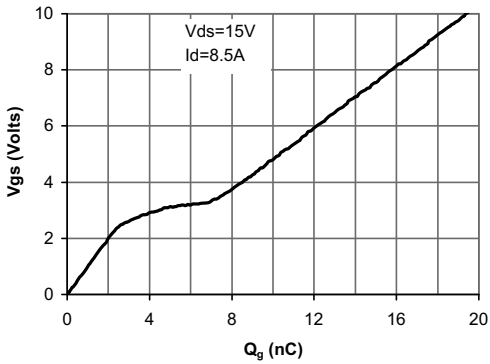


Figure 7: Gate-Charge Characteristics

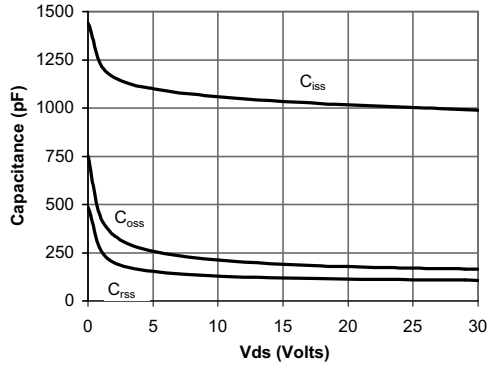


Figure 8: Capacitance Characteristics

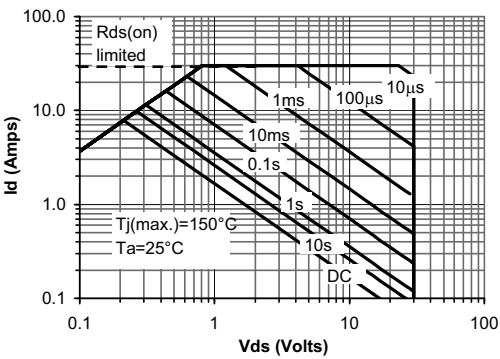


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

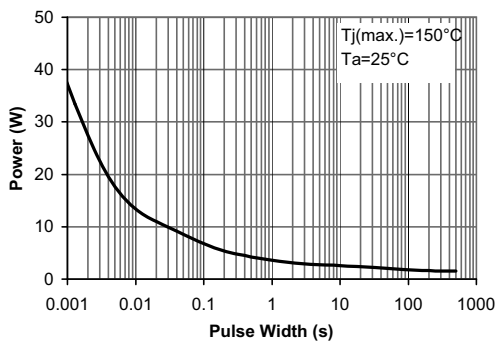


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

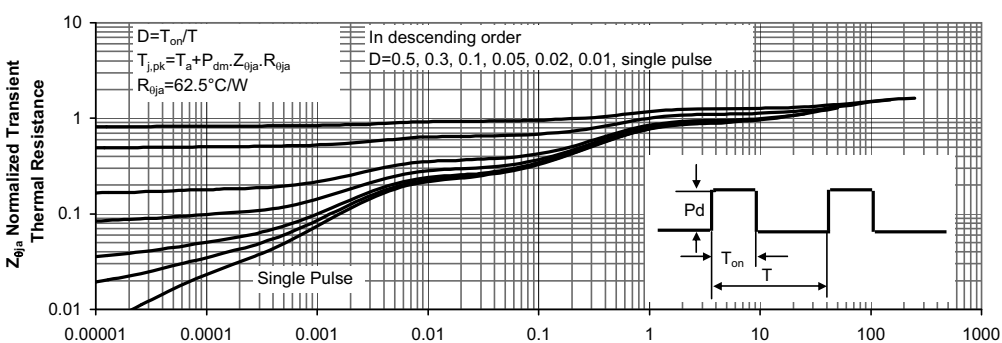


Figure 11: Normalized Maximum Transient Thermal Impedance

■Electrical Characteristics (P-ch)

Ta=25℃

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
STATIC PARAMETERS							
Drain-source breakdown voltage	BVdss	Id=-250μA, Vgs=0V		-30			V
Zero gate voltage drain current	Idss	Vds=-24V, Vgs=0V				-1	μA
			Tj=55℃			-5	
Gate-body leakage current	Igss	Vds=0V, Vgs=±12V				±100	nA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=-250μA		-0.6	-1.0	-1.4	V
On state drain current	Id(on)	Vgs=-4.5V, Vds=-5V		-10			A
Static drain-source on-resistance	Rds(on)	Vgs=-10V, Id=-3A			102	130	mΩ
			Tj=125℃		154	200	
		Vgs=-4.5V, Id=-2A			128	180	mΩ
		Vgs=-2.5V, Id=-1A			187	260	mΩ
Forward transconductance	Gfs	Vds=-5V, Id=-3A		3.0	4.5		S
Diode forward voltage	Vsd	Is=-1A, Vgs=0V			-0.85	-1.00	V
Max. body-diode continuous current	Is					-2	A
DYNAMIC PARAMETERS							
Input capacitance	Ciss	Vgs=0V, Vds=-15V, f=1MHz			409		pF
Output capacitance	Coss				55		pF
Reverse transfer capacitance	Crss				42		pF
Gate resistance	Rg	Vgs=0V, Vds=0V, f=1MHz			12		Ω
SWITCHING PARAMETERS							
Total gate charge	Qg	Vgs=-4.5V, Vds=-15V, Id=-3A			4.40		nC
Gate-source charge	Qgs				0.80		nC
Gate-drain charge	Qgd				1.32		nC
Turn-on delay time	td(on)	Vgs=-10V, Vds=-15V, Rl=5Ω, Rgen=3Ω			5.3		ns
Turn-on rise time	tr				4.4		ns
Turn-off delay time	td(off)				31.5		ns
Turn-off fall time	tf				8.0		ns
Body diode reverse recovery time	trr	If=-3A, dI/dt=100A/μs			15.8		ns
Body diode reverse recovery charge	Qrr	If=-3A, dI/dt=100A/μs			8.0		nC

NOTE :

1. The value of Rθja is measured with the device mounted on 1in² FR-4 board of 2oz. Copper, in still air environment with Ta=25℃. The value in any given applications depends on the user's specific board design, The current rating is based on the t≤10s themal resistance rating.
2. Repetitive rating, pulse width limited by junction temperature.
3. The Rθja is the sum of the thermal impedance from junction to lead Rθjl and lead to ambient.
4. The static characteristics in Figures 1 to 6 are obtained using 80μs pulses, duty cycle 0.5%max.
5. These tests are performed with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with Ta=25℃. The SOA curve provides a single pulse rating.

■Typical Electrical and Thermal Characteristics (P-ch)

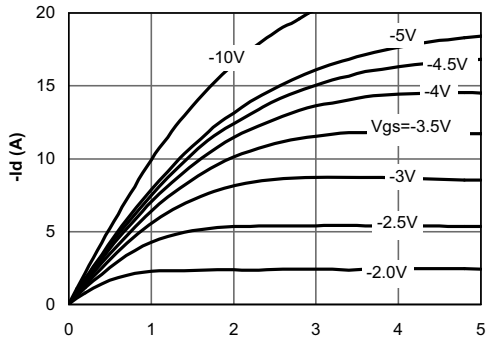


Fig 1: On-Region Characteristics

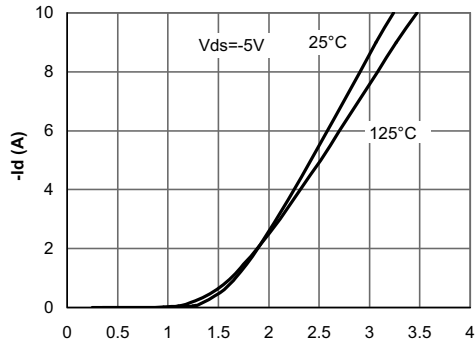


Figure 2: Transfer Characteristics

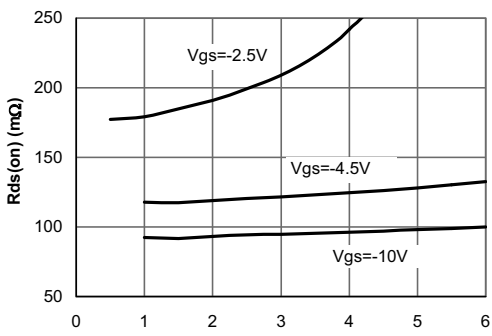


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

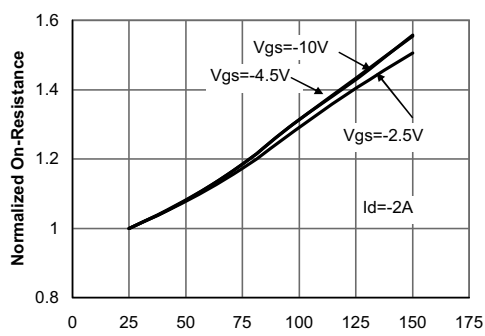


Figure 4: On-Resistance vs. Junction Temperature

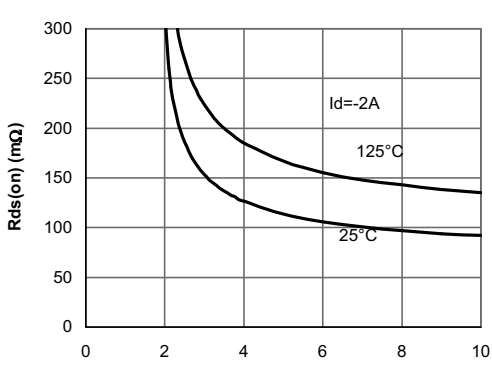


Figure 5: On-Resistance vs. Gate-Source Voltage

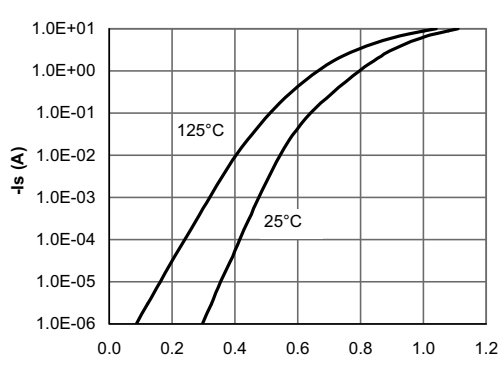


Figure 6: Body-Diode Characteristics

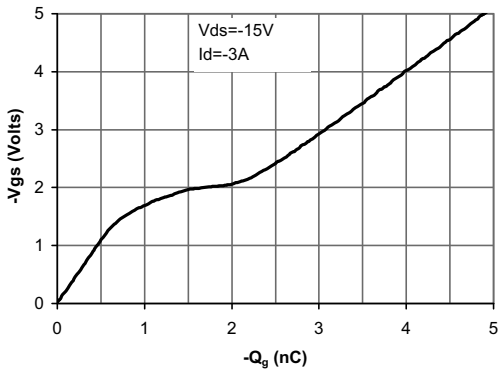


Figure 7: Gate-Charge Characteristics

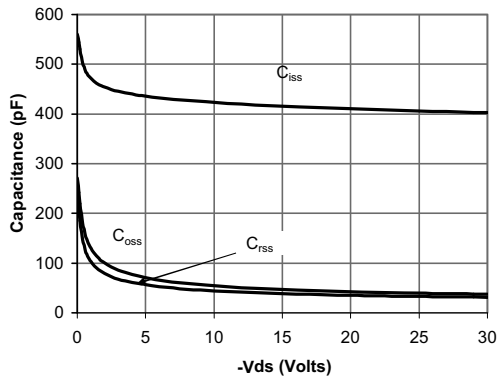


Figure 8: Capacitance Characteristics

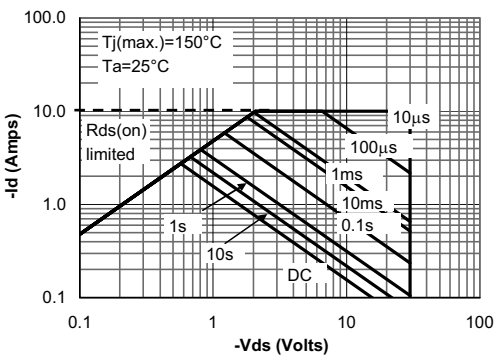


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

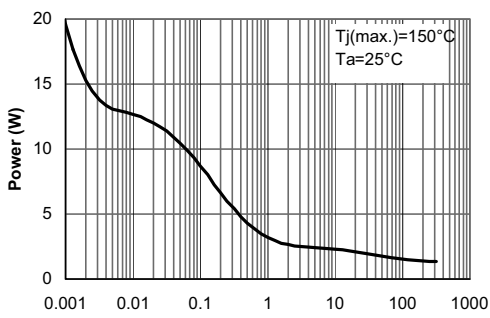


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

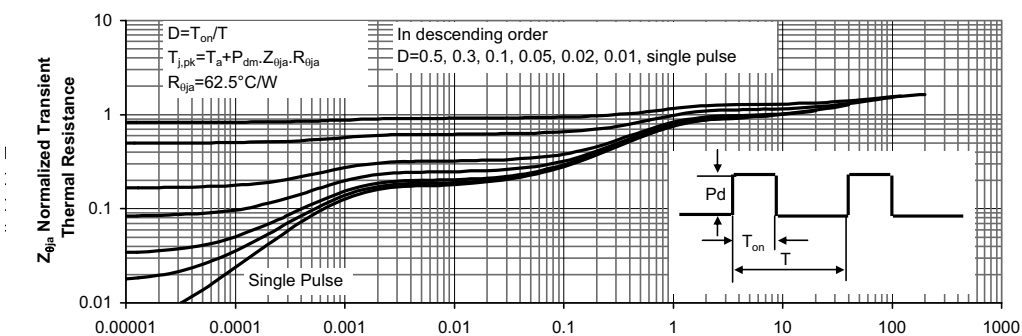


Figure 11: Normalized Maximum Transient Thermal Impedance