

RoHS Compliant Product

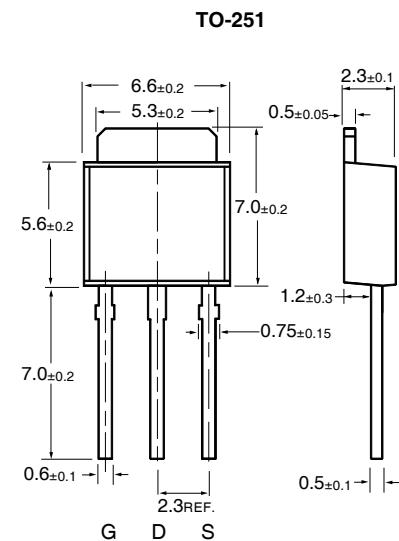
## Description

The SID01N60 provide the designer with the best combination of fast switching.

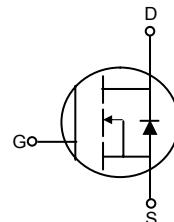
The TO-251 is universally preferred for all commercial-industrial surface mount applications and suited for AC/DC converters.

## Features

- \* Dynamic dv/dt Rating
- \* Simple Drive Requirement
- \* Fast Switching
- \* Repetitive Avalanche Rated



Dimensions in millimeters



**Marking Code: 01N60  
XXXX(Date Code)**

## Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	$V_{DS}$	600	V
Gate-Source Voltage	$V_{GS}$	±20	V
Continuous Drain Current, $V_{GS}$ @10V	$I_D$ @ $T_c=25^\circ C$	1.6	A
Continuous Drain Current, $V_{GS}$ @10V	$I_D$ @ $T_c=100^\circ C$	1	A
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	6	A
Total Power Dissipation	$P_D$ @ $T_c=25^\circ C$	39	W
Linear Derating Factor		0.31	W/ $^\circ C$
Single Pulse Avalanche Energy <sup>2</sup>	$E_{AS}$	13	mJ
Avalanche Current	$I_{AR}$	1.6	A
Repetitive Avalanche Energy	$E_{AR}$	0.5	mJ
Operating Junction and Storage Temperature Range	$T_j, T_{stg}$	-55~+150	$^\circ C$

## Thermal Data

Parameter	Symbol	Ratings	Unit
Thermal Resistance Junction-case	Max.	R <sub>thj-c</sub>	$^\circ C/W$
Thermal Resistance Junction-ambient	Max.	R <sub>thj-a</sub>	$^\circ C/W$



Elektronische Bauelemente

SID01N60

1.6A, 600V, RDS(ON) 8Ω

N-Channel Enhancement Mode Power Mos.FET

**Electrical Characteristics( Tj=25°C Unless otherwise specified)**

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	600	—	—	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA
Breakdown Voltage Temp. Coefficient	ΔBV <sub>Ds</sub> /ΔT <sub>j</sub>	—	0.6	—	V/°C	Reference to 25°C, I <sub>D</sub> =1mA
Gate Threshold Voltage	V <sub>GS(th)</sub>	2.0	—	4.0	V	V <sub>Ds</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA
Gate-Source Leakage Current	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> =±20V
Drain-Source Leakage Current (T <sub>j</sub> =25°C)	I <sub>DSS</sub>	—	—	100	uA	V <sub>Ds</sub> =600V, V <sub>GS</sub> =0
Drain-Source Leakage Current(T <sub>j</sub> =150°C)		—	—	500	uA	V <sub>Ds</sub> =480 V, V <sub>GS</sub> =0
Static Drain-Source On-Resistance	R <sub>Ds(ON)</sub>	—	7.2	8.0	Ω	V <sub>GS</sub> =10V, I <sub>D</sub> =0.8A
Forward Transconductance	G <sub>fs</sub>	—	0.8	—	S	V <sub>Ds</sub> =50V, I <sub>D</sub> =0.8A
Total Gate Charge <sup>3</sup>	Q <sub>g</sub>	—	7.7	—	nC	I <sub>D</sub> =1.6A V <sub>Ds</sub> =480V V <sub>GS</sub> = 10V
Gate-Source Charge	Q <sub>gs</sub>	—	1.5	—		
Gate-Drain ("Miller") Charge	Q <sub>gd</sub>	—	2.6	—		
Turn-on Delay Time <sup>3</sup>	T <sub>d(ON)</sub>	—	8	—	nS	V <sub>DD</sub> =300V I <sub>D</sub> =1.6A V <sub>GS</sub> =10V R <sub>G</sub> =10 Ω R <sub>D</sub> =187.5 Ω
Rise Time	T <sub>r</sub>	—	5	—		
Turn-off Delay Time	T <sub>d(off)</sub>	—	14	—		
Fall Time	T <sub>f</sub>	—	7	—		
Input Capacitance	C <sub>iss</sub>	—	286	—	pF	V <sub>GS</sub> =0V V <sub>Ds</sub> =25V f=1.0MHz
Output Capacitance	C <sub>oss</sub>	—	25	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	5	—		

**Source-Drain Diode**

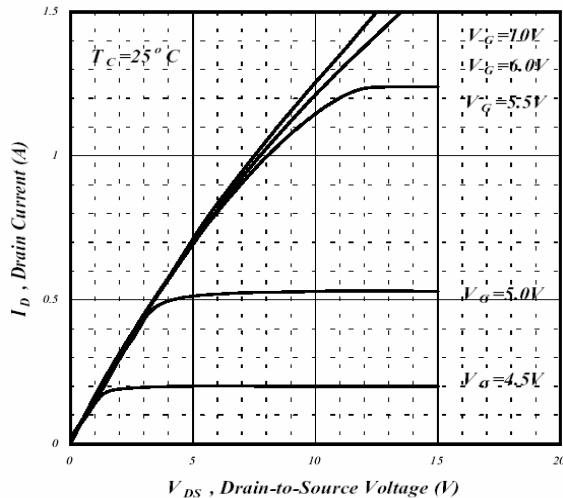
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Forward On Voltage <sup>3</sup>	V <sub>SD</sub>	—	—	1.5	V	I <sub>s</sub> =1.6A, V <sub>GS</sub> =0V, T <sub>j</sub> =25°C
Continuous Source Current(Body Diode)	I <sub>s</sub>	—	—	1.6	A	V <sub>D</sub> =V <sub>G</sub> =0V, V <sub>s</sub> =1.5 V
Pulsed Source Current(Body Diode) <sup>1</sup>	I <sub>SM</sub>	—	—	6	A	

Notes: 1. Pulse width limited by safe operating area.

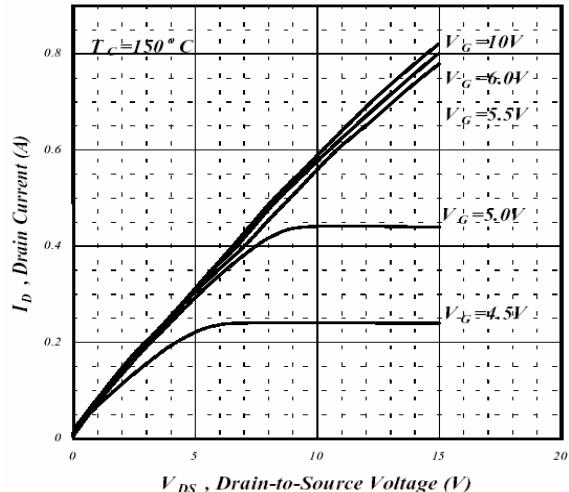
2. Staring T<sub>j</sub>=25°C, V<sub>DD</sub>=50V, L=10mH, R<sub>G</sub>=25Ω, I<sub>AS</sub>=1.6A.

3. Pulse width≤300us, duty cycle≤2%.

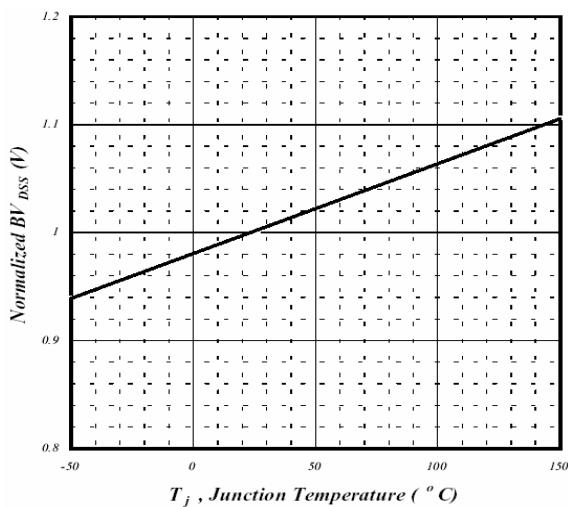
### Characteristics Curve



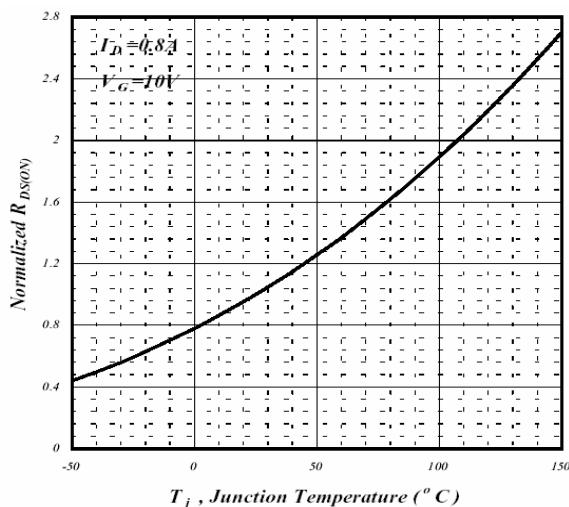
**Fig 1. Typical Output Characteristics**



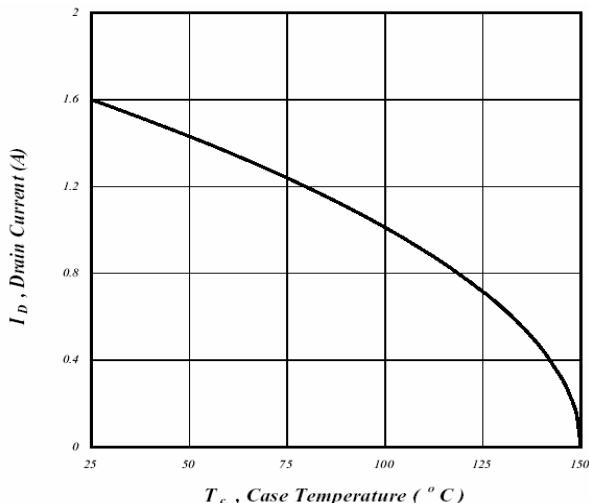
**Fig 2. Typical Output Characteristics**



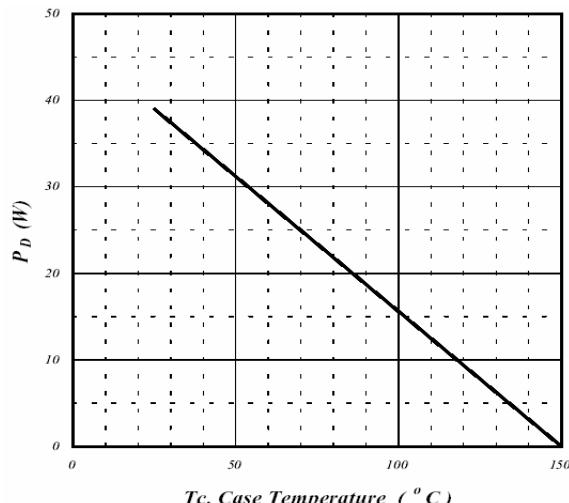
**Fig 3. Normalized  $BV_{DSS}$  v.s. Junction Temperature**



**Fig 4. Normalized On-Resistance v.s. Junction Temperature**



**Fig 5. Maximum Drain Current v.s. Case Temperature**



**Fig 6. Type Power Dissipation**

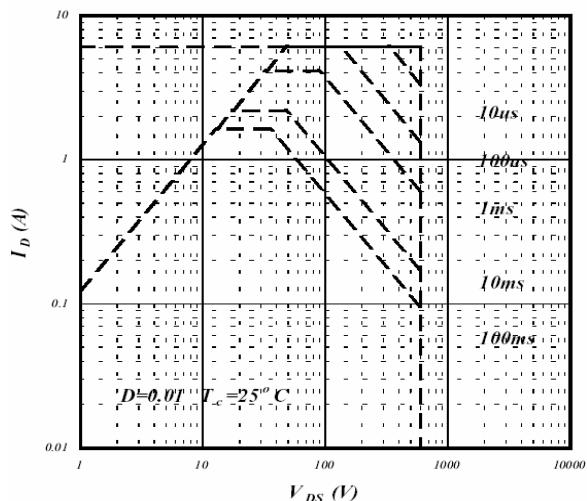
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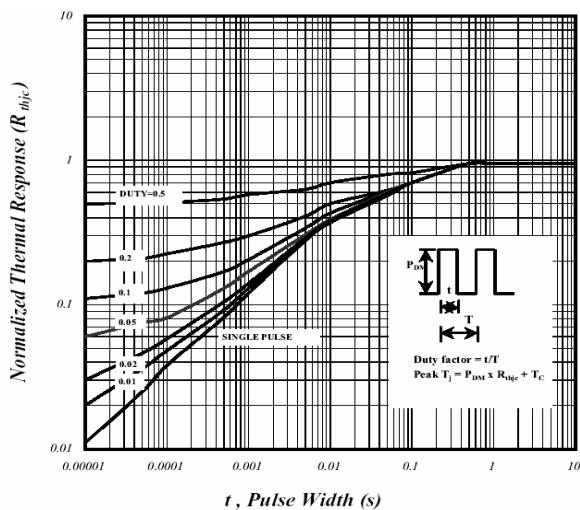
## SID01N60

1.6A, 600V, RDS(ON)8Ω

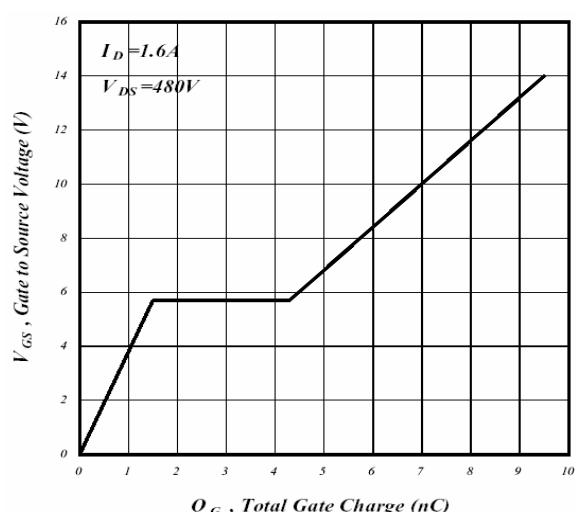
N-Channel Enhancement Mode Power Mos.FET



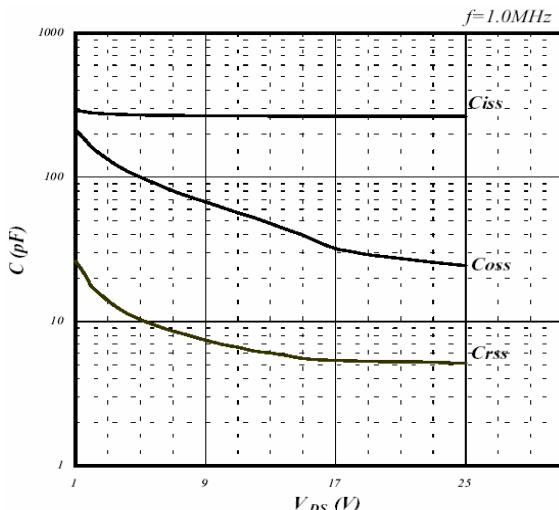
**Fig 7. Maximum Safe Operating Area**



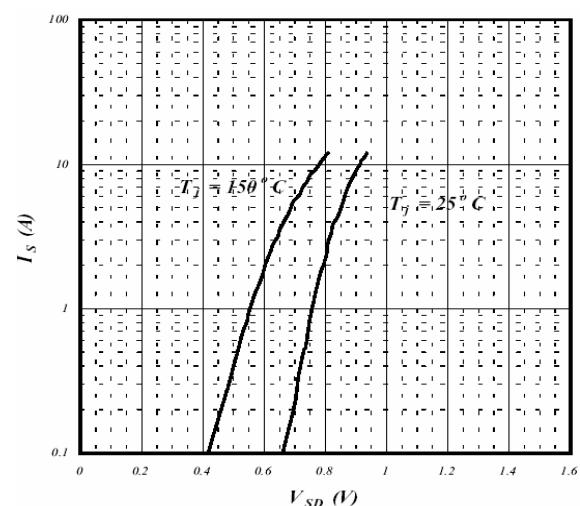
**Fig 8. Effective Transient Thermal Impedance**



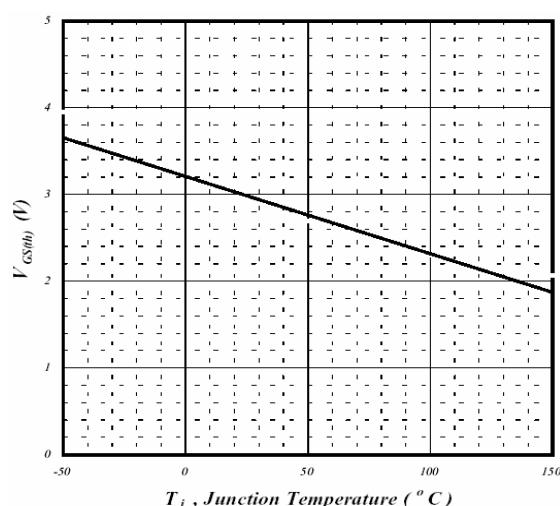
**Fig 9. Gate Charge Characteristics**



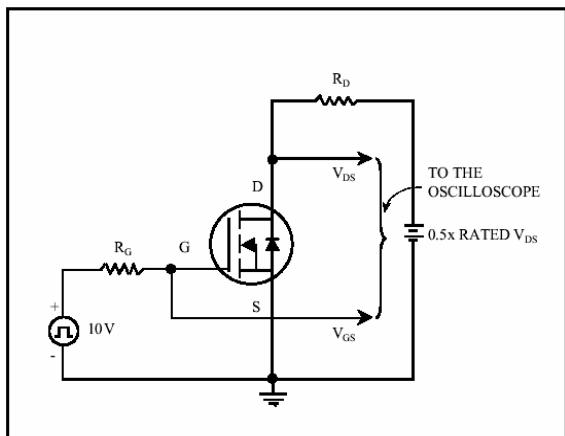
**Fig 10. Typical Capacitance Characteristics**



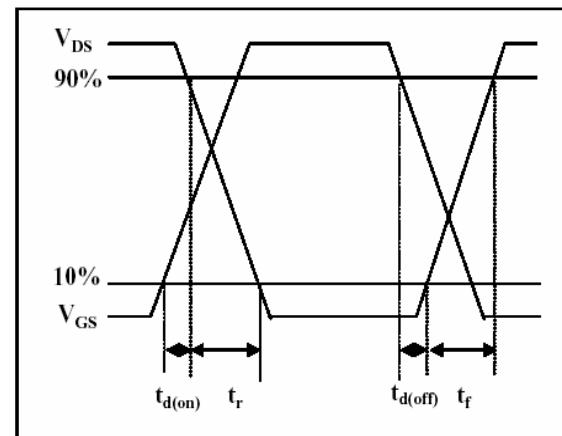
**Fig 11. Forward Characteristics of Reverse Diode**



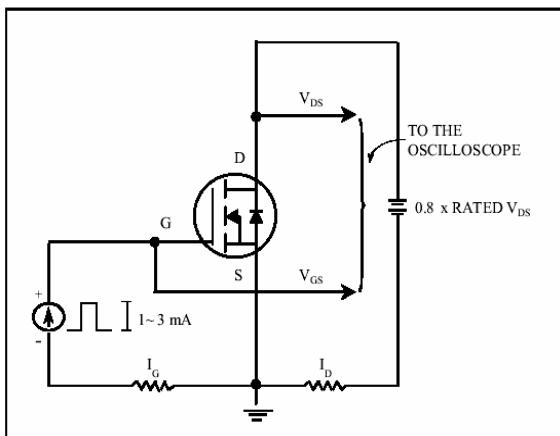
**Fig 12. Gate Threshold Voltage v.s. Junction Temperature**



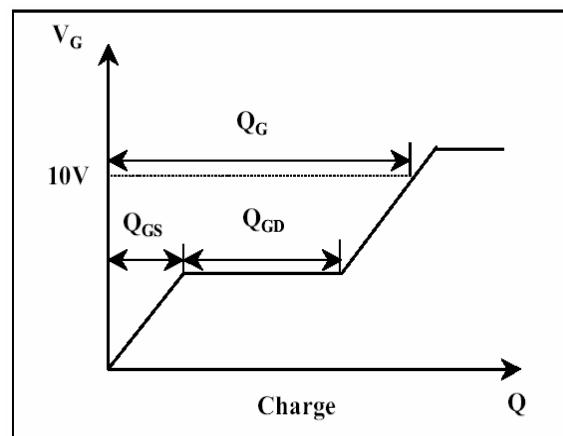
**Fig 13. Switching Time Circuit**



**Fig 14. Switching Time Waveform**



**Fig 15. Gate Charge Circuit**



**Fig 16. Gate Charge Waveform**