Complementary MOSFET

ELM34605AA-N

■General Description

ELM34605AA-N uses advanced trench technology to provide excellent Rds(on) and low gate charge.

■Features

N-channel	P-channel
• Vds=30V	Vds=-30V
• Id=7A	Id=-6A
• Rds(on) < 25mΩ(Vgs=10V)	Rds(on) < 35mΩ(Vgs=-10V)
• Rds(on) < 37mΩ(Vgs=4.5V)	Rds(on) < 60mΩ(Vgs=-4.5V)

■Maximum Absolute Ratings

Parameter		Symbol	N-ch (Max.)	P-ch (Max.)	Unit	Note
Drain-source voltage		Vds	30	-30	V	
Gate-source voltage		Vgs	±20	±20	V	
Continuous drain current	Ta=25℃	Id	7	-6	A	
	Ta=70℃		6	-5		
Pulsed drain current		Idm	20	-20	A	1
Power dissipation	Ta=25℃	Pd	2.0	2.0	W	
	Ta=70℃		1.3	1.3		
Junction and storage temperature range		Tj,Tstg	-55 to 150	-55 to 150	℃	

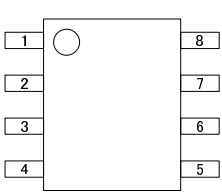
■Thermal Characteristics

Parameter		Symbol	Device	Typ.	Max.	Unit	Note
Maximum junction-to-ambient		Rθja	N-ch	48.0	62.5	℃/W	
Maximum junction-to-ambient		Rθja	P-ch	48.0	62.5	℃/W	

1. Pulse width limited by maximum junction temperature.
2. Duty cycle ≤ 1%.

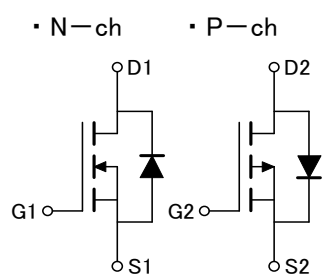
■Pin Configuration

SOP-8 (TOP VIEW)



Pin No.	Pin name
1	SOURCE1
2	GATE1
3	SOURCE2
4	GATE2
5	DRAIN2
6	DRAIN2
7	DRAIN1
8	DRAIN1

■Circuit



■Electrical Characteristics (N-ch)

Ta=25℃

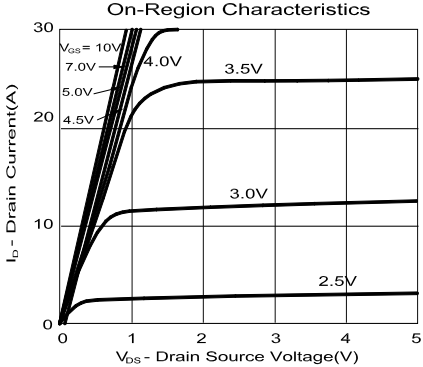
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BVdss	Id=250μA, Vgs=0V	30			V	
Zero gate voltage drain current	Idss	Vds=24V, Vgs=0V			1	μA	
		Vds=20V, Vgs=0V, Tj=55℃			10		
Gate-body leakage current	Igss	Vds=0V, Vgs=±20V			±100	nA	
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=250μA	1.0	1.5	2.5	V	
On state drain current	Id(on)	Vgs=10V, Vds=5V	20			A	1
Static drain-source on-resistance	Rds(on)	Vgs=10V, Id=7A		18	25	mΩ	1
		Vgs=4.5V, Id=6A		25	37		
Forward transconductance	Gfs	Vds=5V, Id=7A		19		S	1
Diode forward voltage	Vsd	If=1A, Vgs=0V			1	V	1
Max.body-diode continuous current	Is				1.3	A	
Pulsed current	Ism				2.6	A	3
DYNAMIC PARAMETERS							
Input capacitance	Ciss	Vgs=0V, Vds=10V, f=1MHz		790	988	pF	
Output capacitance	Coss			175	245	pF	
Reverse transfer capacitance	Crss			65	98	pF	
SWITCHING PARAMETERS							
Total gate charge	Qg	Vgs=10V, Vds=15V, Id=6A		16.0		nC	2
Gate-source charge	Qgs			2.5		nC	2
Gate-drain charge	Qgd			2.1		nC	2
Turn-on delay time	td(on)	Vgs=10V, Vds=10V, Id ≅ 1A Rgen=6Ω		2.2	4.4	ns	2
Turn-on rise time	tr			7.5	15.0	ns	2
Turn-off delay time	td(off)			11.8	21.3	ns	2
Turn-off fall time	tf			3.7	7.4	ns	2

NOTE :

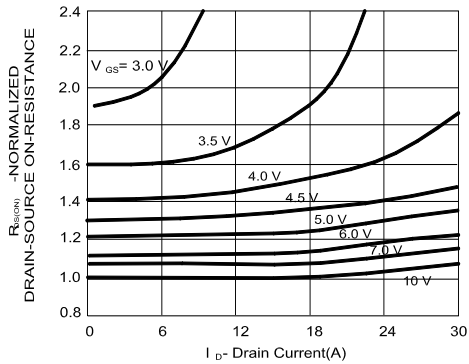
1. Pulse test : Pulse width ≤ 300μsec, duty cycle ≤ 2%.
2. Independent of operating temperature.
3. Pulse width limited by maximum junction temperature.

■Typical Electrical and Thermal Characteristics (N-ch)

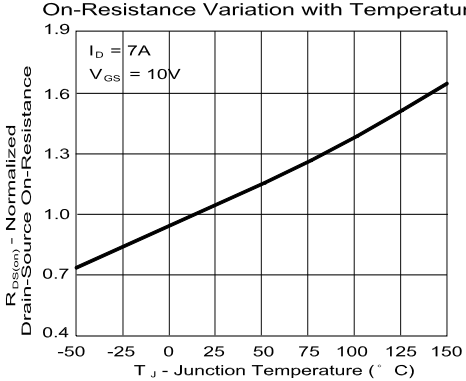
On-Region Characteristics



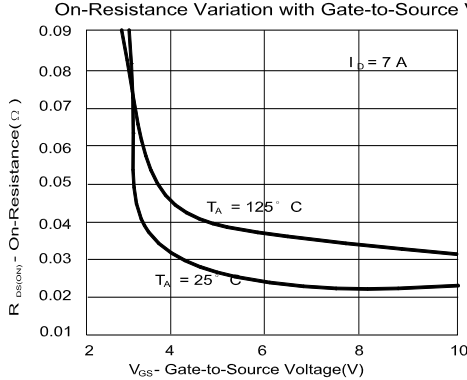
On-Resistance Variation with Drain Current and Gate Voltage



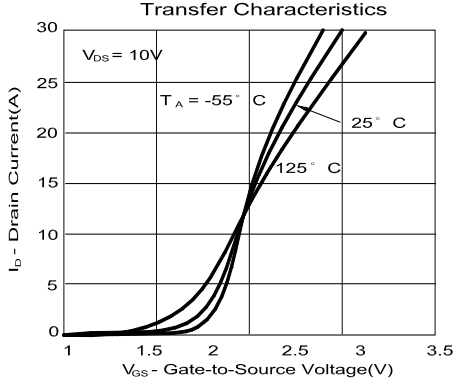
On-Resistance Variation with Temperature



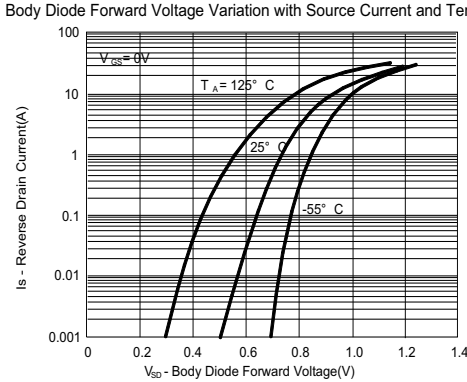
On-Resistance Variation with Gate-to-Source Voltage



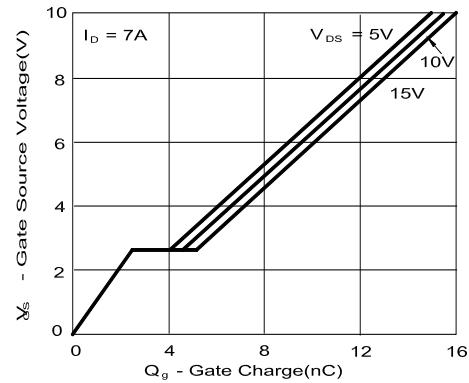
Transfer Characteristics



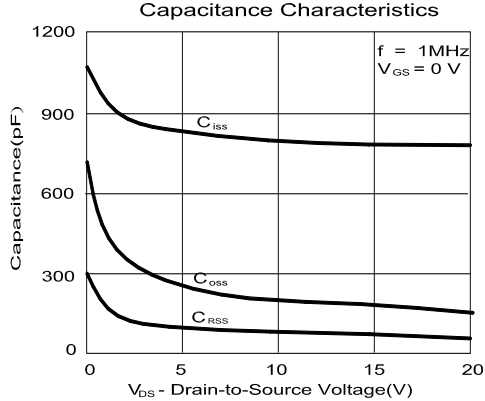
Body Diode Forward Voltage Variation with Source Current and Temperature



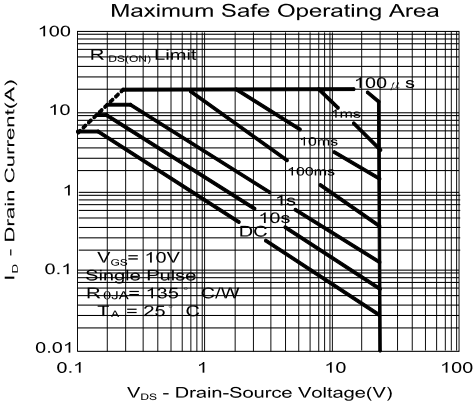
Gate Charge Characteristics



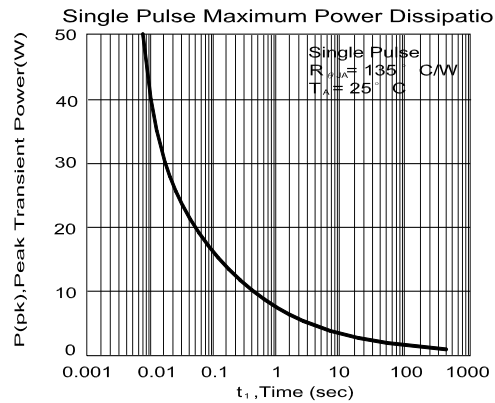
Capacitance Characteristics



Maximum Safe Operating Area



Single Pulse Maximum Power Dissipation



■Electrical Characteristics (P-ch)

Ta=25℃

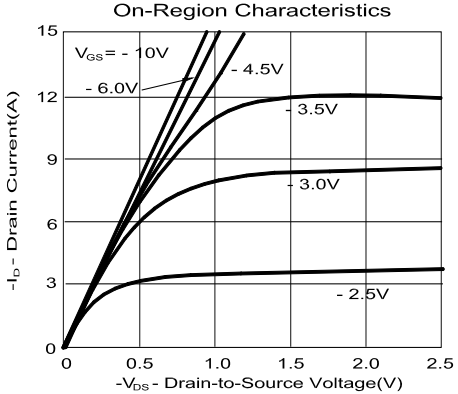
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BVdss	Id=-250μA, Vgs=0V	-30			V	
Zero gate voltage drain current	Idss	Vds=-24V, Vgs=0V			-1	μA	
		Vds=-20V, Vgs=0V, Tj=55℃			-10		
Gate-body leakage current	Igss	Vds=0V, Vgs=±20V			±100	nA	
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=-250μA	-1.0	-1.5	-2.5	V	
On state drain current	Id(on)	Vgs=-10V, Vds=-5V	-20			A	1
Static drain-source on-resistance	Rds(on)	Vgs=-10V, Id=-6A		28	35	mΩ	1
		Vgs=-4.5V, Id=-5A		44	60		
Forward transconductance	Gfs	Vds=-5V, Id=-5A		10		S	1
Diode forward voltage	Vsd	If=-1A, Vgs=0V			-1	V	1
Max.body-diode continuous current	Is				-1.3	A	
Pulsed current	Ism				-2.6	A	3
DYNAMIC PARAMETERS							
Input capacitance	Ciss	Vgs=0V, Vds=-10V, f=1MHz		970	1213	pF	
Output capacitance	Coss			370	520	pF	
Reverse transfer capacitance	Crss			180	270	pF	
SWITCHING PARAMETERS							
Total gate charge	Qg	Vgs=-10V, Vds=-15V Id=-5A		28		nC	2
Gate-source charge	Qgs			6		nC	2
Gate-drain charge	Qgd			12		nC	2
Turn-on delay time	td(on)	Vgs=-10V, Vds=-15V Id ≅ -1A, Rgen=6Ω		20		ns	2
Turn-on rise time	tr			17		ns	2
Turn-off delay time	td(off)			160		ns	2
Turn-off fall time	tf			75		ns	2

NOTE :

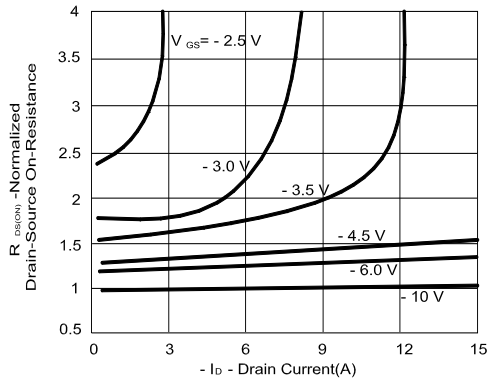
1. Pulse test : Pulse width ≤ 300μsec, duty cycle ≤ 2%.
2. Independent of operating temperature.
3. Pulse width limited by maximum junction temperature.

■Typical Electrical and Thermal Characteristics (P-ch)

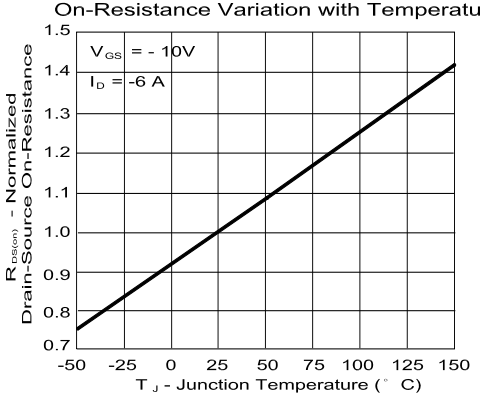
On-Region Characteristics



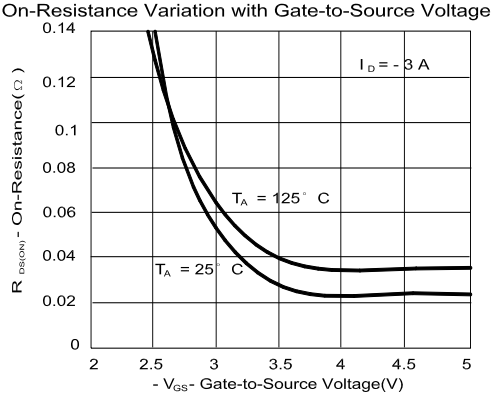
On-Resistance Variation with Drain Current and Gate Voltage



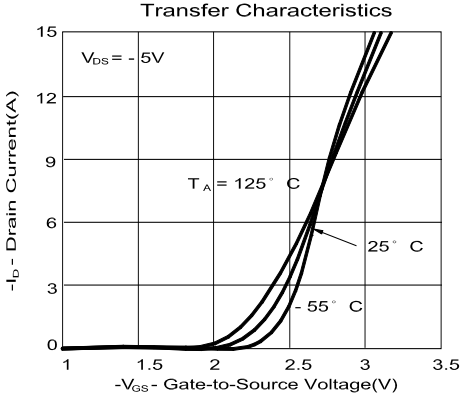
On-Resistance Variation with Temperature



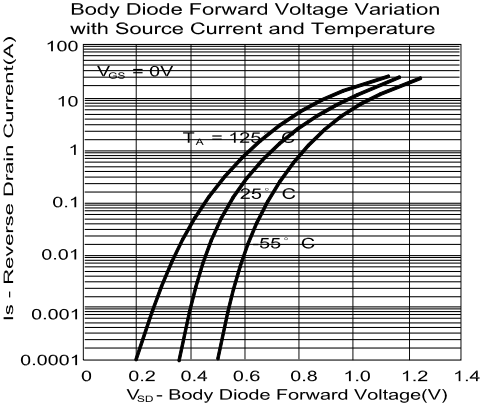
On-Resistance Variation with Gate-to-Source Voltage



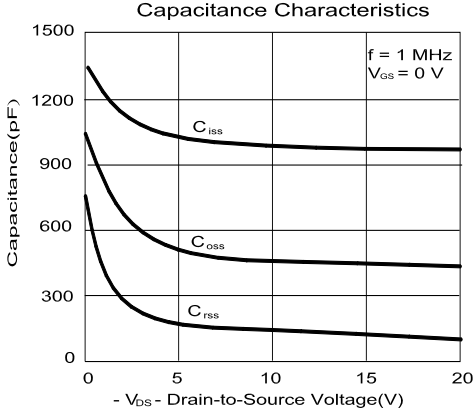
Transfer Characteristics



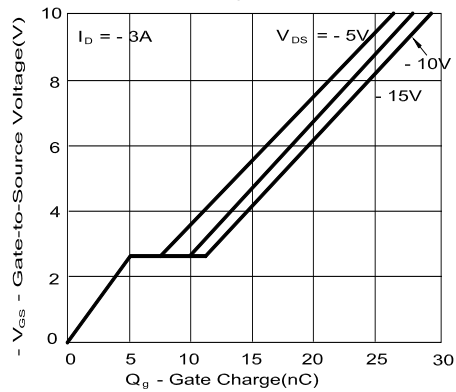
Body Diode Forward Voltage Variation
with Source Current and Temperature



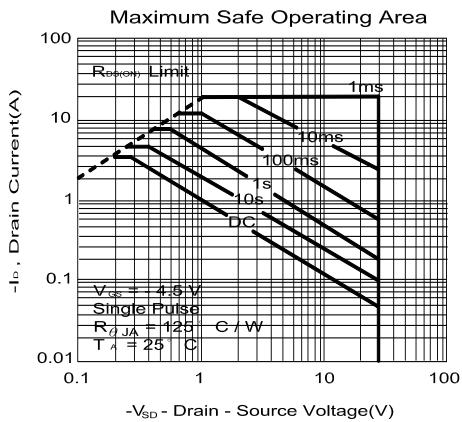
Capacitance Characteristics



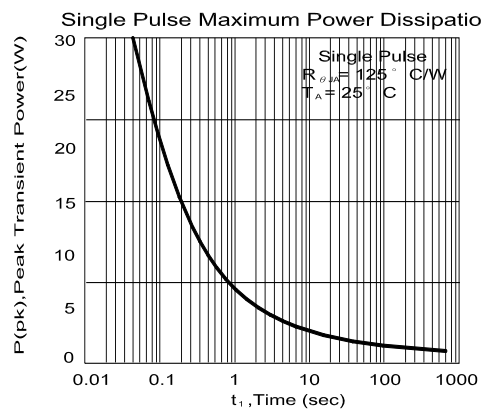
Gate Charge Characteristics



Maximum Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve