

TECHNICAL DATA  
DATA SHEET 681, REV. -

## HERMETIC POWER MOSFET N-CHANNEL

### FEATURES:

- 100 Volt, 0.092 Ohm, 18A MOSFET
- Isolated Hermetic Ceramic Package
- Fast Switching
- Low  $R_{DS(on)}$
- Electrically Equivalent to IRC140 Series

### MAXIMUM RATINGS

ALL RATINGS ARE AT  $T_C = 25^\circ\text{C}$  UNLESS OTHERWISE SPECIFIED.

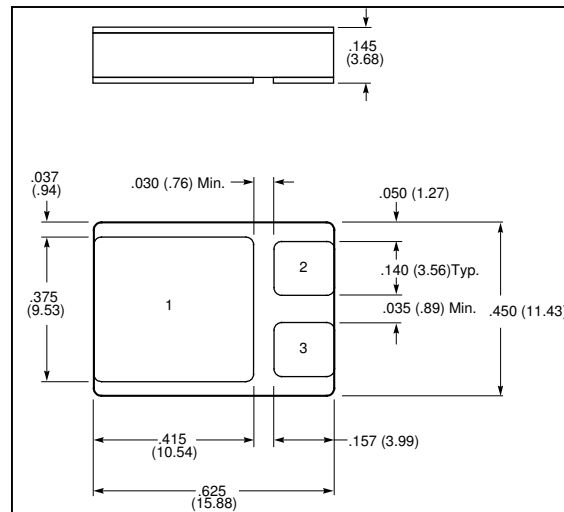
RATING	SYMBOL	MIN.	TYP.	MAX.	UNITS
GATE TO SOURCE VOLTAGE	$V_{GS}$	-	-	$\pm 20$	Volts
ON-STATE DRAIN CURRENT @ $T_C = 25^\circ\text{C}$ @ $T_C = 100^\circ\text{C}$	$I_D$	-	-	18 12	Amps
PULSED DRAIN CURRENT @ $T_C = 25^\circ\text{C}$	$I_{DM}$	-	-	72	Amps
OPERATING AND STORAGE TEMPERATURE	$T_{OP}/T_{STG}$	-55	-	+150	$^\circ\text{C}$
TOTAL DEVICE DISSIPATION @ $T_C = 25^\circ\text{C}$	$P_D$	-	-	160	Watts
THERMAL RESISTANCE, JUNCTION TO CASE	$R_{thJC}$	-	-	0.78	$^\circ\text{C}/\text{W}$

### ELECTRICAL CHARACTERISTICS

DRAIN TO SOURCE BREAKDOWN VOLTAGE $V_{GS} = 0\text{V}, I_D = 1.0\text{mA}$	$BV_{DSS}$	100	-	-	Volts
STATIC DRAIN TO SOURCE ON STATE RESISTANCE $V_{GS} = 10\text{V}, I_D = 12\text{A}$	$R_{DS(ON)}$	-	-	0.092	$\Omega$
GATE THRESHOLD VOLTAGE $V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	$V_{GS(th)}$	2.0	-	4.0	Volts
FORWARD TRANSCONDUCTANCE $V_{DS} \geq 15\text{V}, I_D = 12\text{A}$	$g_{fs}$	9.1	-	-	$\text{S}(1/\Omega)$
ZERO GATE VOLTAGE DRAIN CURRENT $V_{DS} = 0.8 \times \text{Max. Rating}, V_{GS} = 0\text{V}$ $V_{DS} = 0.8 \times \text{Max. Rating}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$	$I_{DSS}$	-	-	25 250	$\mu\text{A}$
GATE TO SOURCE LEAKAGE FORWARD $V_{GS} = 20\text{V}$ GATE TO SOURCE LEAKAGE REVERSE $V_{GS} = -20\text{V}$	$I_{GSS}$	-	-	100 -100	nA
TURN ON DELAY TIME RISE TIME TURN OFF DELAY TIME FALL TIME $V_{DD} = 50\text{V}, I_D = 18\text{A}, R_G = 9.1\Omega, V_{GS} = 10\text{V}$	$t_{d(ON)}$ $t_r$ $t_{d(OFF)}$ $t_f$	-	-	21 145 64 105	nsec
DIODE FORWARD VOLTAGE $T_C = 25^\circ\text{C}, I_S = 18\text{A}, V_{GS} = 0\text{V}$	$V_{SD}$	-	-	1.5	Volts
REVERSE RECOVERY TIME $T_J = 25^\circ\text{C}, I_S = 18\text{A}, di/ds = 100\text{A}/\mu\text{sec}, V_{DD} \leq 50\text{V}$	$t_{rr}$	-	-	400	nsec
INPUT CAPACITANCE OUTPUT CAPACITANCE REVERSE TRANSFER CAPACITANCE $V_{GS} = 0\text{V}$ $V_{DS} = 25\text{V}$ $f = 1.0\text{MHz}$	$C_{iss}$ $C_{oss}$ $C_{rss}$	-	1600 550 120	-	pF

**SENSITRON**  
**DATA SHEET 681**  
**REVISION -**

**MECHANICAL DIMENSIONS: in Inches / mm**



**LCC-3P**

**PINOUT TABLE**

<b>DEVICE TYPE</b>	<b>PIN 1</b>	<b>PIN 2</b>	<b>PIN 3</b>
MOSFET LCC-3P PACKAGE	DRAIN	SOURCE	GATE

**TECHNICAL DATA**

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