



PJ6A6.8FU

QUAD ARRAY FOR ESD PROTECTION

FEATURES

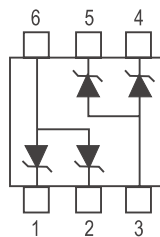
- Low Leakage < 1 μ A@5Volts
- Breakdown Voltage : 6.4-7.2 Volt@5mA
- Low Capacitance (40pF typical)
- ESD Protection Meeting IEC61000-4-2 Level 4
- In compliance with EU RoHS 2002/95/EC directives

MECHANICAL DATA

Case : SOT-363, Plastic

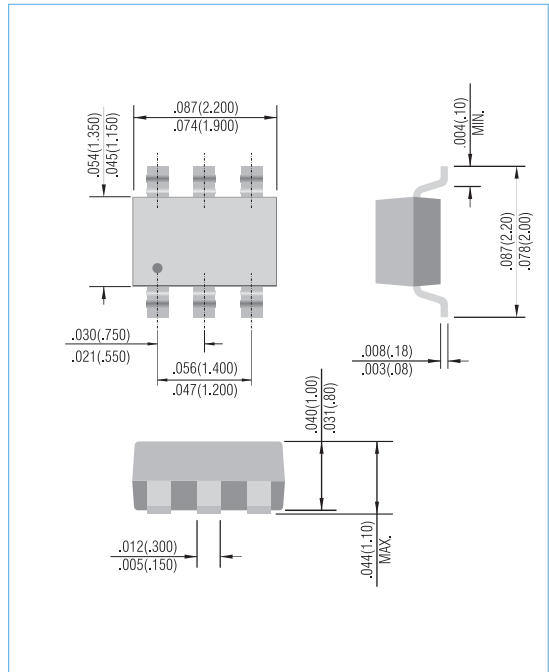
Terminals : Solderable per MIL-STD-750, Method 2026

Approx. Weight : 0.006gram



SOT-363

Unit: inch (mm)



MAXIMUM RATINGS (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	VALUE	UNIT
Peak Power Dissipation@8x20 μ s (Note 1)	P _{PK}	75	W
Steady State Power Dissipation (Note 2)	P _D	385	mW
Thermal Resistance-Junction-to-Ambient Derate Above 25°C	R _{θJA}	328 3.0	°C/W mW/°C
Lead Soldering Temperature (10 seconds duration)	T _L	260	°C
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 to + 150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

Notes : 1.Per Waveform Figure 1.

2.Mounted on FR-4 Board = 70 x 60 x 1mm



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ELECTRICAL CHARACTERISTICS

Part Number	Breakdown Voltage $V_{BR}@5mA$			Leakage Current $I_{RM}@V_{RM}$		MAX $V_F@I_F=10mA$	MAX $Z_z@5mA$	MAX $Z_z@0.5mA$	Typ Capacitance @0V Bias	Marking
	Min	Nom	Max	V_{RWM}	I_{RWM}					
	V	V	V	V	μA					
PJ6A6.8FU	6.4	6.8	7.2	5	1.0	1.25	30	300	40	FUT

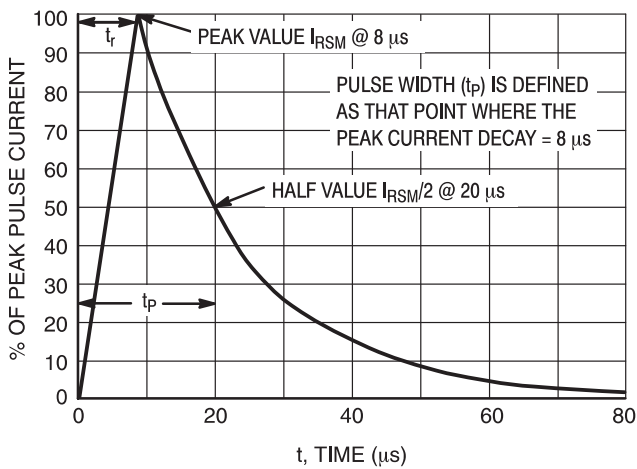


Figure 1. $8 \times 20 \mu s$ Pulse Waveform

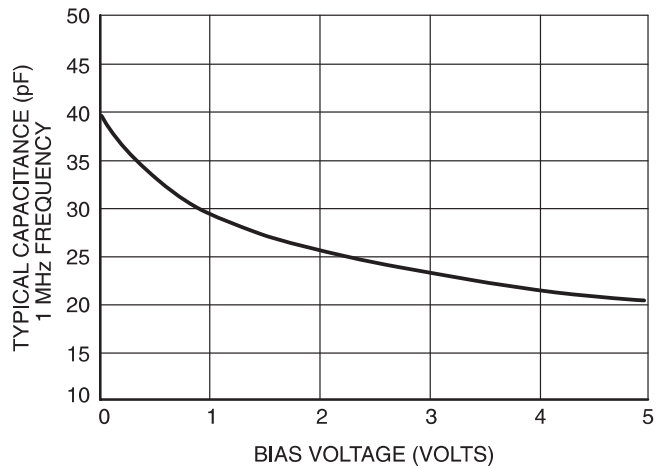


Figure 2. Capacitance

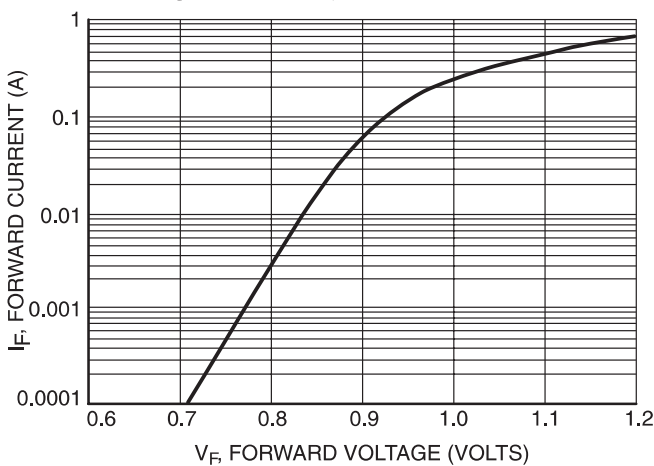


Figure 3. Forward Voltage

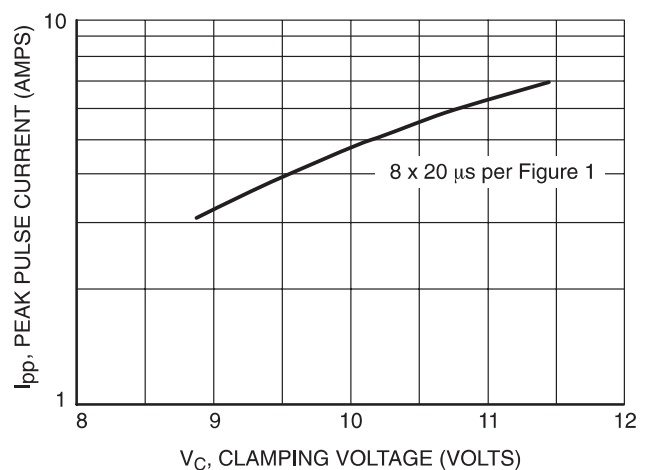
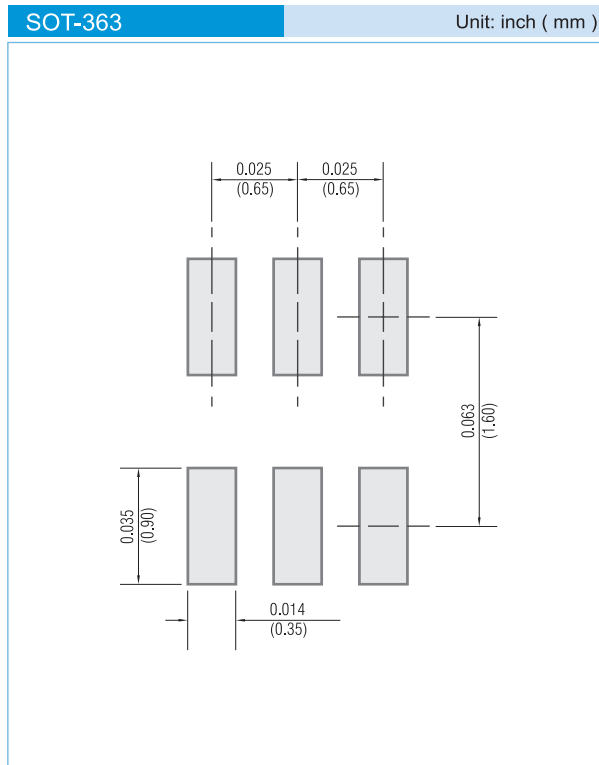


Figure 4. Clamping Voltage versus Peak Pulse Current



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MOUNTING PAD LAYOUT



ORDER INFORMATION

- Packing information
 - T/R - 10K per 13" plastic Reel
 - T/R - 3K per 7" plastic Reel

LEGAL STATEMENT

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