

* SLAVE, Z = HIGH IMPEDANCE

(V_{DD} = +5 V)

| PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL | PIN NO. | I/O | SIGNAL |
|---------|--------|-----------------|---------|-----|-----------------|---------|----------|-----------------|---------|--------|-----------------|
| 1 | — | NC | 53 | — | NC | 105 | — | NC | 157 | — | NC |
| 2 | — | NC | 54 | — | NC | 106 | — | NC | 158 | — | NC |
| 3 | — | GND | 55 | O | MASTER | 107 | I/O | D0 | 159 | I/O | ATN |
| 4 | * I, O | A10 | 56 | — | V _{DD} | 108 | I/O | DP0 | 160 | I/O | SDP0 |
| 5 | * I, O | A11 | 57 | O | TIP | 109 | I | DLE | 161 | I/O | SD7 |
| 6 | * I, O | A12 | 58 | I | BCLK | 110 | * Z, O | SC0 | 162 | I/O | SD6 |
| 7 | * I, O | A13 | 59 | I/O | D31 | 111 | * Z, O | SC1 | 163 | — | GND |
| 8 | * I, O | A14 | 60 | I/O | D30 | 112 | I | BS0 | 164 | I/O | SD5 |
| 9 | — | GND | 61 | I/O | D29 | 113 | I | BS1 | 165 | I/O | SD4 |
| 10 | * I, O | A15 | 62 | I/O | D28 | 114 | I | BS2 | 166 | I/O | SD3 |
| 11 | * I, O | A16 | 63 | — | GND | 115 | O | READYO | 167 | I/O | SD2 |
| 12 | * I, O | A17 | 64 | I/O | D27 | 116 | I | CS | 168 | — | GND |
| 13 | — | V _{DD} | 65 | I/O | D26 | 117 | I | RESET | 169 | I/O | SD1 |
| 14 | * I, O | A18 | 66 | — | V _{DD} | 118 | I | BOFF | 170 | I/O | SD0 |
| 15 | * I, O | A19 | 67 | I/O | D25 | 119 | — | GND | 171 | I/O | SDP1 |
| 16 | — | GND | 68 | I/O | D24 | 120 | O | FETCH | 172 | I/O | SD15 |
| 17 | * I, O | A20 | 69 | I/O | DP3/ABRT | 121 | * Z, I/O | BB | 173 | — | GND |
| 18 | * I, O | A21 | 70 | I/O | D23 | 122 | — | V _{DD} | 174 | I/O | SD14 |
| 19 | * I, O | A22 | 71 | — | GND | 123 | I | HLDAl | 175 | I/O | SD13 |
| 20 | * I, O | A23 | 72 | I/O | D22 | 124 | O | HOLD | 176 | I/O | SD12 |
| 21 | * I, O | A24 | 73 | I/O | D21 | 125 | I | SCLK | 177 | O | SDIRP0 |
| 22 | — | GND | 74 | I/O | D20 | 126 | O | IRQ | 178 | O | SDIR7 |
| 23 | * I, O | A25 | 75 | I/O | D19 | 127 | I | AUTO | 179 | O | SDIR6 |
| 24 | * I, O | A26 | 76 | I/O | D18 | 128 | I | DIFFSENS | 180 | O | SDIR5 |
| 25 | * I, O | A27 | 77 | I/O | D17 | 129 | O | TGS | 181 | — | GND |
| 26 | O | MAC | 78 | O | TSTOUT | 130 | — | V _{DD} | 182 | I | TSTIN |
| 27 | — | V _{DD} | 79 | — | GND | 131 | O | IGS | 183 | O | SDIR4 |
| 28 | * I, O | A28 | 80 | I/O | D16 | 132 | O | SELDIR | 184 | O | SDIR3 |
| 29 | * I, O | A29 | 81 | I/O | DP2 | 133 | O | RSTDIR | 185 | O | SDIR2 |
| 30 | — | GND | 82 | — | V _{DD} | 134 | O | BSYDIR | 186 | — | V _{DD} |
| 31 | * I, O | A30 | 83 | I/O | D15 | 135 | — | GND | 187 | O | SDIR1 |
| 32 | * I, O | A31 | 84 | I/O | D14 | 136 | O | SDIR11 | 188 | O | SDIR0 |
| 33 | * I, O | ADS | 85 | I/O | D13 | 137 | O | SDIR10 | 189 | O | SDIRP1 |
| 34 | * I, O | BE3 | 86 | I/O | D12 | 138 | O | SDIR9 | 190 | — | GND |
| 35 | * I, O | BE2 | 87 | — | GND | 139 | O | SDIR8 | 191 | O | SDIR15 |
| 36 | * Z, O | FC2 | 88 | I/O | D11 | 140 | I/O | SD11 | 192 | O | SDIR14 |
| 37 | * Z, O | FC1 | 89 | I/O | D10 | 141 | I/O | SD10 | 193 | O | SDIR13 |
| 38 | * Z, O | FC0 | 90 | I/O | D9 | 142 | I/O | SD9 | 194 | O | SDIR12 |
| 39 | I | GPI0 | 91 | I/O | D8 | 143 | — | GND | 195 | * I, O | BE0 |
| 40 | I | GPI1 | 92 | I/O | DP1 | 144 | I/O | SD8 | 196 | * I, O | BE1 |
| 41 | I | GPI2 | 93 | — | GND | 145 | I/O | I/O | 197 | * I, O | A2 |
| 42 | I | GPI3 | 94 | I/O | D7 | 146 | I/O | REQ | 198 | * I, O | A3 |
| 43 | O | GPO | 95 | I/O | D6 | 147 | I/O | C/D | 199 | * I, O | A4 |
| 44 | — | GND | 96 | I/O | D5 | 148 | — | GND | 200 | — | GND |
| 45 | * I, O | W/R | 97 | — | V _{DD} | 149 | I/O | SEL | 201 | * I, O | A5 |
| 46 | * Z, O | TT1 | 98 | I/O | D4 | 150 | I/O | MSG | 202 | * I, O | A6 |
| 47 | * Z, O | TT0 | 99 | I/O | D3 | 151 | I/O | RST | 203 | * I, O | A7 |
| 48 | * O, I | TBI | 100 | — | GND | 152 | I/O | ACK | 204 | — | V _{DD} |
| 49 | I | READY1 | 101 | I/O | D2 | 153 | I/O | BSY | 205 | * I, O | A8 |
| 50 | * O, I | TEA | 102 | I/O | D1 | 154 | — | GND | 206 | * I, O | A9 |
| 51 | — | NC | 103 | — | NC | 155 | — | NC | 207 | — | NC |
| 52 | — | NC | 104 | — | NC | 156 | — | NC | 208 | — | NC |

INPUT

| | |
|---------------------------------------|------------------------------|
| <u>AUTO</u> | : SCRIPTS AUTO-START MODE |
| <u>BCLK</u> | : BUS CLOCK |
| <u>BOFF</u> | : BACK OFF |
| <u>BS0 - BS2</u> | : BUS MODE SELECT |
| <u>CS</u> | : CHIP SELECT |
| <u>DLE</u> | : DATA LATCH ENABLE |
| <u>DIFFSENS</u> | : DIFFERENTIAL SENSE |
| <u>GPI0 - GPI3</u> | : GENERAL PURPOSE |
| <u>HLD\overline{A}</u> | : HOLD ACKNOWLEDGE |
| <u>READY\overline{I}</u> | : READY |
| <u>RESET</u> | : CHIP RESET |
| <u>SCLK</u> | : SCSI CLOCK |
| <u>TBI</u> | : TRANSFER BURST INHIBIT |
| <u>TEA</u> | : TRANSFER ERROR ACKNOWLEDGE |
| <u>TSTIN</u> | : TEST |

OUTPUT

| | |
|-----------------------|---|
| <u>A2 - A31</u> | : ADDRESS BUS |
| <u>ADS</u> | : ADDRESS STROBE |
| <u>BE0 - BE3</u> | : BYTE ENABLE |
| <u>BSYDIR</u> | : DRIVER ENABLE CONTROL FOR SCSI BSY SIGNAL |
| <u>FC0 - FC2</u> | : FUNCTION CODES TRANSFER MODIFIER |
| <u>FETCH</u> | : FETCHING OP CODE |
| <u>GPO</u> | : GENERAL PURPOSE |
| <u>HOLD</u> | : HOLD |
| <u>IGS</u> | : INITIATOR DRIVER DIRECT CONTROL |
| <u>IRQ</u> | : INTERRUPT REQUEST |
| <u>MAC</u> | : MEMORY ACCESS CONTROL |
| <u>MASTER</u> | : MASTER STATUS |
| <u>READYO</u> | : READY |
| <u>RSTDIR</u> | : DRIVER ENABLE CONTROL FOR SCSI RST SIGNAL |
| <u>SC0, SC1</u> | : SNOOP CONTROL |
| <u>SDIR0 - SDIR15</u> | : DRIVER DIRECTION CONTROL FOR SCSI DATA LINES |
| <u>SDIRP0, SDIRP1</u> | : DRIVER DIRECTION CONTROL FOR SCSI PARITY SIGNAL |
| <u>SELDIR</u> | : DRIVER ENABLE CONTROL FOR SCSI SEL SIGNAL |
| <u>TGS</u> | : TRIGGER DRIVER DIRECT CONTROL |
| <u>TIP</u> | : TRANSFER IN PROGRESS |
| <u>TSTOUT</u> | : TEST |
| <u>TT0, TT1</u> | : TRANSFER TYPE |
| <u>W/R</u> | : READ/WRITE |

INPUT/OUTPUT

| | |
|-------------------|---|
| <u>ACK</u> | : DATA HANDSHAKE SIGNAL |
| <u>ATN</u> | : ATTENTION |
| <u>BB</u> | : BUS BUSY |
| <u>BSY</u> | : SCSI BUS ARBITRATION SIGNAL |
| <u>C/D</u> | : SCSI PHASE LINE COMMUNICATION DATA |
| <u>D0 - D31</u> | : HOST DATA BUS |
| <u>DP0 - DP2</u> | : HOST BUS DATA PARITY |
| <u>DP3/ABRT</u> | : HOST BUS DATA PARITY |
| <u>MSG</u> | : SCSI PHASE LINE MESSAGE |
| <u>REQ</u> | : DATA HANDSHAKE SIGNAL |
| <u>RST</u> | : SCSI BUS RESET |
| <u>SD0 - SD15</u> | : 16-BIT SCSI DATA BUS |
| <u>SDP0, SDP1</u> | : SCSI DATA PARITY BIT |
| <u>SEL</u> | : SCSI BUS ARBITRATION SIGNAL SELECT DEVICE |