



MULTI-RATE 10-GBPS SONET/SDH/10GE/FEC DEMUX

FEATURES

- 10-Gigabit MSA (Multi-Source Agreement) compliant
- Fully integrated CDR and demultiplexer with limiting amplifier
- 1:16 DEMUX with LVDS data outputs
- Phase adjustment (Typical 35 ps range with 2.5ps granularity)
- Decision Threshold Level adjustment
- Serial data polarity invert
- Bit order reversal
- Lock detect
- Exceeds SONET jitter requirements
- Core power supply: 1.8V
- I/O power supply: CML and LVDS at 1.8V, CMOS at 1.8V or 3.3V
- Power consumption: 950 mW
- Standard CMOS fabrication process
- 143-pin BGA package

SUMMARY OF BENEFITS

- Compliant with Optical Internetworking Forum (OIF), Telcordia, ITU-T, ANSI, and IEEE 802.3ae industry standards.
- Reduces design cycle and time to market.
- High level of integration allows for higher port density solutions.
- Uses the most effective silicon economy of scale for CMOS-based devices.
- Low-power consumption eliminates the need for external cooling sources.

APPLICATIONS

- OC-192/STM-64/10GE transmission equipment
- SONET/SDH optical modules
- ADD/DROP multiplexers
- Digital cross-connects
- ATM switch backbones
- SONET test equipment
- Terabit routers
- Edge routers

BCM8129 Functional Block Diagram



