### 4 Channel EMI Filter Network

#### **Features**

- Functionally and pin compatible with CSPRC032A
- Optiguard<sup>™</sup> coated for improved reliability at assembly
- 4 EMI filter lines per device
- Filters attenuate to -30dB at 3GHz
- CSP package minimizes cross-talk
- 9-bump 2.485mm X 0.985mm Chip Scale Package (CSP), 0.5mm pitch
- 0.30mm Eutectic solder bumps
- Ultra small foot print suitable for portable devices
- Silicon substrate
- Lead-free version available

## **Applications**

- EMI filtering for RF sections of wireless devices
- Cellular phones
- Cordless phones
- Internet appliances
- **PDAs**
- Laptop computers

## **Product Description**

The CM1300 is a 4-channel low pass EMI filter (R-C-R configuration) in a Chip Scale Package (CSP). Many portable applications require the attenuation of signals in the 800-3000 MHz band. California Micro Devices' unique thin film technology provides a minimum of -25dB of attenuation over this frequency band.

The bump size and pitch of these filters are selected such that the device can be placed directly on a FR4 printed circuit board using conventional assembly techniques. The pin-out for the device features a signal 'flow through' design, allowing optimal signal routing. The solder bump contacts are a 63/37 Sn/Pb alloy (Sn/ Ag/Cu for lead-free finish) and are 0.30 mm in diameter.

The CM1300 incorporates Optiguard™ coating which results in improved reliability at assembly. The device is available in a space-saving, low-profile Chip Scale Package with optional lead-free finishing.

#### **Electrical Schematic**

A1 
$$50\Omega$$
  $50\Omega$  B1

R  $C$  R

 $43pF$ 

GND

B2

GND

B3

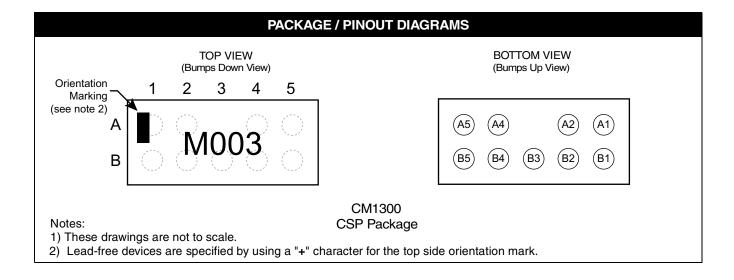
GND

B4

 $C$  R

 $C$  R

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## **Ordering Information**

PART NUMBERING INFORMATION								
			Standar	rd Finish	Lead-free Finish <sup>2</sup>			
			Ordering Part		Ordering Part			
	Bumps	Package	Number <sup>1</sup>	Part Marking	Number <sup>1</sup>	Part Marking		
	9	CSP	CM1300-03CS	M003	CM1300-03CP	M003		

Note 1: Parts are shipped in Tape & Reel form unless otherwise specified.

Note 2: Lead-free devices are specified by using a "+" character for the top side orientation mark.

# **Specifications**

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	RATING	UNITS				
Storage Temperature Range	-55 to +150	°C				
Power Rating per Resistor	25	mW				

STANDARD OPERATING CONDITIONS						
PARAMETER	RATING	UNITS				
Operating Temperature Range	-40 to +85	°C				

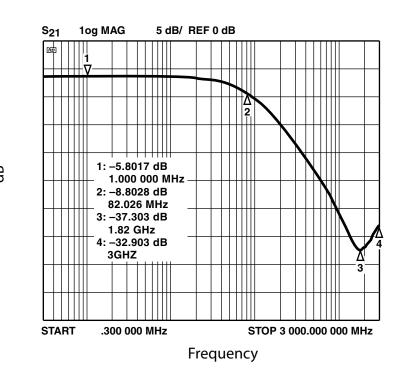
ELECTRICAL OPERATING CHARACTERISTICS <sup>1</sup>									
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS			
I <sub>LEAK</sub>	Leakage Current, An or Bn to GND	V <sub>IN</sub> =6.0V			1	μΑ			
R	Resistance		45	50	55	Ω			
С	Capacitance		34	43	52	pF			
TOLR	Resistor Absolute Tolerance	$R = 50\Omega$			<u>+</u> 10	%			
TOL <sub>C</sub>	Capacitor Absolute Tolerance	C=43pF			<u>+</u> 20	%			
TCR	Temperature Coefficient of Resistance				<u>+</u> 150	ppm/°C			
TCC	Temperature Coefficient of Capacitance				<u>+</u> 500	ppm/°C			
F <sub>C</sub>	Filter Cutoff Frequency $ Z_{SOURCE} = 0 \Omega Z_{LOAD} = \infty $ $ Z_{SOURCE} = 50 \Omega Z_{LOAD} = 50 \Omega $	R=50Ω C=43pF;		74 82		MHz MHz			

Note 1: Electrical Operating Characteristics are guaranteed over the Operating Temperature Range unless otherwise specified.

### **Filter Performance**

#### CM1213 Filter Typical Measured Frequency Response (S21) Measurement

The measurement is done with  $50\Omega$ -source and  $50\Omega$ -load impedance using a HP8753C Network Analyzer with a HP85047A S-parameter Test Set.



# **Application Information**

Refer to Application Note AP-217, "The Chip Scale Package", for a detailed description of Chip Scale Packages offered by California Micro Devices.

PRINTED CIRCUIT BOARD RECOMMENDATIONS						
PARAMETER	VALUE					
Pad Size on PCB	0.275mm					
Pad Shape	Round					
Pad Definition	Non-Solder Mask defined pads					
Solder Mask Opening	0.325mm Round					
Solder Stencil Thickness	0.125 - 0.150mm					
Solder Stencil Aperture Opening (laser cut, 5% tapered walls)	0.330mm Round					
Solder Flux Ratio	50/50 by volume					
Solder Paste Type	No Clean					
Pad Protective Finish	OSP (Entek Cu Plus 106A)					
Tolerance — Edge To Corner Ball	<u>+</u> 50μm					
Solder Ball Side Coplanarity	<u>+</u> 20μm					
Maximum Dwell Time Above Liquidous	60 seconds					
Soldering Maximum Temperature	260°C					

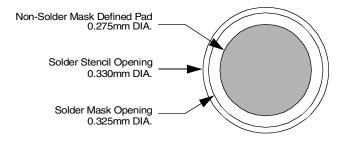


Figure 1. Recommended Non-Solder Mask Defined Pad Illustration

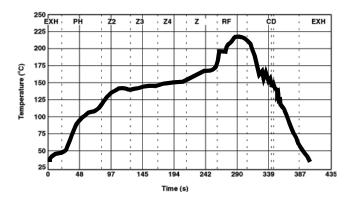


Figure 2. Eutectic (SnPb) Solder Ball Reflow Profile

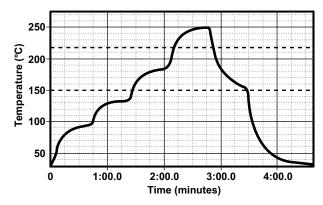


Figure 3. Lead-free (SnAgCu) Solder **Ball Reflow Profile** 

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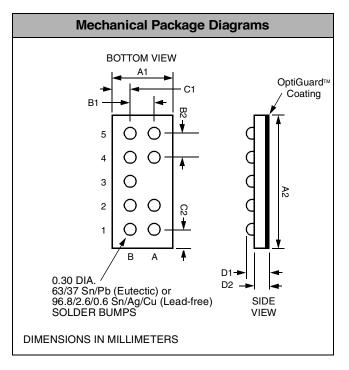
### **Mechanical Details**

The CM1300 devices are packaged in custom Chip Scale Packages (CSP).

#### CM1300 9-bump CSP Mechanical Specifications

The CM1300 devices are packaged in a 9-bump custom Chip Scale Package (CSP). Dimensions are presented below.

PACKAGE DIMENSIONS								
Pac	ckage	Custom CSP						
Bumps		9						
Dim	IV	Millimeters Inches						
Dilli	Min	Nom	Max	Min	Nom	Max		
A1	0.940	0.985	1.030	0.0370	0.0388	0.0406		
A2	2.440	2.485	2.530	0.0961	0.0978	0.0996		
B1	0.495	0.500	0.505	0.0195	0.0197	0.0199		
B2	0.495	0.500	0.505	0.0195	0.0197	0.0199		
C1	0.1925	0.2425	0.2925	0.0076	0.0095	0.0115		
C2	0.1925	0.2425	0.2925	0.0076	0.0095	0.0115		
D1	0.600	0.670	0.739	0.0236	0.0264	0.0291		
D2	0.394	0.445	0.495	0.0155	0.0175	0.0195		
# per tape and reel		3500 pieces						
	Controlling dimension: millimeters							



Package Dimensions for CM1300 9-bump Chip Scale Package

#### **CSP Tape and Reel Specifications**

PART NUMBER	PKG. SIZE (mm)	POCKET SIZE (mm) B <sub>0</sub> X A <sub>0</sub> X K <sub>0</sub>	TAPE WIDTH W	REEL DIA.	QTY PER REEL	P <sub>0</sub>	P <sub>1</sub>
CM1300	2.485 X 0.985 X 0.695	2.62 X 1.12 X 0.762	8mm	178mm (7")	3500	4mm	4mm

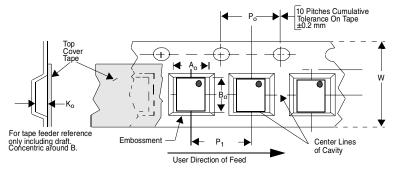


Figure 4. Tape and Reel Mechanical Data