

SERIES 626 10-TAPS 30 PIN H-CMOS DIGITAL DELAY MODULES-REDUCED SIZE

- 30 Pin Chip Carrier • Designed For Surface Mounting • H-Cmos Logic
- Low Profile • 10 Equally Spaced Taps

Specifications:

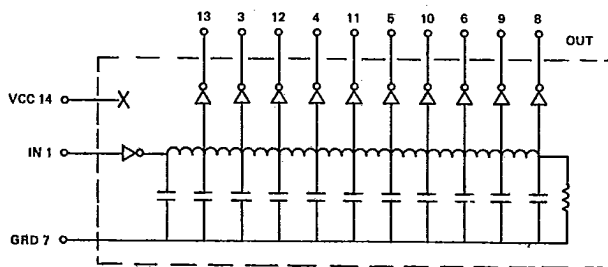
- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Voltage : 3.2v
- Logic 0 Input Voltage : 0.9v
- Logic 1 Output Voltage : 4.5v
- Logic 0 Output Voltage : 0.1v
- Input Current : 1μA Max
- Supply Current : ICCH = 5mA, ICCL = 20μA
- Fan-Out : 10 LSTTL Loads Min
- Rise-Time : 8NS TYP. (measured from 0.8v to 2.0v)
- Operating Temp Range : 0°C to 70°C
- Temp Coefficient : 300 PPM/°C



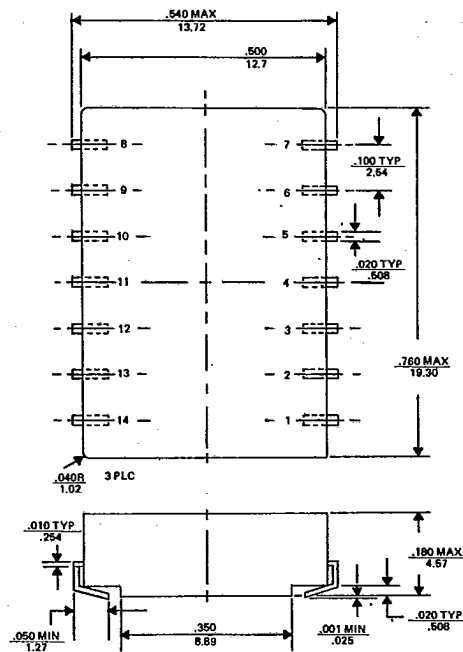
Electrical Specifications at 25°C (Measured with no Loads on Taps)

Part Number	Total Delay Nanosecond (1)	Tap to Tap Delay NSEC (1)
626-10*	9.0±1.0	1.0±0.4
626-20*	18.0±2.0	2.0±0.5
626-25*	22.5±2.0	2.5±0.7
626-50*	45.0±2.0	5.0±1.0
626-60*	54.0±3.0	6.0±1.0
626-70*	63.0±3.5	7.0±1.0
626-75*	67.5±3.5	7.5±1.0
626-100	100.0±5.0	10.0±2.0
626-125	125.0±6.0	12.5±2.0
626-150	150.0±7.5	15.0±2.0
626-200	200.0±10.0	20.0±2.0
626-250	250.0±12.5	25.0±2.5

Note: (1) Measured at 1.5v level leading edge.
*Time Delay measured with respect to 1st Tap.



SCHMATIC



INCHES .XXX ± .010
MILLIMETERS .XX ± .26