



**CHENMKO ENTERPRISE CO.,LTD**

**SURFACE MOUNT**

**N-Channel Enhancement Mode Field Effect Transistor**

**VOLTAGE 20 Volts CURRENT 2.8 Ampere**

**CHT2302PT**

*Lead free devices*

#### APPLICATION

- \* Servo motor control.
- \* Power MOSFET gate drivers.
- \* Other switching applications.

#### FEATURE

- \* Small surface mounting type. (SC-59/SOT-346)
- \* High density cell design for low R<sub>DSON</sub>.
- \* Suitable for high packing density.
- \* Rugged and reliable.
- \* High saturation current capability.
- \* Voltage controlled small signal switch.

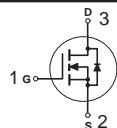
#### CONSTRUCTION

- \* N-Channel Enhancement

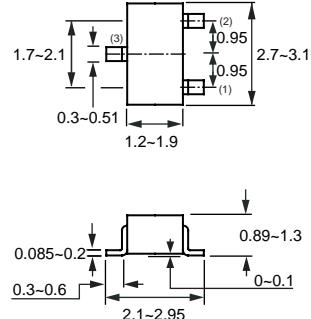
#### MARKING

- \* 02

#### CIRCUIT



**SC-59/SOT-346**



Dimensions in millimeters

**SC-59/SOT-346**

#### Absolute Maximum Ratings

T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	CHT2302PT	Units
V <sub>DSS</sub>	Drain-Source Voltage	20	V
V <sub>GSS</sub>	Gate-Source Voltage	±8	V
I <sub>D</sub>	Maximum Drain Current - Continuous (Note 1)	2.8	A
	- Pulsed (Note 2)	10	
I <sub>S</sub>	Drain-Source Diode Forward Current (Note 1)	1.6	A
P <sub>D</sub>	Maximum Power Dissipation (Note 1)	1250	mW
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec

2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%

#### Thermal characteristics

R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	85	°C/W
2005-12			

## RATING CHARACTERISTIC CURVES ( CHT2302PT )

**Electrical Characteristics**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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### OFF CHARACTERISTICS

$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V
$I_{DS(0)}$	Zero Gate Voltage Drain Current	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			1	$\mu\text{A}$
$I_{GS(0)}$	Gate-Body Leakage	$V_{GS} = 8 \text{ V}, V_{DS} = 0 \text{ V}$			+100	nA
$I_{GS(0)}$	Gate-Body Leakage	$V_{GS} = -8 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA

### ON CHARACTERISTICS (Note 2)

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	0.7		1.2	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS}=4.5\text{V}, I_D=3.6\text{A}$			85	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}, I_D=3.1\text{A}$			115	
$V_{SD}$	Diose Forward Voltage	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$			1.0	V

### SWITCHING CHARACTERISTICS (Note 3)

$Q_g$	Total Gate Charge	$V_{DS}=10\text{V}, I_D=1\text{A}$ $V_{GS}=4.5\text{V}$	6.52		nC
$Q_{gs}$	Gate-Source Charge		1.6		
$Q_{gd}$	Gate-Drain Charge		1.16		
$t_{on}$	Turn-On Time	$V_{DD}= 10\text{V}$ $I_D = 1.0\text{A}, V_{GEN}= 4.5 \text{ V}$ $R_L = 10\Omega, R_{GEN}= 10\Omega$	12		nS
$t_r$	Rise Time		36		
$t_{off}$	Turn-Off Time		34		
$t_f$	Fall Time		10		

Note : 3. Guaranteed by design , not subject to production testing

## RATING CHARACTERISTIC CURVES ( CHT2302PT )

### Typical Electrical Characteristics

Figure 1. Output Characteristics

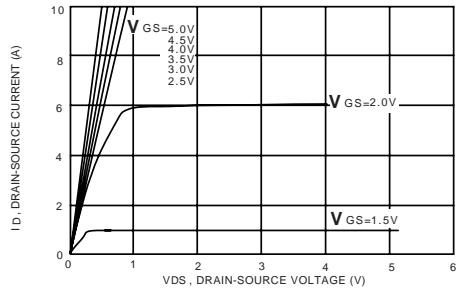


Figure 2. Transfer Characteristics

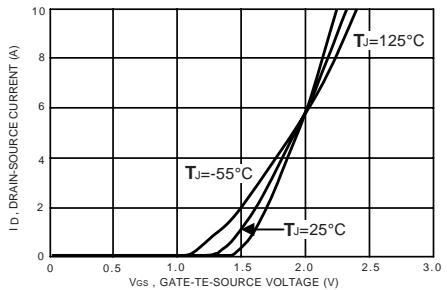


Figure 3. Breakdown Voltage Variation with Temperature

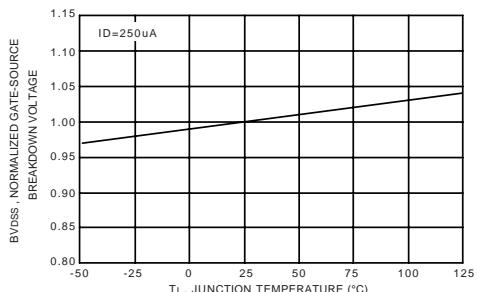


Figure 4. On-Resistance Variation with Temperature

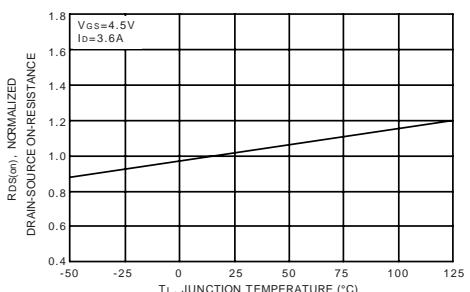


Figure 5. Gate Threshold Variation with Temperature

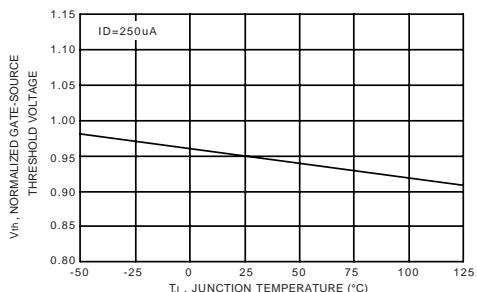


Figure 6. Gate Charge

