HITACHI

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FOR	MESSRS:	
I ON	WILGOING .	

DATE: May.13,2008

CUSTOMER'S ACCEPTANCE SPECIFICATIONS TX07D09VM1CAB

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*When product will be discontinued, customer will be informed by HITACHI with twelve months prior to discontinuation.

ACCEPTED BY;

PROPOSED BY; Dan Meng

KAOHSIUNG HITACHI	Sh.	7B64PS 2701-TX07D09VM1CAB-3 PAGE	1 1/12
ELECTRONICS CO.,LTD.	No.	75041 3 2101-1X01D09VW1CAD-3	1-1/14

RECORD OF REVISION

SHEET No.	. SUMMARY							
7B64PS 2704 -	4.3 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS							
TX07D09VM1CAB - 2	Revised							
Page 4 - 2/2		OPERATING		STORAGE				
·	ITEM	1			Max.			
	Ambient Temperature				70°C			
	Ambient remperature	-200	1	30 C	700			
	·	<u>, </u>	V					
- A	ITEM	OPERAT	FING	STO	RAGE			
	I I EIVI	Min.	Max.	Min.	Max.			
	Ambient Temperature	-20℃	70℃	-30℃	80℃			
	_							
7D64DS 2711	<u> </u>							
ł	1							
	1	ıre hetween	_30°C	and 70°C	at normal			
	1, '			and 700	, at nonna			
	Training.	1						
	(2) Keep the temperatu	ıre between	-30°C	and 80℃	at normal			
	humidity.							
7B64PS 2714 -	14. RELIABILITY TEST							
	Revised	1						
Page 14 – 1/1	TEST ITEM CONDITION							
	Heat Cycle							
	(Operation)							
	Thermal Shock	● -30±3°C30 minutes , 70±3°C30 minutes						
	1 1	·						
		Temperature transition time is within 4 minutes						
	High Temperature (Storage)	● 70±3°C1	100 hours					
	High Temperature (Operation)	● 60±3°C4	l8 hours					
				AIDITION:				
		- 00 00						
	1 1	ł			ur			
	(Operation)							
	Thermal Shock	· ·			30 minutes			
	(Storage)							
	High Temperature				nami 4 minutes			
	High Temperature (Operation)	● 70±3°C4	18 hours		·			
	7B64PS 2704 - TX07D09VM1CAB - 2 Page 4 - 2/2 7B64PS 2711 - TX07D09VM1CAB - 2 Page 11 - 3/3	TX07D09VM1CAB - 2 Page 4 - 2/2 ITEM Ambient Temperature Note 2 : For storage or Note 2 : For storage	TREM Ambient Temperature Detween humidity. TROTDO9VM1CAB - 2 Page 4 - 2/2 ITEM OPERAT Min. Ambient Temperature -20°C Note 2 : For storage condition Ta Note 2 : For storage condition Ta TXOTD09VM1CAB - 2 Page 11 - 3/3 11.5 STORAGE Revised (2) Keep the temperature between humidity. (2) Keep the temperature between humidity. (2) Keep the temperature between humidity. TROTD09VM1CAB - 2 Page 14 - 1/1 TXOTD09VM1CAB - 2 Page 14 - 1/1 14. RELIABILITY TEST Revised TEST ITEM Heat Cycle (Operation) 30 cycles, 100 cycles (Storage) High Temperature (Storage) High Temperature (Operation) 30 cycles, 100 cycles (Storage) TEST ITEM Heat Cycle (Operation) 30 cycles, 100 cycles (Storage) High Temperature (Storage) TEST ITEM Heat Cycle (Operation) 30 cycles, 100 cycles (Storage) High Temperature (Storage) 40 -20±3°C (Operation) 30 cycles, 100 cycles (Storage) Thermal Shock (Storage) 40 -30±3°C (Operation) 40 cycles (Storage) 40 -20±3°C (Operation) 40 cycles (Storage) 40 -20±3	A.3 ENVIRONMENTAL ABSOLUTE MAXI Revised A.3 ENVIRONMENTAL ABSOLUTE MAXI Revised A.4 ENVIRONMENTAL ABSOLUTE MAXI Revised A.5 ENVIRONMENTAL ABSOLUTE MAXI Ambient Temperature -20°C 60°C A.5 ENVIRONMENTAL ABSOLUTE MAXI Ambient Temperature -20°C 60°C A.5 ENVIRONMENTAL ABSOLUTE MAXI Ambient Temperature -20°C 60°C Ambient Temperature -20°C 70°C 70°C Ambient Temperature -20°C 70°C 70°C 70°C 70°C 70°C 70°C 70°C 70°C 70	### ABSOLUTE MAXIMUM RA Revised STOP ABSOLUTE MAXIMUM RA Revised			

RECORD OF REVISION

DATE	SHEET No.	SUMMARY
May.13,'08	7B64PS 2712 -	12.2 Location of lot mark
		Lot mark change: Print on FPC → Barcode label on frame
	Page 12 – 1/1	
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OHSILING	HITACHI	Sh. 750450 0705 7407 0705 000
	CS CO.,LTD. DATE	May.13,'08 No. 7B64PS 2702-TX07D09VM1CAB-3 PAGE 2-2/

3.GENERAL DATA

The specifications are applied to the following TFT-LCD (Transmissive Amorphas silicon TFT) module with Back-light unit. LCD driving circuit and LED driving circuit do not obtain in this module.

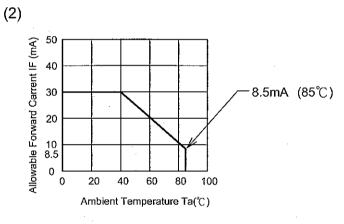
(1)	Part Name	TX07D09VM1CAB
(2)	Module Dimensions	50.54(W)mm x 68.62(H)mm x 3.63(D)mm typ.
		Except FPC Area
(3)	Effective Display Area	41.04(W)mm x 54.72(H)mm
(4)	Dot Pitch	0.057mm x 3(R,G,B)(W) x 0.171(H)mm
(5)	Resolution	240 x 3(R,G,B)(W) x 320 (H) dots
(6)	Color Pixel Arrangement	R,G,B Vertical Stripe
(7)	LCD Type	Transmissive Color TFT LCD (Normally White)
(8)	Display Type	Active Matrix
(9)	Number of Colors	262 ^K Colors (R,G,B 6 Bit Digital each)
(10)	Backlight	Light Emitting Diode (LED) x 5
(11)	Weight	(24)g
(12)	Interface	45 pin C-MOS
(13)	Viewing Direction	6 O'clock (The direction it's hard to be discolored)
(14)	Touch Panel	Resistance type. The surface is anti-glare

4. ABSOLUTE MAXIMUM RATINGS

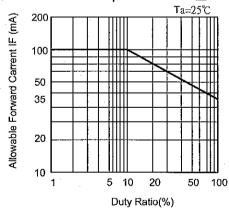
4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS OF LCD

	ITEM			MIN.	MAX.	UNIT	REMARKS
Power	Supply for Logic		VCC	-0.3	3.6	V	
Power Supply Voltage for Source Driver and Vcom		VDH	-0.3	6.0	V		
Input V	/oltage		Vi	0	VCC	V	(1)
	Power Supply for Gate	High	V _{GH}	-0.3	VGL+20	V	
Gate		Low	VgL	-6	0.3	V	
	Forward Current		IF	-	30	mA	(2)
LED	LED Puise Forward Current		IFP	-	80	mA	(3)
	Reverse Voltage		VR	-	5	· V	
Sta	tic Electricity		-	-	(±15)	kV	(4) (5)

Note (1) Hsync, Vsync, DCLK, R0~R5, G0~G5, B0~B5



(3) IFP Conditions : pulse width \leq 10ms and Duty \leq 1/10



- (4) Make certains you are grounded when handling LCM.
- (5) Testing condition : 200pF 0 Ω , 25° C 70%RH.

KAOHSIUNG HITACHI		M42 /00 Sh.	700400		DAOE	1.4/0
ELECTRONICS CO.,LTD.	DATE	May.13,'08 No.	· / /864PS	2704-TX07D09VM1CAB-3	PAGE	4-1/2

4.2 ELECTRICAL ABSOLUTE MAXIMUM RATINGS OF TOUCH PANEL

ITEM	SPECIFICATION	UNIT	CONDITION	REMARKS
Supply Voltage	7.0	V	DC	
Endurance Voltage	25	V	DC	(Note 1)

Note 1: Waiting 1 minute.

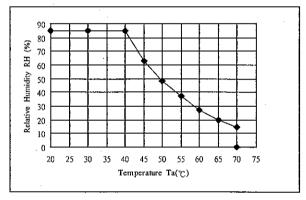
4.3 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STC	RAGE	REMARKS	
I I □IVI	Min.	Max.	Min.	Max.	KEWAKKS	
Ambient Temperature	-20°C	70℃	-30℃	80℃	(Note 2,3,6,7,9,10)	
Humidity	(No	te 1)	(No	ote 1)	Without condensation	
Vibration	-	-	-	(11.76)m/s ² (1.2G)	(Note 4,5)	
Shock	-	-	-	(490)m/s ² (50G)	(Note 5,8)	
Corrosive Gas	Not Acceptable		Not Acceptable			

Note 1 : Ta≤40°C : 85%RH max.

Ta>40°C : Absolute humidity must be lower than the humidity of 85%RH at 40°C.

as follow diagram.



Note 2 : For storage condition Ta at -30°C < 48h , at 80°C < 100h.

For operating condition Ta at -20°C < 100h

Note 3: Background color changes slightly depending on ambient temperature.

This phenomenon is reversible.

Note 4 : 5Hz~100Hz(Except resonance frequency)

Note 5: This LCM will resume normal operation after finishing the test.

Note 6: The response time will be slower as low temperature.

Note 7 : Only operation is guaranteed at operating temperature. Contrast, response time, another display quality are evaluated at 25℃.

Note 8: Pulse Width: 10ms

Note 9: This is panel surface temperature, not ambient temperature.

Note 10: If LED is drived by high current, the life time of LED will be reduced, also high temperature and high humidity.

KAOHSIUNG HITACHI		Sh.	
ELECTRONICS CO.,LTD.	DATE May.13,'0	8 No. 7B64PS 2704-TX07D09VM1CAB-3 PA	GE 4-2/2

5. ELECTRICAL CHARACTERISTICS

5.1 ELECTRICAL CHARACTERISTICS OF LCD

Ta=25°C, VSS=0V --

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage for logic	VCC	<u>-</u>	2.5	3.0	3.3	V
Power Supply Voltage for Source Driver and Vcom	VDH		4.5	5.0	6.0	
Input voltage for logic	Vi	"H" level	0.8VCC	_	VCC	V
(note 1)	VI	"L" levei	VSS	<u>-</u> _	0.2VCC	V ·
	VGH		14.0	15.0	16.0	
Power Supply for LCD	VGL	-	-16.0	-15.0	-14.0	V
	VCOM	VCOM-VSS	-	2.2	-	
	ICC	VCC-VSS=3.0V	-	0.6	-	
Power Supply Current	IDH	VDH-VSS=5.0V	-	2.5	-	mΛ
(note 2)	IGH	VGH-VSS=15.0V	-	0.06	-	mA
	IGL	VGL-VSS=-15.0V	-	0.22	-	
Vsync Frequency	fV	-	54	60	68	Hz
Hsync Frequency	fH	-	18.57	20.77	22.73	kHz
DCLK Frequency	fCLK	-	5.0	5.6	6.5	MHz

Note 1: DCLK, RD0~RD5, GD0~GD5, BD0~BD5.

Note 2 : fV=60Hz, Ta=25℃, Pattern used as display pattern : All black.

Note 3: Need to made sure of flickering and rippling of display when setting the frame frequency in your set.

5.2 ELECTRICAL CHARACTERISTICS OF BACK LIGHT

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARKS
LED Input Voltage	VF	IF=20mA	-	(3.2)	3.5	V	LED / Part
LED Forward Current	IF	-	-	20	25	mA	LED / Part
LED Reverse Current	IR	VR=5V	-	-	50	μΑ	LED / Part

KAOHSIUNG HITACHI		Mov 42 700	Sh.	7B64PS 2705-TX07D09VM1CAB-3 PAGE	E 1/0
ELECTRONICS CO.,LTD.	DATE	Iviay. 13, 06	No.	7B04PS 2705-1X07D09VW1CAB-3 FAGE	5-1/2

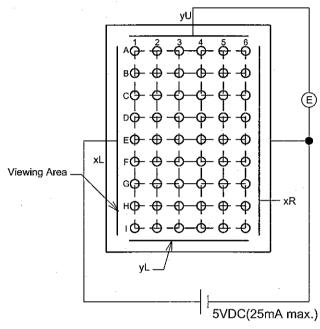
5.3 ELECTRICAL CHARACTERISTICS OF TOUCH PANEL

ITEM		SPECIFICATION	UNIT
Basistanaa hakusaa Tamaisal	xR - xL	190~540	ohm
Resistance between Terminal	yU - yL	140~540	ohm
Insulance Resistance (Note 1)	x - y	10M min.	ohm
(Nicko 2.2)	х	1.5 max.	%
Linearity (Note 2,3)	У	1.5 max.	%
Chattering		10 max.	ms

Note 1: Operating Voltage 25V DC.

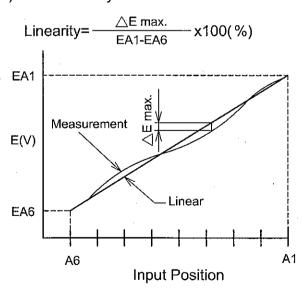
Note 2: Test Condition.

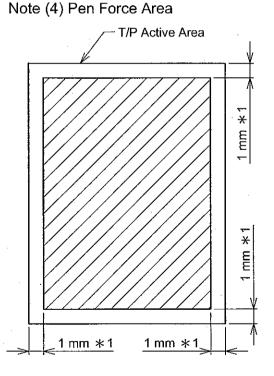
(a) Y axis linearity testing method (with tip radius 0.8, polaycetal pen). VxL-xR=5V, VOUT=VyU.



(b) X axis linearity method VyU-yL=5V, VOUT=VxL.

Note 3 : Calculation
(a) Y axis linearity





5.4 MECHANICAL CHARACTERISTICS OF TOUCH PANEL

ITEM	SPECIFICATION	PECIFICATION UNIT REMARI						
Pen Input Pressure	0.1 - 1.3	N	R0.8mm Polyacetal pen Note(4)					
Surface Hardness	3H min.	-	JIS K 5400					

KAOHSIUNG HITACHI	DATE	May 12 '09	Sh.	7B64PS 2705-TX07D09VM1CAB-3 F	PAGE	5-2/2
ELECTRONICS CO.,LTD.		May.13,'08	No.	7B04F3 2703-1X07B03VWTOAB-3 F	-AGL	J-2/2

6. OPTICAL CHARACTERISTICS

6.1 OPTICAL CHARACTERISTICS OF LCD (BACK LIGHT ON)

Ta=25°C

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE	
Brightness(center)		В	$\phi = 0^{\circ} \theta = 0^{\circ}$	290	340	· -	cd/m ²	(1)	
Uniformity		-	$\phi = 0^{\circ} \theta = 0^{\circ}$	70:	_	-	%	(2),(3),(4)	
		θx	ϕ =0°,K \geq 5.0	-	60				
Viouring Anglo		θx	ϕ =180°,K \ge 5.0	-	80	-	deg	(5) (6)	
Viewing Angle		θ y	<i>φ</i> =90°,K≧5.0	-	70	-] ueg	(5),(6)	
	•	θ y	<i>φ</i> =270°,K≧5.0	-	70	-			
Contrast Ratio		К	φ=0° θ=0°	200	300	Pro-	-	(4)	
Response Time (r	ise-fall)	tr+tf	$\phi = 0^{\circ} \theta = 0^{\circ}$	-	(30)		ms	(8)	
Color Tone	Red	х		(0.55)	(0.60)	(0.65)	-		
(Primary Color)	Reu	у		(0.29)	(0.34)	(0.39)	-		
	Green	X		(0.31)	(0.36)	(0.41)	-		
	Green	у	$\phi = 0^{\circ} \theta = 0^{\circ}$	(0.50)	(0.55)	(0.60)		(4)	
	Dluc	х	$\varphi = 0$ $\theta = 0$	(0.10)	(0.15)	(0.20)	-	(4)	
	Blue	у		(0.09	(0.14)	(0.19)	-		
	\A/bita	, x		(0.29)	(0.34)	(0.39)			
	White	у		(0.30)	(0.35)	(0.40)	-		

(Measurement condition: HITACHI standard)

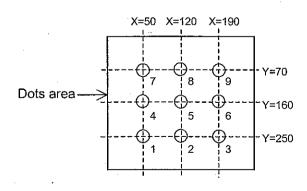
Note (4)~(7): See page 6-2/2

Note 1: Active area center LED Current: 20mA / Part

Note 2: Driving Condition Display Pattern: White Raster

LED Current: 20mA / Part Measurement of the following

9 places on the display.

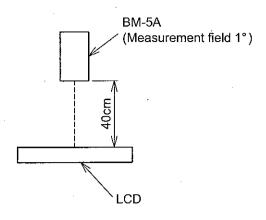


Note 3: Definition of the brightness uniformity

Min. brightness Max. brightness

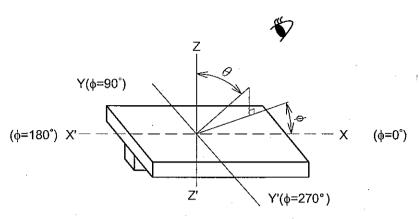
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ELECTRONICS CO.,LTD.	DATE	May.13,'08	No.	7B64PS 2706-TX07D09VM1CAB-3	⊮AGE	6-1/2

Note 4: Measurement Condition

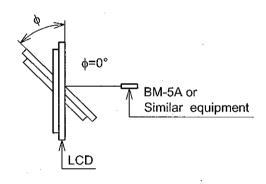


Note 5 : Definition of θ and ϕ (Normal)

Viewing direction



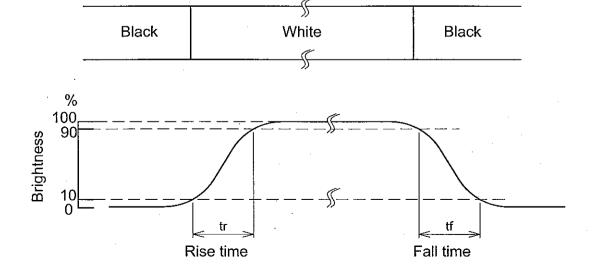
Note 6: Definition of Viewing angle



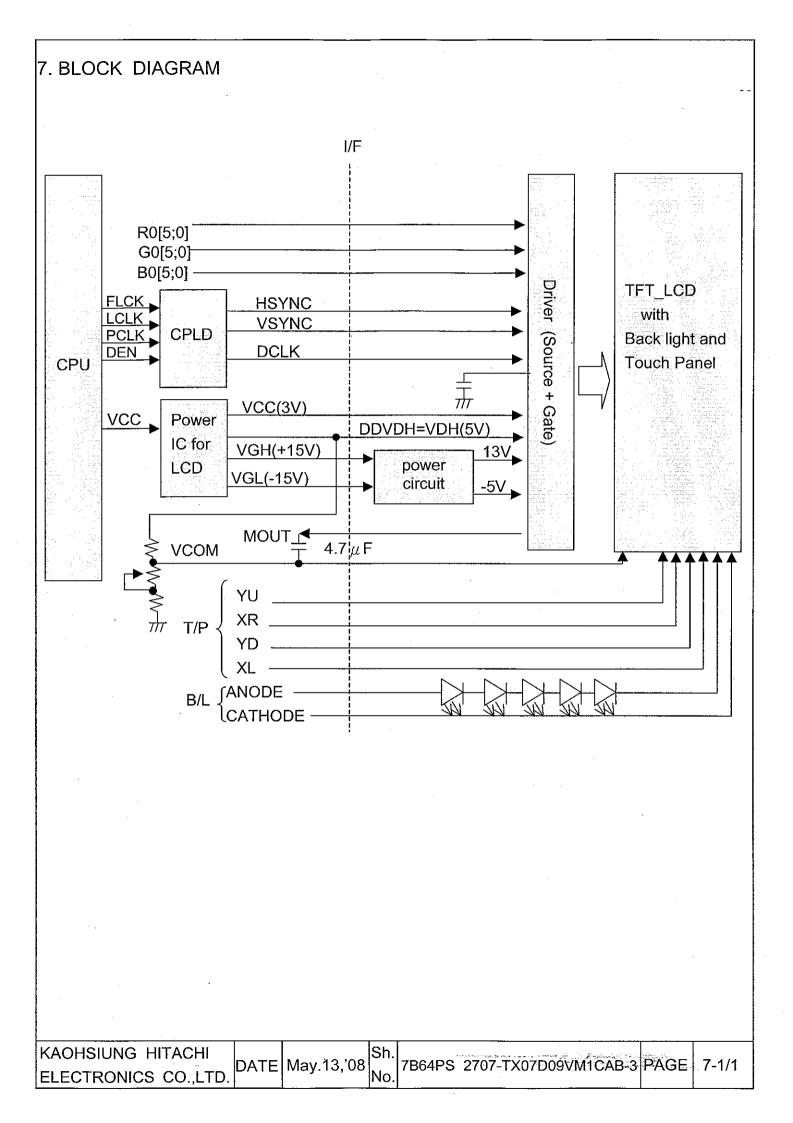
Note 7 : Definition of contrast "K" White Brightness

K= Black Brightness

Note 8: Definition optical response time



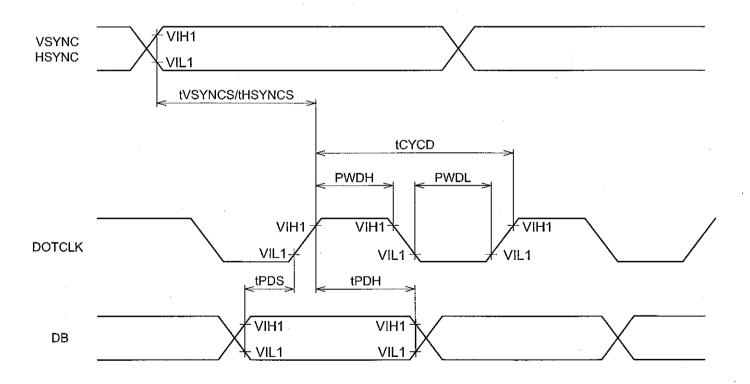
KAOHSIUNG HI	TACHI
ELECTRONICS	CO.,LTD.



8. INTERFACE TIMING

8.1 INTERFACE TIMING

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
DOTCLK Cycle Time	tCYCD	83	-	-		1 trans
DOTOLIK Oyole Tillle	ICTOD	60	-	ı	ns	3 trans
DOTCLK Low Level Pulse Width	PWDL	25	1	-	ns	-
DOTCLK High Level Pulse Width	PWDL	25	-	-	ns	ı
VSYNC Setup Time	tVSYNCS	0		1	clock	-
HSYNC Setup Time	tHSYNCS	0	-	1	clock	-
RGB Data Setup Time	TPDS	10	· -	-	ns	-
RGB Data Hold Time	TPDH	20	-	-	ns	-
DOTCLK/VSYNC/HSYNC Rising Edge , Falling Edge Time	trgbr / trgbf		-	20	ns	-



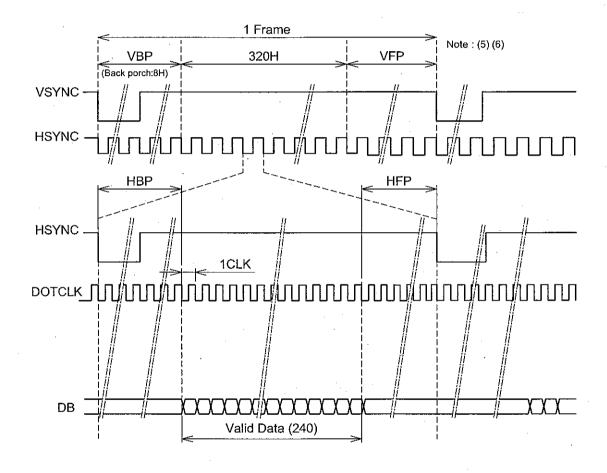
KAOHSIUNG HITACHI DATE May.13,'08 Sh. No. 7B64PS 2708-TX07D09VM1CAI

8.2 TIMING CHART

	SYMBOL	MIN.	TYP.	MAX.	UNIT
Back porch for Horizontal	HBP	_	12	-	Clock
Front porch for Horizontal	HFP	15	18 Note(1)	21	Clock
Back porch for Vertical	VBP	-	8 Note(3)	-	HSYNC
Front porch for Vertical	VFP	17	(20) Note(2)	22	HSYNC

Note(1): (DOTCLK total) - ((Valid data period for Horizontal) + (HBP))

(2): (HSYNC total) - ((Active Area period) + VBP)



(3) Note about VSYNC timing setting
Check the timing chart of VSYNC and HSYNC, If timing setting is not set as fig1, it must be set as fig2.

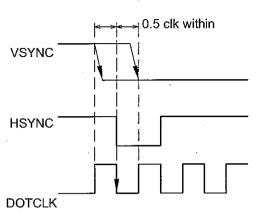


fig1. VSYNC timing

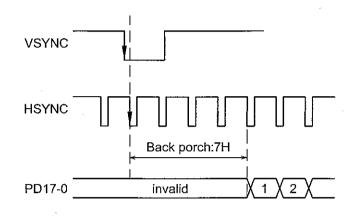
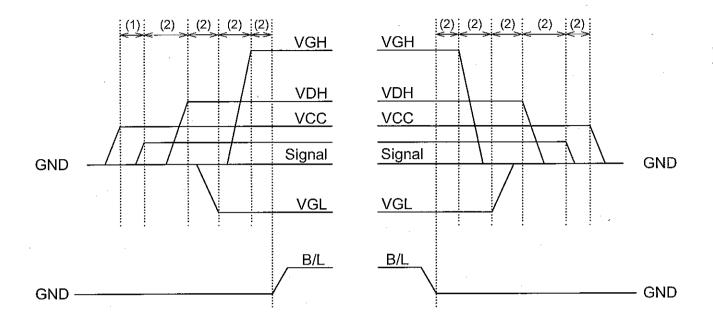


fig2. Vertical back porch regulation

- (4) The DOTCLK signal must be supplied consecutively.
- (5) Front and back porch (VBP , VFP) must be set before and after the display operation period.
- (6) The front porch period continues until the next input of VSYNC signal.

8.3 POWER ON/OFF SEQUENCE



- (1): VCC must be in satble situation (VCC \ge 2.7v), then Signal can be input. The period for (1) is t>0ms min.
- (2): t>0ms.

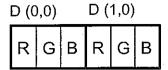
8.4 RELATIONSHIP BETWEEN DISPLAYED COLOR AND INPUT DATA

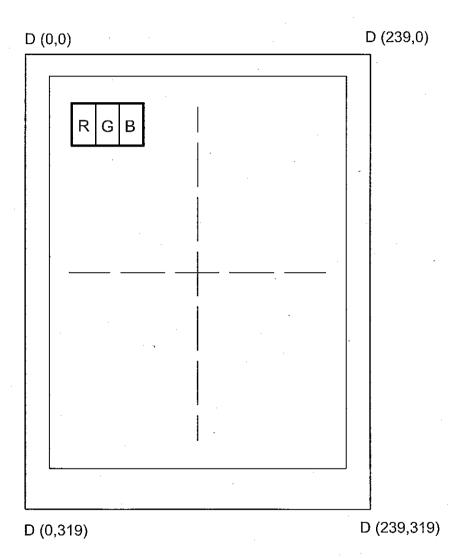
8.4.1 Display Colors

0.7.1 0	8.4.1 Display Colors Red Data										Green Data Blue Data								
											~					_			
Input	color			R3	R2	R1			G4	G3	G2		G0			B3	B2	B1	
		MS	3		LSB		MS	В			l	.SB	MS	B			L	LSB	
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)	0	0	0	Ō	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	.1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1_	1	1	1	1	1_	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)_	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(61)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red		:	:	<u>:</u>	:	:	:	:	:	:	:	•	:	:	:	:	:	:	:
I Neu		:	:	:	:	:	:	:	:	:	:	• •	:	:	:	:	:	:	:
	Red(2)	1	1	1	1	0	1	0	0	0	0	0	0	0	0.	0	0	0	0
	Red(1)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0_	0	0	0	0	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(61)	0	0	0	0	0	0	0	0	0,	0	1	0	0	0	0	0	0	0
Green		:	:	:	:	<u>:</u>	:	:	:	•		:	:	:	:	:	:	:	
Green	•	:		:	:	:	:_	:	:	:	:	:	:	:	:	:	:	:	:
	Green(2)	0	0	0	0	0	0	1_	1	1	1	0	1	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	O	0	0	0	0	0	0
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(61)	0	0	0	0	0	0	0	0	0.	0	0	0	0	0	0	0	1	0
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:		:		:	:	:
) Dide		:	:	:	:	:	:	:	:	:	:	:		<u>:</u>	:	:		:	:
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

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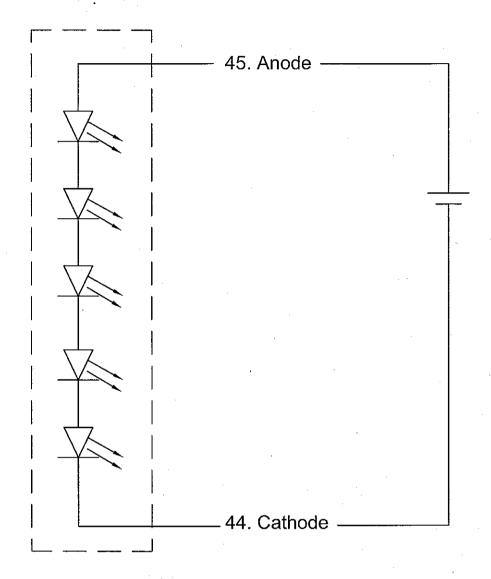
8.4.2 Data address





8.5 POWER SUPPLY CIRCUIT FOR LED BL

LED B/L



8.5 INTERNAL PIN CONNECTION

CN1 (FPC): Adaptable socket: FH23-45S-0.3SHW(Hirose Electric Co.,Ltd)

PIN No.	SIGNAL	FUNCTION
1	VSS	Ground
2	VCOM	Common electrode
3	VSS	Ground
4	MOUT	Alternating signal for Vcom output
5	VSS	Ground
6	NC	No connection
7	NC	No connection
8	NC NC	No connection
9	VCC	Power Supply Voltage for logic
10	DCLK	Dot clock signal
11	NC	No connection
12	VSS	Ground
13	D20	(LSB)
14	D21	Dad Data
15	D22	Red Data
16	D23	(MCD)
17	D24	(MSB)
18	D25	
19	D10	(LSB)
20	D11	
21	D12	
22	D13	Green Data
23	D14	
24	D15	(MSB)
25	D00	(LSB)
26	D01	
27	D02	Blue Data
28	D03	
29	D04	(MSB)
30	D05	
31	VSS	Ground
32	VDH	Power Supply Voltage for source Dr. and Vcom
33	HSYNC	Horizontal Sync Signal
		Power Supply for Gate Driver
34	VGL	(Low)
35	NC	No Connection
		Power Supply for Gate Driver
36	VGH	(High)
37	VSYNC	Vertical Sync Signal
38	VSS	Ground
39	YU	Touch Panel Upper Side
40	XR	Touch Panel right Side
41	. YD	Touch Panel Lower Side
42	XL	Touch Panel Left Side
43	VSS	Ground Supply voltage for LED
44	CATHODE	1 1 7 0
45	ANODE	Supply voltage for LED

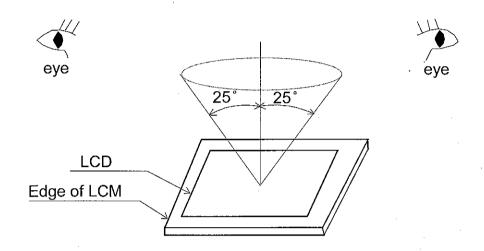
KAOHSIUNG HITACHI	DATE	May.13,'08	Sh.	7B64PS 2708-TX07D09VM1CAB-3 PAGE	8_8/8
ELECTRONICS CO.,LTD.	DAIL	Way. 15, 66	No.	TBO41 0 2700-1X07D09VW10AB-01 AGE	0-0/0

9. DIMENSIONAL OUTLINE 50.54 ± 0.2 49.84 ± 0.3 (T/P OUTLINE) 0.35 ± 0.3 45.04 ±0.3 (T/P V.A.) 2.75 ± 0.3 3.0 ± 0.3 0.35 ± 0.3 4.0 max. 43.04 min. (T/P A.A.) 5.0 ± 0.3 3.75 max. 41.04 ± 0.1 (LCD Viewing Area) 4.75 ± 0.3 3.63 ± 0.2 RGB 25.46±0.5 -Bending Area (27.36)Detail A (25.68) (2.5)67.82 (T/P OUTLINE) ±0.3 56.72 min.(T/P A.A.) 58.72 ±0.3(T/P VA) T/P ,B/L,FPC Soldering Area 13.80 ± 0.05 15.7mm $\times 18.5$ mm H = 4.5 max. 68.62 ± 0.2 Ground Layer (0.3)(12.00)Stiffener 4.5 max. Viewing Direction (15 min.) (2.6 min.) (5 min.) (1.31)(18.50) 3.5±0.3 0.2 ± 0.03 3.93 ± 0.3 (Including FPC) 13.2±0.03(P 0.3) Scale: NTS Unit: mm Detail A KAOHSIUNG HITACHI DATE | May.13,'08 | Sh. No. 7B63PS 2709-TX07D09VM1CAB-3 PAGE 9-1/1 ELECTRONICS CO.,LTD.

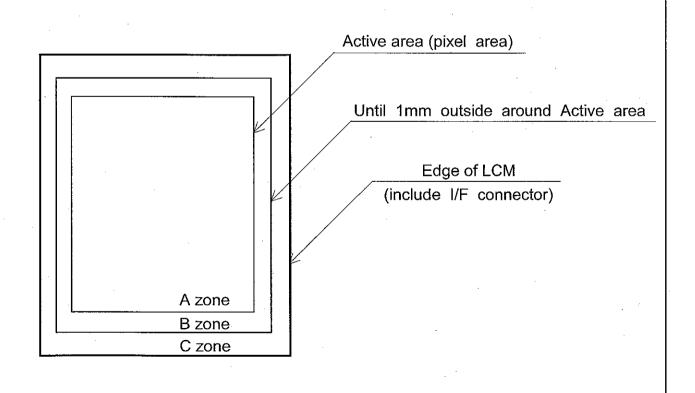
10. APPEARANCE STANDARD

10.1 APPEARANCE INSPECTION CONDITION Visual inspection should be done under the following condition.

- (1) The inspection should be done in a dark room. (More than 1000(lx) and non-directive)
- (2) The distance between eyes of an inspector and the LCD module is 30cm.
- (3) The viewing zone is shown the figure. Viewing angle ≤ 25°



10.2 DEFINITION OF ZONE



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10.3 APPEARANCE SPECIFICATION

(1)LCM Appearance

*) If the problem related to this section occurs about this item, the responsible persons of both party (Customer and HITACHI) will discuss the matter in detail.

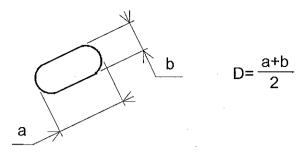
No.	ITEM	CRITERIA					
	Scratches	Length L(mm) L<2.0	L(mm) W(mm)		l	mum number cceptable ignored	ZONE A,B
	Dent	~	Distinguished one is acceptable (To be judged by limit sample (Note4))				A
	Wrinkles in Polarize	Same as above					Α
	Stains	Fil	amentous	(Line sha	pe)		
	Foreign	Length	Wid	lth	Maxii	mum number	
	Materials	L(mm)	W(m		а	cceptable	
	Bubbles	L≦0.5	W≦			ignored	A,B
,	Dark spot	0.5≦L≦2.5	W≦			3	
		2.5 <l< td=""><td>W></td><td>0.1</td><td></td><td>none</td><td></td></l<>	W>	0.1		none	
c			Round(Do	ot shape)			
		Average diam	eter	Max	Maximum number		
М		D(mm)	acceptable				
'*'		D≦0.		igno	red	A,B	
		0.2 <d≦0.< td=""><td>4</td><td></td><td>3</td><td></td><td></td></d≦0.<>	4		3		
		0.4 <d< td=""><td></td><td></td><td>nor</td><td>ne</td><td></td></d<>			nor	ne	
		Those wiped out eas	sily are acc	eptable			
	Color Tone	To be judged by li	mit sample	(Note4)			Α
	Color Uniformity	Same as above		•		•	A
	Newton Ring (Touch Panel)	To be judged by limit	t sample (N	lote4)			A,B
	Touch Panel Uncleanness	No conspicuous dirt					Α
	Rubbing Scratch	To be judged by limit	t sample (N	lote4)			-
	Line Defect	Not allowed				·	
١. ا	Bright Dots	Red + Green + Blue - ≤4 dots			≦4 dots		
╽╌┕╴╽		Red + Green + Blue	Linked 2 d	ots		≦2 set	
c	<u> </u>		Linked 3 o	r more do	ts	0 set]. ,
	Dark Dots	Black		-		≦4 dots	Α
D			Linked 2 d	ots		≦2 set	
	•		Linked 3 o	r more dot	ts	0 set	
	Total	Bright dots + Dark do	ots	.7778.1		≦6 dots]

		· · · · · · · · · · · · · · · · · · ·		
KAOHSIUNG HITACHI		M= 40 200	Sh.	7DC4DC 0740 TV07D00 (0440 050 0 FACE 40 0/4
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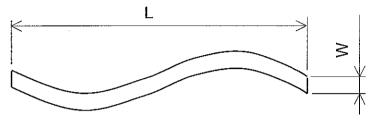
(2) Glass indentation

ITEM	SPECIFICATIONS
Common Indentation	$\begin{array}{c cccc} X & Y & Z \\ & \leq 5.0 & \leq 1.0 & \leq t \end{array}$
Corner Broken	$\begin{array}{c cccc} X & Y & Z \\ \hline & & \leq 2.0 & \leq 2.0 & \leq t \end{array}$
Proceeding Crack	None

Note 1: Definition of average diameter (D)

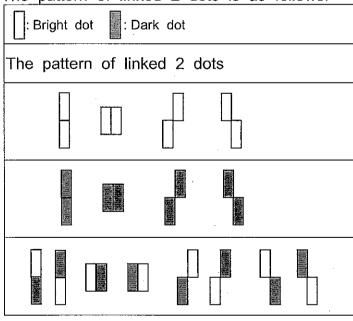


Note 2: Definition of length (L) and width (W)



Note 3: Definition of dot defect

- (a) Regardless of bright or intermittent bright, 1/3 or more defects of a dot area is counted as the defect dot.
- (b) Bright dots are measured while the display is black.
- (c) Dark dots are measured while the display is illuminated with red, green and blue.
- (d) The pattern of linked 2 dots is as follows.



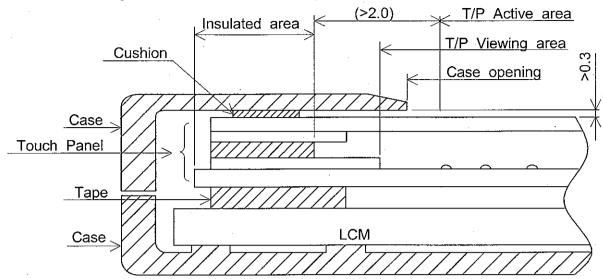
Note 4: When problem occurred, it judged the basis of both company deliberation after limit sample creation.

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11. PRECAUTION IN DESIGN

11.1 MOUNTING PRECAUTION

(1) When assembling the Touch Panel and you case, please refer to the figure below.



- (2) The clearance between the Touch Panel and case shall be designed so that the case edge never presses the input screen when it is deformed by heat or other causes.
- (3) The case shall be designed not to touch the tail portion (FPC for Touch Panel).
- (4) The boundary space between the effective area and the insulated area is unstable. Touching this area may effect the operation of the Touch Panel.

 The case must be designed so that it does not touch the boundary space.

11.2 PRECAUTIONS AGAINST ELECTROSTATIC DISCHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band, etc. And don't touch I/F pins directly.

11.3 HANDLING PRECAUTIONS

(1) Since the Touch Panel on the top, and the frame on the bottom tend to be easily damaged, they should be with full care so as not to get them touched, pushed or rubbed by a piece on glass, tweezers and anything else which are harder a pencil lead 3H.

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(2) As the adhesives used for adhering upper/lower polarizer's and frame are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl alcohol. The following are recommended for use: normal hexane

Please contact with us when it is necessary for you to use chemicals other than the above.

- (3) Lightly wipe to clean the dirty surface with absorbent cotton or other soft material like chamois, soaked in the recommended chemicals without scrubbing it hardly.
 - Always wipe the surface horizontally or vertically. Never give a wipe in a circle. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.
- (4) Immediately wipe off saliva or water drop attached on the display area because it may cause deformation or faded color.
- (5) Fogy dew deposited on the surface may cause a damage, stain or dirt to the polarizer.

When you need to take out the LCD module from some place at low temperature for test, etc.

It is required to be warmed them up to temperature higher than room temperature before taking them out.

- (6) Touching the display area or I/F pins with bare hands or contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched with bare hands. (Some cosmetics are detrimental to polarizer's.)
- (7) In general, the glass is fragile so that, especially on its periphery, tends to be cracked or chipped in handling. Please not give the LCD module sharp shocks by falling, etc.
- (8) Maximum pressure to the surface must be less than 1.96×10⁴ Pa.

 And if the pressure area is less than 1cm², maximum pressure must be less than 1.96N.
- (9) Top sheets shall be cleaned gently using a soft cloth such as those used for glasses.
 Hard wiping accumulated dust will leave scars on the surface even using a cloth.

11.4 OPERATION PRECAUTION

(1) Using a LCM module beyond its maximum ratings may result in its permanent destruction.

LCM module's should usually be used under recommended operating conditions shown in chapter 4 and chapter 5. Exceeding any of these conditions may adversely affect its reliability.

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- (2) Response time will be extremely delayed at lower temperature than the specified operating temperature range and on the other hand LCD's shows dark blue at higher temperature.

 However those phenomena do not main defects of the LCD module. Those phenomena will disappear in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some display patterns will be abnormally display.
- (4) A slight dew depositing on terminals may cause electrochemical reaction which leads to terminal open circuit. Please operate the LCD module under the relative condition of 40°C 85%RH.
- (5) Resistance range: Your controller shall be set up to allow the resistance range of Touch Panel specified in our CAS.
- (6) Pointed position of Touch Panel may shift owing to a change in resistance of Touch Panel depending on the operation condition. To compensate this shift, the set shall be given a calibration function.
- (7) Input shall be made with a stylus pen (polyacetal, R0.8). Chances are very high that use of a metal piece including a ball point pen or sharp edge will impair accuracy.
- (8) The Touch Panel is an auxiliary input device. The system shall be designed to have other input device.

11.5 STORAGE

In case of storing LCD module for a long period of time (for instance, for years) for the purpose of replacement use, the following precautions necessary.

- (1) Store the LCD modules in a dark place; do not expose them to sunlight or ultraviolet rays.
- (2) Keep the temperature between -30°C and 80°C at normal humidity.
- (3) Store the LCD modules in the container which is used for shipping from us.
- (4) No articles shall be left on the surface over an extended period of time.

11.6 SAFETY

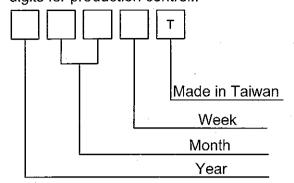
Wear finger cots or gloves whenever handling or assembling a Touch Panel its glass edges are sharp.

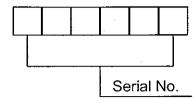
KAOHSIUNG HITACHI	DATE	Sh	7B64PS 2711-TX07D09VM1CAB-3 PAGE 1	1 2/2
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12.DESIGNATION OF LOT MARK

12.1 LOT MARK

Lot mark is consisted of 4 digits for production lot and 6 digits for production control..



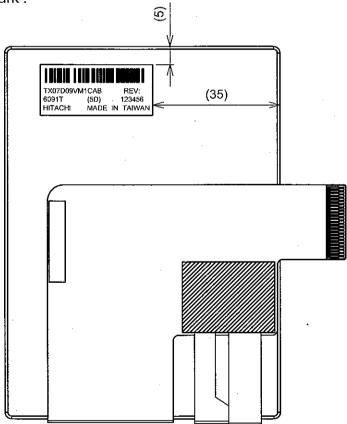


Year	Mark
2007	7
2008	8
2009	9
2010	0
2011	1

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Mark	01	02	03	04	05	06
Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Mark	07	08	09	10	11	12

Figure In Lot Mark	
LOT Mark	
1	
2	
3	
4	
5	

12.2 Location of lot mark:



13. PRECAUTION FOR USE

- (1) A limit sample should be provided by the both parities on an occasion when the both parties agree to its necessity.
 Judgement by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- (2) On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
 - 1) When a question is arisen in the specifications.
 - 2) When a new problem is arisen which is not specified in this specifications.
 - 3) When an inspection specifications change or operating condition change by customer is reported to HITACHI, and some problem is arisen in the specification due to the change.
 - 4) When a new problem is arisen at the customer's operating set for sample evaluation.
- (3) Regarding the treatment for maintenance and repairing, both parties will discuss it in six months later after latest delivery of this product.

The precaution that should be observed when handling LCM have been explained above.

If any points are unclear or if you have any requests, please contact with HITACHI.

14. RELIABILITY TEST

TEST ITEM	CONDITION	JUDGEMENT NOTEL	
High Temperature and Humidity (Operation) 40±2℃, RH=85%, 250 hours			
Heat Cycle (Operation)	-20±3°C1hour , 70±3°C1 hour30 cycles, 8 hours / cycle		
Thermal Shock (Storage)	 → -30±3°C30 minutes , 80±3°C30 minutes → 100 cycles , 1 hour / cycle → Temperature transition time is within 4 minutes 		
High Temperature (Storage)	OUTS (IVV HOUIS		
Low Temperature (Storage)	● -30±3°C48 hours	No display malfunctions	
High Temperature (Operation)	● 70±3°C48 hours		
Low Temperature (Operation)	● -20±3°C100 hours		
ESD (Storage)	 200pF, 0Ω, (±15)kV, 70%RH 9 places on a panel surface 3 times each places at 1 sec interval 		
Vibration (Storage)	I ■ X . Y .∠ girection		
Shock (Storage)			

Note1: Condensation of water is not permitted on the module.

Note2: The module should be inspected after 1 hour storage in room conditions.

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