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YOUR MODULE NO.: _____ OUR MODULE NO.: ____ K283QVB-V46-F

YOUR SPEC NO.: OUR FULL SPEC NO.: FS-K283QVB-V46-F-01

APPROVED BY					

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Kitronix (Dong Guan) Ltd.

No. A20, Luyi Road, Tianxin Country, Tangxia Town, Dongguan City, Guangdong Province. www.DataSheet4U.com



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Product

Standard LCD Module 240 x RGB x 320 Dots 2.83" TFT LCD Wide temperature With white color LED backlight With Touch Panel

Kitronix (Dong Guan) Ltd. No. A20, Luyi Road, Tianxin Country, Tangxia Town, Dongguan City, Guangdong Province.



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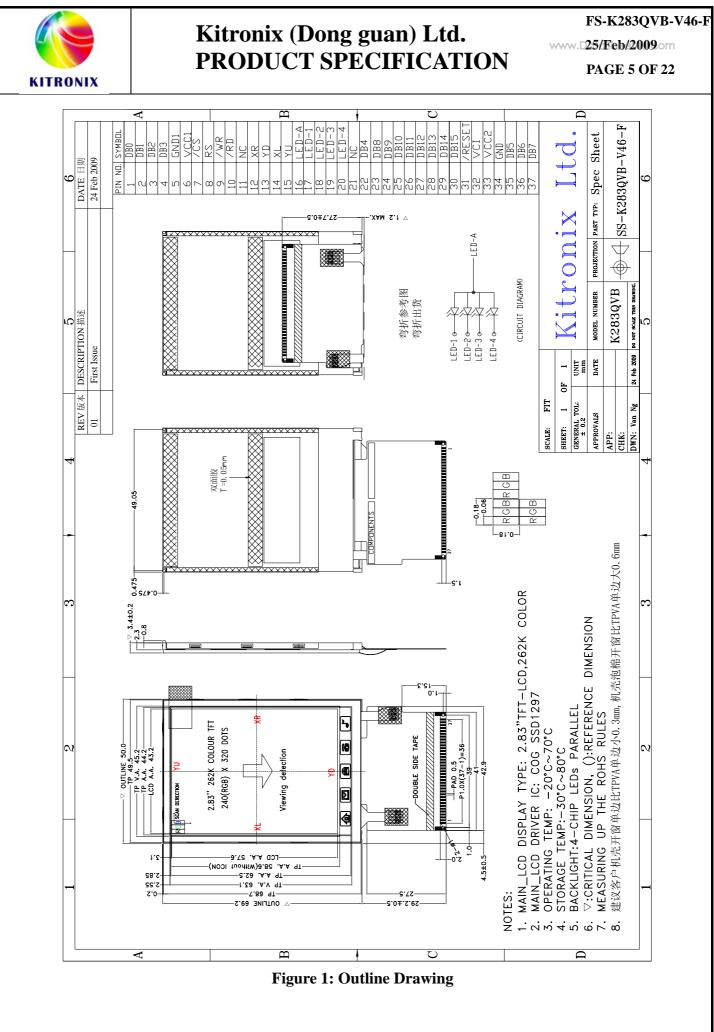
2. General Description

- 2.83"(diagonal), 240 x RGB x 320 dots, 262k colors, Transmissive, TFT LCD module.
- Viewing Direction: 12 o'clock.
- Driving IC: SSD1297 or equivalent TFT controller/driver.
- 16-bits data bus (I80 system interface).
- Logic voltage: 2.8V (typ.).
- Touch panel.

3. Mechanical Specifications

The mechanical detail is shown in Fig. 1 and summarized in Table 1 below.

Table 1					
Par	rameter	Specifications	Unit		
Outline dimensions		50.0(W) x 69.2(H) x 3.4(D) (Exclude FPC, cables of touch panel and backlight)	mm		
	View area	45.2(W) x 63.1 (H)	mm		
	TP active area	44.2(W) x 62.5(H)	mm		
Color TFT	LCD active area	43.2(W) x 57.6(H)	mm		
240xRGBx320	Display format	240 x RGB x 320	dots		
	Color configuration	RGB stripes	-		
	Dot pitch	0.180(RGB)(W) x 0.180(H)	mm		
V	Veight	TBD	grams		



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4. Interface signals

	indee signals	Table 2: Pin assignment
Pin No.	Symbol	Description
1-4	[DB0-DB3]	16-bit bi-directional data bus.
5	GND1	
3	GNDI	Ground for the logic and analog circuit.
6	VCC1	A power supply for the internal logic circuit and for the I/O circuit. VCC = $2.2 \sim 3.3$ V.
7	/CS	Chip select signal. 0: chip can be accessed; 1: chip cannot be accessed.
8	RS	Register Select Signal (H: Data, L: Instruction)
9	/WR	I80 system: Serves as a write signal and writes data at the rising edge.
10	/RD	I80 system: Serves as a read signal and reads data at the low level.
11	NC	NC
12	XR	
13	YD	Terminal of touch panel.
14	XL	Terminal of touch panel.
15	YU	
16	LEDA	Anode of LED backlight.
17	LEDK1	
18	LEDK2	Cathode of LED backlight.
19	LEDK3	
20	LEDK4	
21	NC	NC
22	DB4	16-bit bi-directional data bus.
23-30	[DB8-DB15]	
31	/RESET	Reset pin. Setting either pin low initializes the LSI.
20	NOT .	Must be reset the chip after power being supplied.
32	VCI	A power supply for the internal logic circuit and for the I/O circuit.
33	VCC2	$VCC = 2.2 \sim 3.3V.$
34	GND	Ground for the logic and analog circuit.
35-37	[DB5-DB7]	16-bit bi-directional data bus.



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5. Absolute Maximum Ratings

5.1 Electrical Maximum Ratings – for IC Only

Table 3: Electrical Maximum Ratings – for IC

		80 101 1	<u> </u>		
Parameter	Symbol	Min.	Max.	Unit	Note
Power supply voltage (VCC)	VCC	-0.3	+4.0	V	1

Note:

1.VCC, GND must be maintained.

2. The modules may be destroyed if they are used beyond the absolute maximum ratings.

5.2 Environmental Condition

<u>Table 4</u>						
Item	Operat tempera (Top	ture	Stor temper (Tst (Not	Remark		
	Min.	Max.	Min.	Max.		
Ambient temperature	-20°C	+70°C	-30°C	+80°C	Dry	
Humidity (Note 1)	80 < 50% RH for 40°	No condensation				

Note 1: Product cannot sustain at extreme storage conditions for long time.

6. Electrical Specifications

Typical Electrical Characteristics

At Ta = 25 °C, VCC=IOVCC= 2.4V to 3.3V, GND=0V.

<u>Table 5</u>							
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Supply voltage (logic)	VCC-GND		2.6	2.8	3.3	V	
Supply current (Logic & LCD)	ICC	VCC=2.8V	-	-	10	mA	
Supply voltage of white LED backlight	VLED =V(_{BL+})- V(_{BL-})	Forward current =60 mA Number of LED	2.9	3.2	3.5	V	
Luminance (on the module surface)		dies = 4	-	150	-	cd/m ²	



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7. Optical Characteristics

	Table 7: Optical specifications							
Iterre		Symphol	Condition	Specifications		Unit		
Items		Symbol	Condition	Min.	Тур.	Max.	Unit	
Contrast Ra	atio	CR		-	350	-	-	
Response T	imo	T _R		-	15	20	ms	
Kesponse I	line	$T_{\rm F}$		-	35	50	ms	
	Red	X _R		0.600	0.620	0.640	I	
	Reu	Y _R		0.310	0.330	0.350	I	
	Green	X _G		0.283	0.303	0.323	I	
Chromaticity		Y _G		0.570	0.590	0.610	I	Note
Cinomaticity	Blue	X _B		0.115	0.135	0.155	I	Note
		Y _B		0.100	0.120	0.140	-	
	White	X_W		0.288	0.308	0.328	I	
	white	Y_W		0.310	0.330	0.350	-	
	Hor.	$\phi 1(3 \text{ o'clock})$		-	45	-		
Viewing angle		$\phi 2(9 \text{ o'clock})$	Center	-	45	-	daa	
Viewing angle	Ver.	$\theta 2(12 \text{ o'clock})$	CR≥10	-	35	-	deg.	
	vel.	$\theta 1(6 \text{ o'clock})$		-	15	-		
NTSC ratio					60.0		%	

Note 1: Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

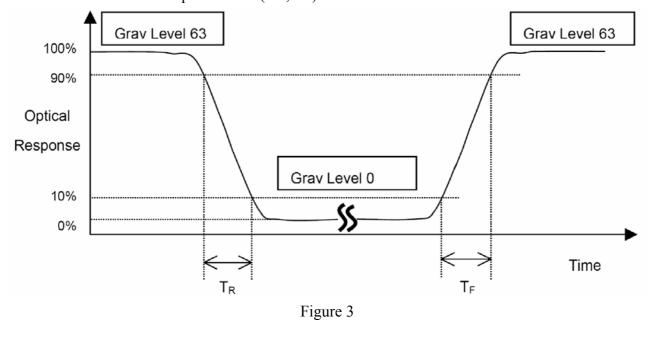
Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63

L0: Luminance of gray level 0

CR = CR(10)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note 5. Note 2: Definition of Response Time (TR, TF):





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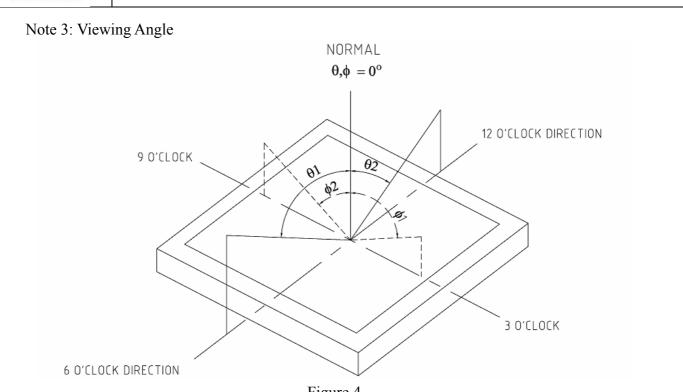
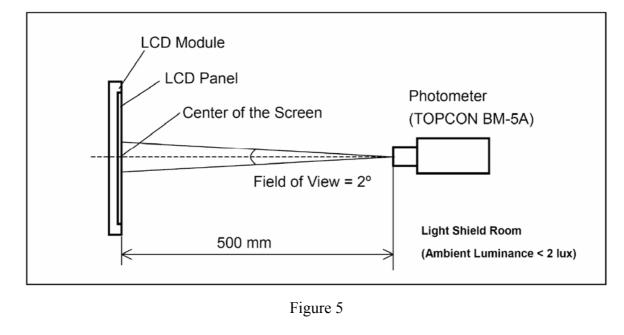


Figure 4

The above "Viewing Angle" is the measuring position with Largest Contrast Ratio; not for good image quality. View Direction for good image quality is 6 O'clock. Module maker can increase the "Viewing Angle" by applying Wide View Film.

Note 4: Measurement Set-Up:

The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.





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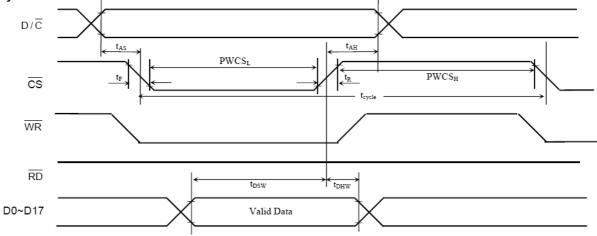
8. Timing Characteristics

8.1 80-system Bus Interface Timing Characteristics of IC

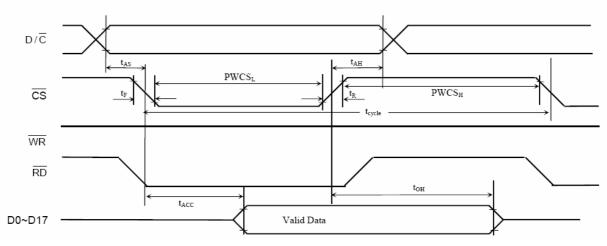
Table 8: Normal Write Mode (VCC = IOVCC=2.4~3.3V)

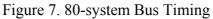
Symbol	Parameter	Min	Тур	Max	Unit
t _{cycle}	Clock Cycle Time (write cycle)	100	-	-	ns
t _{cycle}	Clock Cycle Time (read cycle)	1000	-	-	ns
t _{AS}	Address Setup Time	0	-	-	ns
t _{AH}	Address Hold Time	0	-	-	ns
t _{DSW}	Data Setup Time	5	-	-	ns
t _{DHW}	Data Hold Time	5	-	-	ns
t _{ACC}	Data Access Time	250	-	-	ns
t _{он}	Output Hold time	100	-	-	ns
PWCS _L	Pulse Width /CS low (write cycle)	50	-	-	ns
PWCS _H	Pulse Width /CS high (write cycle)	50	-	-	ns
PWCS _L	Pulse Width /CS low (read cycle)	500	-	-	ns
PWCS _H	Pulse Width /CS high (read cycle)	500	-	-	ns
t _R	Rise time	-	-	4	ns
t _F	Fall time	-	-	4	ns

Write Cycle



Read Cycle







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8.2 Reset Operation of IC

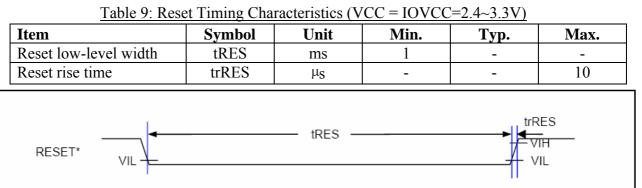


Figure 8: Reset Timing

9. Reliability Test Item



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Test Item	Sample Type	Test Condition	Test result determinant gist
High temperature	Normal temperature	70±3℃;96H	the inspection of
storage	Wide temperature	80±3℃;96H	appearance and function
Low temperature	Normal temperature	-20±3℃;120H	character.
storage	Wide temperature	-30±3℃;120H	
High temperature	Normal temperature	50°C±3°C,90%±3%RH;96H	
/humidity storage	Wide temperature	60°C±3°C,90%±3%RH;96H	
High temperature	Normal temperature	60±3℃;96H	no objection of the function
operation	Wide temperature	70±3℃;96H	character; no fatal objection of
Low temperature	Normal temperature	0±3℃;96H	the appearance.
operation	Wide temperature	-20±3℃;96H	
High temperature	Normal temperature	40°C±3°C,90%±3%RH;96H	
/humidity operation	Wide temperature	50°C±3°C,90%±3%RH;96H	
Temperature Shock	Normal temperature	$-20\pm3^{\circ}C,30\min\rightarrow70\pm3^{\circ}C,30$	inspect the objections
		min;10cycle	appearance, function & the
			whole structure
	Wide temperature	-30±3°C,30min	The inspection of appearance,
		80±3,30min;10cycle	function & the whole structure

10. Suggestions for using LCD modules



10.1 Handling of LCM

- 1. The LCD screen is made of glass. Don't give excessive external shock, or drop from a high place.
- 2. If the LCD screen is damaged and the liquid crystal leaks out, do not lick and swallow. When the liquid is attach to your hand, skin, cloth etc, wash it off by using soap and water thoroughly and immediately.
- 3. Don't apply excessive force on the surface of the LCM.
- 4. If the surface is contaminated ,clean it with soft cloth. If the LCM is severely contaminated , use Isopropyl alcohol/Ethyl alcohol to clean. Other solvents may damage the polarizer . The following solvents is especially prohibited: water , ketone Aromatic solvents etc.
- 5. Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

6. Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.

7. Don't disassemble the LCM.

- 8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Be sure to ground the body when handling the LCD modules.
 - Tools required for assembling, such as soldering irons, must be properly grounded.
 - To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
 - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

9. Do not alter, modify or change the the shape of the tab on the metal frame.

10. Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.



- 11. Do not damage or modify the pattern writing on the printed circuit board.
- 12. Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector
- 13. Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- 14. Do not drop, bend or twist LCM.

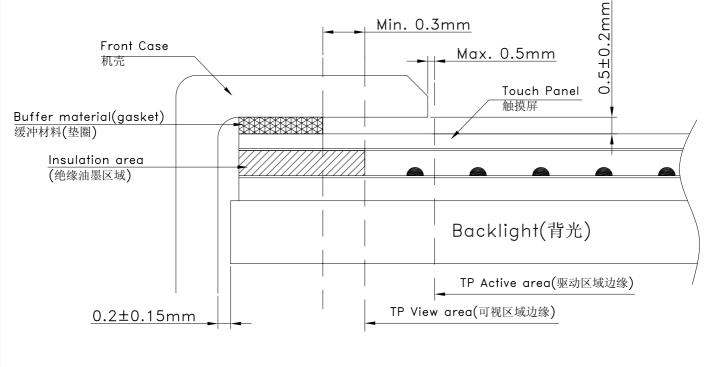
10.2 Cautions for installing and assemabling if the module has Touch Panel

1. Use a buffer material (Gasket) between the touch panel and Front-case to protect damage and wrong operating. The dimension of the buffer material's edge between the TP V.A. edge is Min. 0.3mm.

2. We recommend to design a case that it can't over the boundary of the active area Max. 0.5mm in order to prevent an operation at outside of the active area which can't guarantee the specified durability, because operation at the outside of the active area cause serious damage of a transparent.

3. When design case for installing Module, you would consider give a distance about 0.2 ± 0.15 mm between the module edge to case inside.

4. The corners of the product are not chamfered. When positioning and fixing the product on the case, we sugguest that you would provide a R part on the conner of the case so as not to apply load on the corner of the transparent module.





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10.3 Storage

1. Store in an ambient temperature of 5 to 45 °C, and in a relative humidity of 40% to 60%. Don't expose

to sunlight or fluorescent light.

- 2. Storage in a clean environment, free from dust, active gas, and solvent.
- 3. Store in antistatic container.



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11. Inspection Standard

This specification is made to be used as the standard acceptance/rejection criteria for Color mobile phone LCM with touch pannel.

11.1 Sample plan and Inspection condition

11.1.1 Sample plan

Sampling plan according to MIL-STD-105E , normal level 2 and based on:

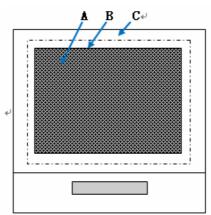
Major defect: AQL 0.65;

Minor defect: AQL 1.5.

11.1.2 Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45 against perpendicular line.

11.2 Definition of inspection zone in LCD



Inspection zones in an LCD

Zone A: character/Digit area;

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area);

Zone C: Outside viewing area (invisible area after assembly in customer's product);

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product. Defects are classified as major defects and minor defects according to the degree of defectiveness defined herein.

11.3 Major defects and Minor defects

11.3.1 Major defects

A major defect is a defect that is likely to result in failure, or to reduce the usability of the product for its intended purpose.

11.3.1.1 Abnormal operation: modules cannot display normally;



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11.3.1.2 Line defect;

11.3.1.3 There is serious distortion or sharp burr on mechanical housing;

11.3.1.4 Glass breakage.

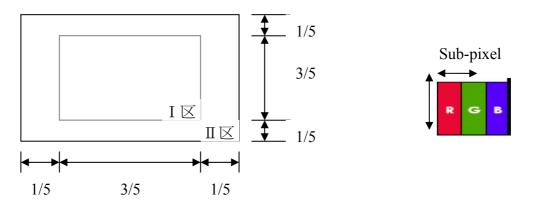
11.3.2 Minor defects:

A minor defect is a defect that is not likely to reduce the usability of the product for its intended purpose.

11.3.2.1 Dot defect:

11.3.2.1.1 Inspection pattern : Full white, full black, red, green and blue screens;

11.3.2.1.2 Criteria :(acceptable);



Note: 1. Dot defect is defined as the defective area of the dot area is larger than 50% of the dot area . And the bright dot defect must be visible through 5% ND filter.

2. Except for the allowed numbers of adjacent dots, the distance between dot defects should be more than 3mm apart.

11.3.2.1.3 The definitions of the inner display area and outer display area.

11.4 Inspection standards table:

11.4.1 Major defect

Item No.	Items to be	Inspection Standard	Classification of defects
11.4.1.1	All functional defects	 No display Display abnormally Missing vertical/horizontal segment Short circuit Back-light no lighting, flickering and abnormal lighting. 	Major
11.4.1.2	Missing	Missing component	-
11.4.1.3	Outline dimension	Overall outline dimension beyond the drawing is not allowed.	
11.4.1.4	linearity	No more than 1.5%	



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11.4.2 Co	smetic Defect	(spot defect)						
Item No	Itemsto be	Inspect	Inspection Standard					
11.4.2.1	Clear Spots Black and white		For dark/white spot, size Φ is defined as $\Phi = (x + y)/2$					
	Spot defect Pinhole, Foreign Particle, polarizer Dirt	Siz Φ≤0.1 0.10<	Zone ze(mm) (Ф≤0.15 (Ф≤0.20	Acceptable A B Ignore 2 1	C C Ignore	Minor		
	Din	$\Phi > 0.$		0				
11.4.2.2	Clear Spot	s Φ≤0.1		Acceptable A B Ignore	C C	Miner		
	TP Dirt	0.10 <	a ⊕≤0.15 a ⊕≤0.25 25	$\frac{2}{1}$	Ignore	Minor		
	Dim Spots		Zone	Acceptable				
11.4.2.3	Circle shaped and dim edged	Φ≤0.2	ze(mm) ΩΦ≤0.4	A B Ignore	С	Minor		
	defects	0.20 < 0.4 < 0 0.4 < 0 $\Phi > 0.$	₽≤0.6	$\frac{2}{1}$	Ignore			
			dot =sub-pixel					
			Acceptable Qty		Qty			
11.4.2.4	Dot defect			Ι	II	Minor		
			ght dot	0	2			
			ark dot tance of two poin	$\frac{1}{t > 5mm}$	2			
11.4.3 Co	smetic Defect		•					
Item No	Items to be	<u> </u>	Inspection Standard					
	Line defect Black line, White line, Foreign	Si	ze(mm)	Acceptat				
11.4.3.1		L(Length)	W(Width)	zon		Minor		
			· · ·	A E	3 C			
		Ignore	$\frac{W \le 0.02}{0.02 < W \le 0.03}$	Ignore	—			
	material on polarizer	L≤3.0 L≤2.0	$0.02 < W \le 0.03$ $0.03 < W \le 0.05$	2	Ignore			
	-	L <u>></u> 2.0	W > 0.05	Define as spot de	fect			



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KIIKUN										
		The line can b	e seen at	fter mobile	phone in the	operating con	dition:			
11.4.3.2		Size(mm)			Acceptable Qty					
	Foreign Material on TP film	L(Length) W(Width)		zone						
		· · · · ·		<u>,</u>	A	В	С	Minor		
		Ignore			Ignore		Ignore			
		L≤3.0			3					
		W>0.05 Define as spot defect								
	Dim line defect Polarizer	If the scratch of operating conductive seen only in the following.								
		Size(mm)			Acceptable Qty			Miner		
11 4 2 2				zone						
11.4.3.3	&BL scratch TP film	L(Length)	W(Width)	А	В	С	Minor		
	scratch	Ignore	W≤0.()2	Ignore					
		L≤3.0	0.02 <	≪W≤0.03		2				
		L≤2.0	0.03 <	≪W≤0.05		1	Ignore			
			W>0	.05	Define as	s spot defect				
		Air bubbles	Air bubbles between glass & polarizer							
	Polarize Air bubble									
				A		table Qty B	С			
		Φ≤0.2		11	Ignoro	D	C			
11.4.3.4							Minor			
		0.20<Φ≤0.3		2		Ignore				
		0.3<Φ≤0.5			1					
		$\Phi > 0.5$ 0								
11.4.4 Ch	ipping Defect									
Item No	Items to be	Inspection Standard						Classification of defects		
	Glass defect	Chips on corner A:LCD Glass defect						Minor		
		Z X Y $Z\leq 0.2 \leq S Disregard$								
		Notes: S=contact pad length								
11.4.4.1		Chips on the corner of terminal shall not be allowed to extend into the ITO pad or expose perimeter seal.								
		B:TP Glass defect								
		X T								
		$\begin{array}{c c c c c c c c c c c c c c c c c c c $								
		$\leq 3.0 \leq 3.0$ Disregard								
		\checkmark								
Mana DataSho	ot 411 com									

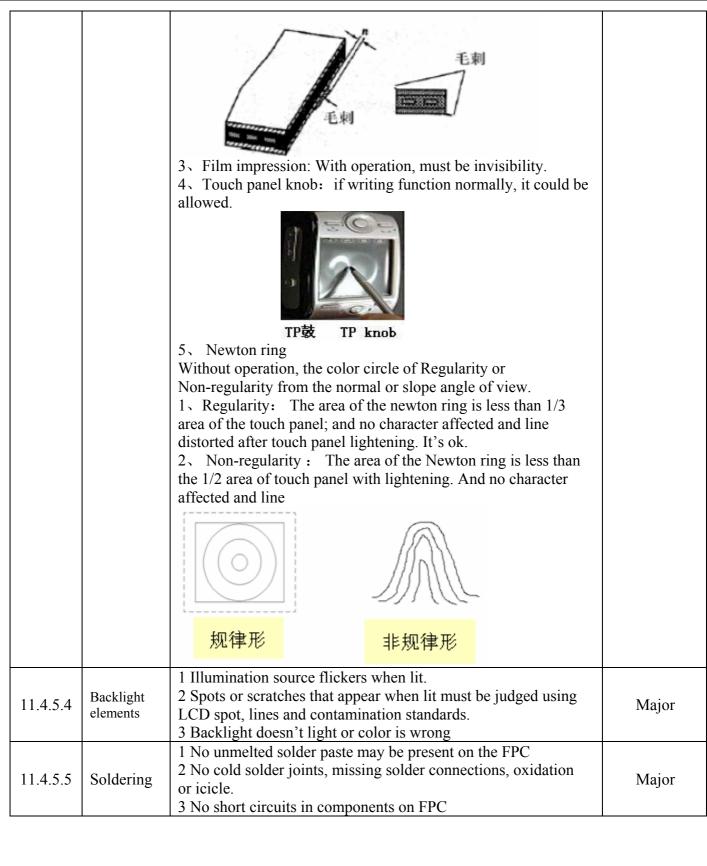
		Kitronix (Dong guan) Ltd.	S-K283QVB-V46 5/Feb/2009om AGE 20 OF 22	
		Usual surface cracks A:LCD Glass defect X Y Z ≤ 3.0 <inner border="" disregard<br="" line="" of="" seal="" the="">B:TP Glass defect X Y Z $\leq 6.0 \leq 2.0$ Disregard Crack: Cracks tend to break are not allowed.</inner>		
11.4.5 Par	rts Defect	· · · · · · · · · · · · · · · · · · ·		
Item No	Items to be	Inspection Standard	Classification of defects	
11.4.5.1	Parts contra position	 Not allow IC and FPC/heat-seal lead width is more than 50% beyond lead pattern. 2. Not allow chip or solder component is off center more than 50% of the pad outline. 	Major	
11.4.5.2	SMT	According to the <acceptability electronic<br="" of="">assemblies>IPC-A-610C class 2 standard. Component missing or function defect are Major defect, the others are Minor defect.</acceptability>	Major	
11.4.5.3	TP Defect	 Pattern font: Pattern fonts are clear and symmetrical, pattern fonts filter lightly are allowed; The fort line is not allow to thinner or thicker than 1/3 of normal size, and swing is not more than 0.1mm. the line is smooth and not broken. 图案字体 Pattern font The wing forward in the side of Visual Area: The length of wing forward inside of the Visual Area: n≤0.2mm; Not excess 3 point, and the distance D≥20mm. 	Major	



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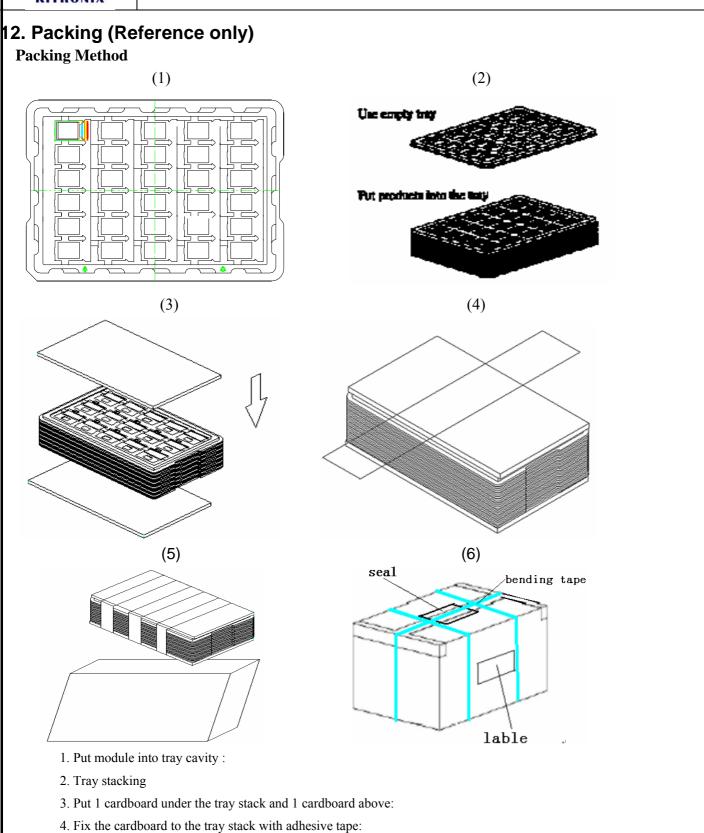




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- 5. Put the tray stack into carton.
- 6. Carton sealing with adhesive tape.

- END -