

# μPD9335C

## Numeric Interface Adapter

### For μPD70208/70216

### Description

The μPD9335C is a Numeric (coprocessor) Interface Adapter (NIA) used to connect the NEC μPD70208/216 (V40™/V50™) to the 8087 Numeric Data Processor (NDP).

The NIA replaces more than 10 LSI TTL devices required to implement the interface. The NIA serves to convert the bus release protocol from the HOLD/HOLDACK of the V40/50 to the REQUEST/GRANT of the 8087. The 9335C also generates the memory control signals for NDP bus cycles.

The NIA also provides for external wait state READY control of NDP bus cycles by synchronizing the READY inputs of the 8087 and the V40/V50. In addition, the NIA contains an internal wait state generator which can be programmed in hardware to match the operation of the V40/V50 internal wait state generator.

### Features

- Direct V40/V50 to 8087 interface through NIA chip
- No external circuitry required
- Internal 0 to 3 wait state generator for NDP cycles
- External READY synchronization for V40/V50 and NDP bus cycles
- Low power consumption CMOS device
- Single 5 V power supply

### Ordering Information

Part Number	Package
μPD9335C	20-pin plastic DIP (300 mil)

V40 and V50 are trademarks of NEC Corporation.

### Block Diagram

