

# Midium Power Transistors (-80V / -2.5A)

## **2SAR544D**

#### Features

1) Low saturation voltage, typically  $V_{CE (sat)}$  = -0.4V (Max.) (I<sub>C</sub> / I<sub>B</sub>= -1A / -50mA)

2) High speed switching

#### Structure

PNP Silicon epitaxial planar transistor

#### Applications

Driver

#### Packaging specifications

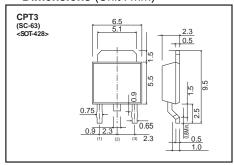
_	Package	CPT3
	Code	TL
	Basic ordering unit (pieces)	2500

#### ● Absolute maximum ratings (Ta = 25°C)

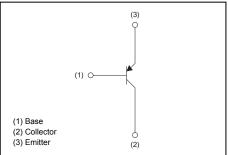
Parameter		Symbol	Limits	Unit
Collector-base voltage		$V_{CBO}$	-80	V
Collector-emitter voltage		$V_{CEO}$	-80	V
Emitter-base voltage		$V_{EBO}$	-6	V
Collector current	DC	Ic	-2.5	Α
	Pulsed	I <sub>CP</sub> *1	-5	Α
Power dissipation		P <sub>D</sub> *2	1	W
		P <sub>D</sub> *3	10	W
Junction temperature		T <sub>j</sub>	150	°C
Range of storage temperature		T <sub>stg</sub>	-55 to 150	°C

<sup>\*1</sup> Pw=10ms, Single Pulse

#### Dimensions (Unit : mm)



#### • Inner circuit



<sup>\*2</sup> Mounted on a substrate.

<sup>\*3</sup> Tc=25°C

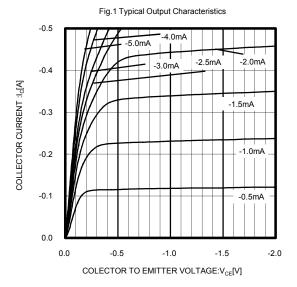
# ● Electrical characteristic (Ta = 25°C)

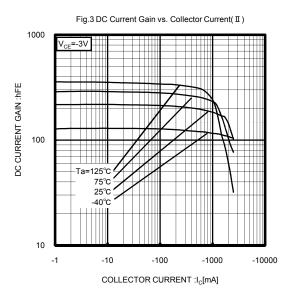
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-emitter breakdown voltage	$BV_{CEO}$	-80	-	-	V	I <sub>C</sub> = -1mA	
Collector-base breakdown voltage	$BV_{CBO}$	-80	-	-	V	I <sub>C</sub> = -100μA	
Emitter-base breakdown voltage	$BV_{EBO}$	-6	-	-	V	I <sub>E</sub> = -100μA	
Collector cut-off current	$I_{CBO}$	-	-	-1	μA	V <sub>CB</sub> = -80V	
Emitter cut-off current	I <sub>EBO</sub>	1	1	-1	μ <b>A</b>	V <sub>EB</sub> = -4V	
Collector-emitter staturation voltage	V <sub>CE(sat)</sub> <sup>*</sup> 1	1	-200	-400	mV	I <sub>C</sub> = -1A, I <sub>B</sub> = -50mA	
DC current gain	h <sub>FE</sub>	120	1	390	-	$V_{CE}$ = -3V, $I_{C}$ = -100mA	
Transition frequency	f <sub>T</sub> *1	ı	280	ı	MHz	V <sub>CE</sub> = -10V I <sub>E</sub> =500mA, f=100MHz	
Collector output capacitance	C <sub>ob</sub>	-	32	-	pF	V <sub>CB</sub> = -10V, I <sub>E</sub> =0A f=1MHz	
Turn-on time	t <sub>on</sub> * <sub>2</sub>	-	50	-	ns	- 120   - 120m4	
Storage time	t <sub>stg</sub> * <sub>2</sub>	-	400	-	ns	$I_C = -1.3A$ , $I_{B1} = -130mA$ , $I_{B2} = 130mA$ , $V_{CC} \sim -10V$	
Fall time	t <sub>f</sub> *2	-	40	-	ns	]	

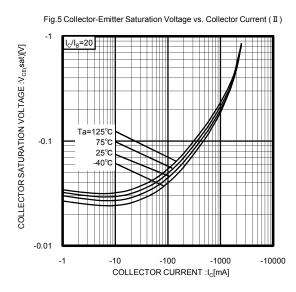
<sup>\*1</sup> Pulsed

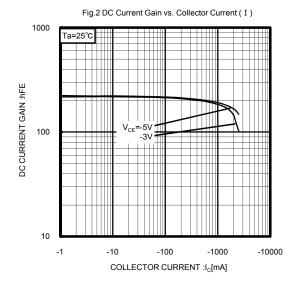
<sup>\*2</sup> See switching time test circuit

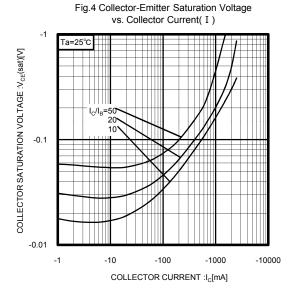
#### • Electrical characteristic curves (Ta = 25°C)

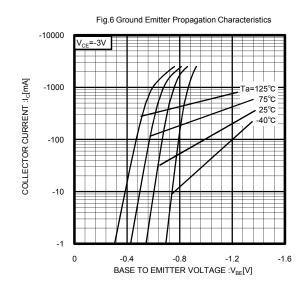


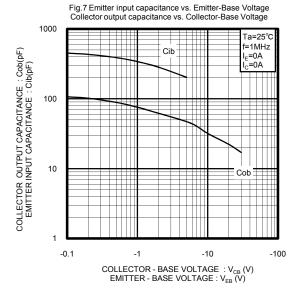


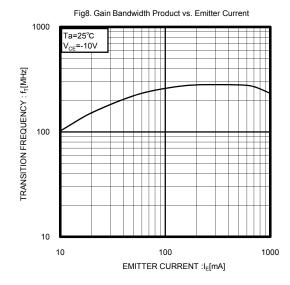


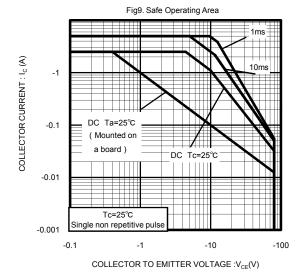




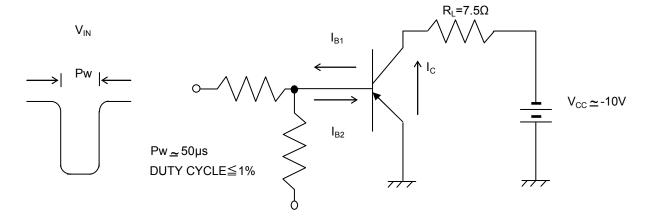


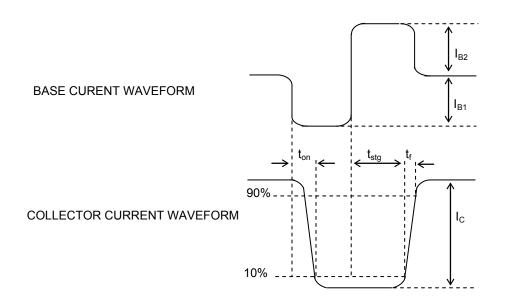






### • Switching time test circuit





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