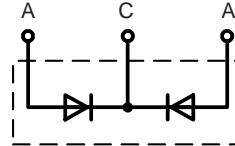


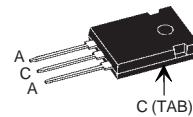
HiPerFRED™ Epitaxial Diode with common cathode and soft recovery

I_{FAV} = 2x30 A
V_{RRM} = 300 V
t_{rr} = 30 ns

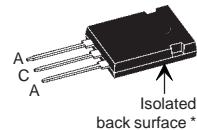
V _{RSM} V	V _{RRM} V	Type
300	300	DSEC 60-03A
300	300	DSEC 60-03AR



TO-247 AD
Version A



ISOPLUS 247™
Version AR



* Patent pending

A = Anode, C = Cathode

Symbol	Conditions	Maximum Ratings	
I _{FRMS}		70	A
I _{FAVM}	T _C = 145°C; rectangular, d = 0.5 T _C = 135°C (AR-Version)	30	A
I _{FSM}	T _{VJ} = 45°C; t _p = 10 ms (50 Hz), sine	300	A
E _{AS}	T _{VJ} = 25°C; non-repetitive I _{AS} = 3 A; L = 180 µH	1.2	mJ
I _{AR}	V _A = 1.5·V _R typ.; f = 10 kHz; repetitive	0.3	A
T _{VJ}		-55...+175	°C
T _{VJM}		175	°C
T _{stg}		-55...+150	°C
P _{tot}	T _C = 25°C	165	W
M _d *	mounting torque	0.8...1.2	Nm
F _c	mounting force with clip	20...120	N
V _{ISOL} **	50/60 Hz, RMS, t = 1 minute, leads-to-tab	2500	V~
Weight	typical	6	g

* Version A only; ** Version AR only

Symbol	Conditions	Characteristic Values	
		typ.	max.
I _R ①	T _{VJ} = 25°C V _R = V _{RRM} T _{VJ} = 150°C V _R = V _{RRM}	250 1	µA mA
V _F ②	I _F = 30 A; T _{VJ} = 150°C T _{VJ} = 25°C	0.91 1.25	V V
R _{thJC}	A-Version AR-Version	0.9 1.1	K/W K/W
R _{thCH}		0.25	K/W
t _{rr}	I _F = 1 A; -di/dt = 200 A/µs; V _R = 30 V; T _{VJ} = 25°C	30	ns
I _{RM}	V _R = 100 V; I _F = 50 A; -di _F /dt = 100 A/µs T _{VJ} = 100°C	7	A

Pulse test: ① Pulse Width = 5 ms, Duty Cycle < 2.0 %

② Pulse Width = 300 µs, Duty Cycle < 2.0 %

Data according to IEC 60747 and per diode unless otherwise specified

IXYS reserves the right to change limits, test conditions and dimensions.

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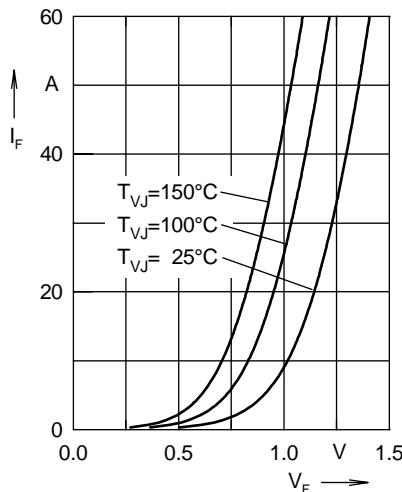


Fig. 1 Forward current I_F versus V_F

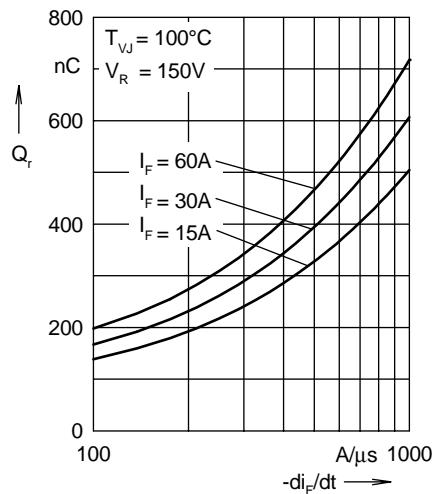


Fig. 2 Reverse recovery charge Q_r versus $-di_F/dt$

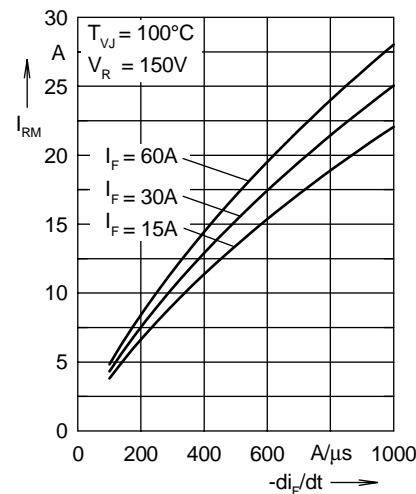


Fig. 3 Peak reverse current I_{RM} versus $-di_F/dt$

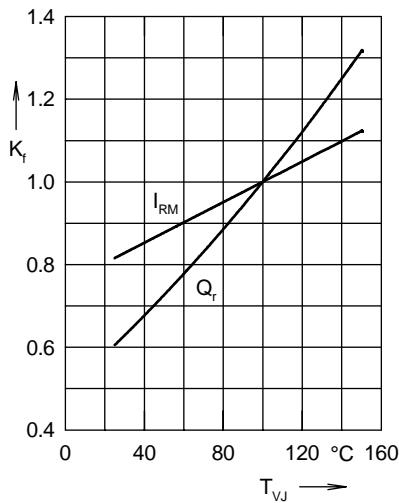


Fig. 4 Dynamic parameters Q_r , I_{RM} versus T_{VJ}

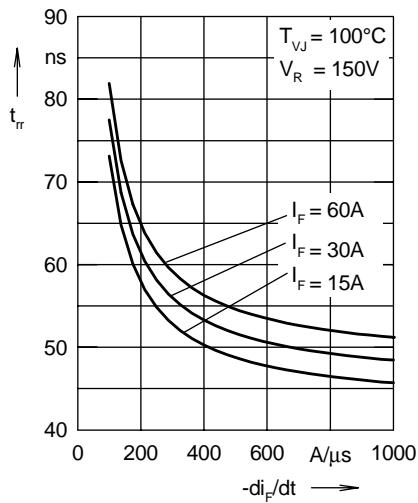


Fig. 5 Recovery time t_{rr} versus $-di_F/dt$

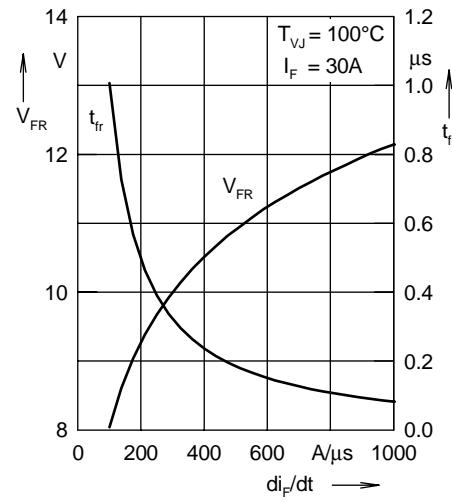


Fig. 6 Peak forward voltage V_{FR} and t_{rr} versus di_F/dt

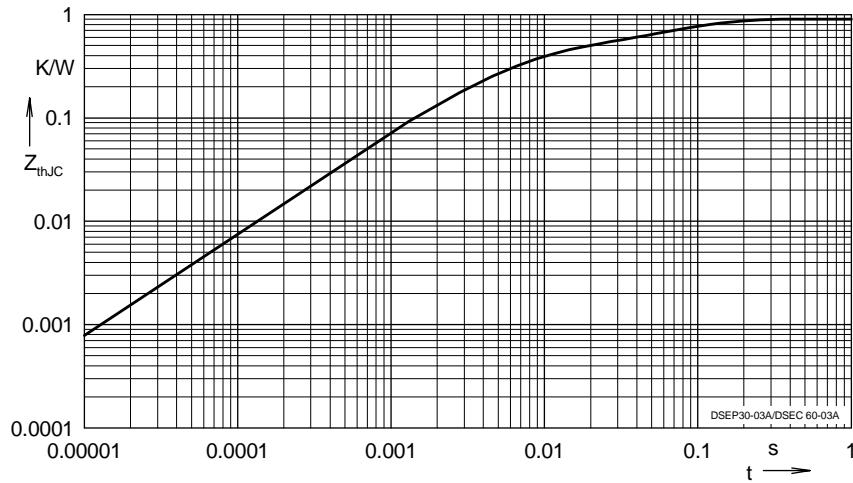


Fig. 7 Transient thermal resistance junction to case

NOTE: Fig. 2 to Fig. 6 shows typical values

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