TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSIV)

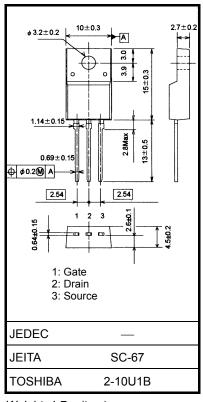
2SK4013

Switching Regulator Applications

- Low drain-source ON resistance: R_{DS (ON)} = 1.35 Ω (typ.)
- High forward transfer admittance: |Y_{fs}| = 5.0 S (typ.)
- Low leakage current: I_{DSS} = 100 μA (max) (V_{DS} = 640 V)
- Enhancement-model: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

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Characteristics			Symbol	Rating	Unit			
Drain-source voltage			V _{DSS}	800	V			
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)			V _{DGR}	800	V			
Gate-source voltage			V _{GSS}	±30	V			
Drain current	DC (Note 1)	Ι _D	6	А			
	Pulse (Note 1)	I _{DP}	18	A			
Drain power dissipation (Tc = 25° C)			PD	45	W			
Single pulse avalanche energy (Note 2)			E _{AR}	317	mJ			
Avalanche current			I _{AR}	6	А			
Repetitive avalanche energy (Note 3)			E _{AR}	4.5	mJ			
Channel temperature			T _{ch}	150	°C			
Storage temperature range			T _{stg}	-55 to 150	°C			

Absolute Maximum Ratings (Ta = 25°C)



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

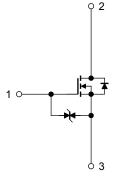
Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W	
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W	

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: $V_{DD} = 90 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}$ (initial), L = 14.5 mH, R_G = 25 Ω , I_{AR} = 6 A

Note 3: Repetitive rating; pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device. Please handle with caution.



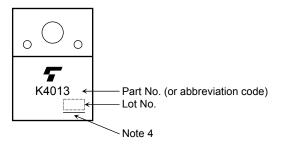
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	leakage current I _{GSS}		$V_{GS}=\pm 25~V,~V_{DS}=0~V$	_	_	±10	μA
Drain-source bre	n-source breakdown voltage V		$I_G=\pm 10~\mu A,~V_{DS}=0~V$	±30	_	_	V
Drain cut-OFF cu	ırrent	I _{DSS}			100	μA	
Drain-source bre	akdown voltage	$V_{(BR) DSS}$ I _D = 10 mA, V _{GS} = 0 V 800 —		_	_	V	
Gate threshold voltage		V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0	_	4.0	V
Drain-source ON resistance		R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}$	_	1.35	1.7	Ω
Forward transfer	admittance	Y _{fs}	$V_{DS} = 20 \text{ V}, \text{ I}_{D} = 3 \text{ A}$	2.5	5.0	_	S
Input capacitance		C _{iss}			1400	_	pF
Reverse transfer capacitance		C _{rss}	V_{DS} = 25 V, V_{GS} = 0 V, f = 1 MHz		30	_	
Output capacitance		Coss			130	_	
Switching time	Rise time	tr	V_{GS} $0 V$ C_{GS} $0 V$ C_{GS} C_{G} $V_{DD} \simeq 400 V$ $Duty \le 1\%, t_{W} = 10 \ \mu s$		25		- ns
	Turn-ON time	t _{on}			80		
	Fall time	t _f			65		
	Turn-OFF time	t _{off}		_	220		
Total gate charge (gate-source plus gate-drain)		Qg			45		
Gate-source charge		Q _{gs}	$V_{DD} \simeq 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$		25		nC
Gate-drain ("miller") charge		Q _{gd}		_	20	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	6	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	18	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 6 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 6 \text{ A}, V_{GS} = 0 \text{ V},$	_	1100	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs		10		μC

Marking

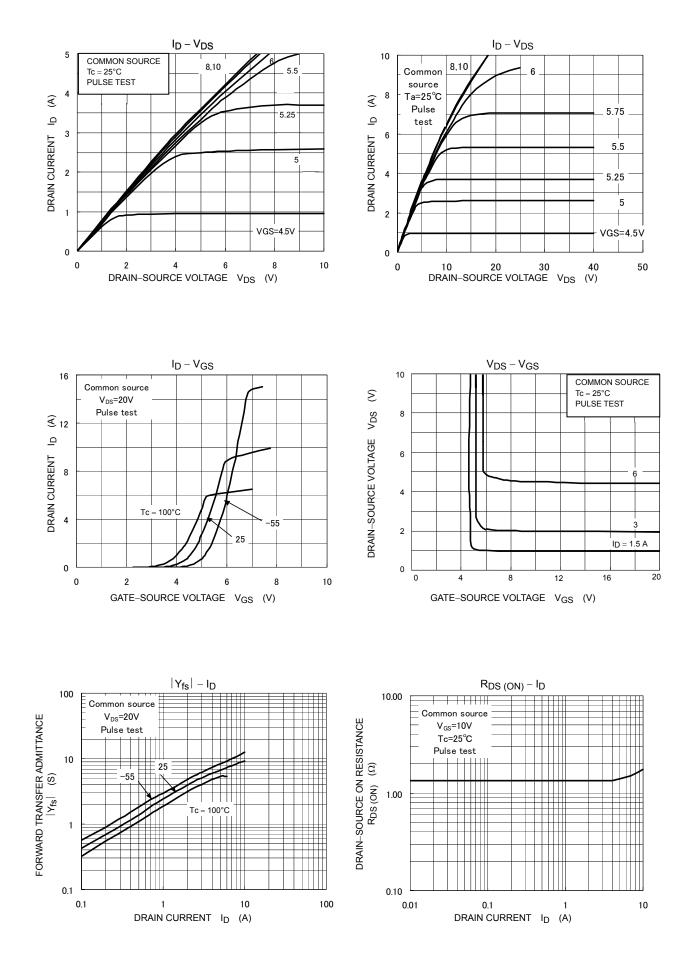


Note 4: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV

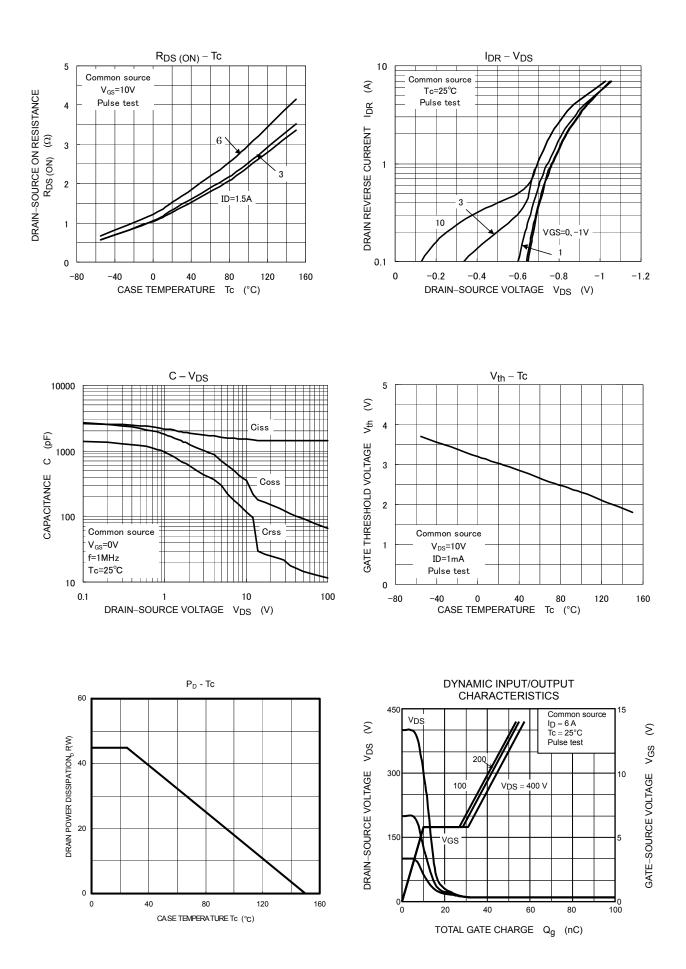
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

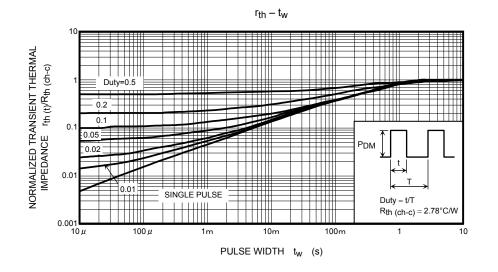
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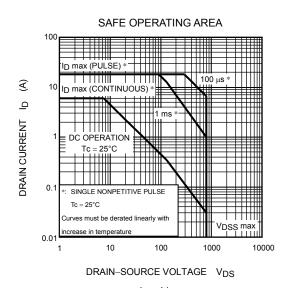
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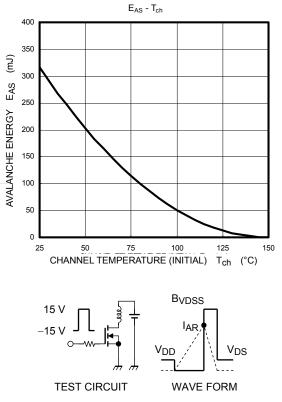


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$$R_{G} = 25 \Omega$$

$$V_{DD} = 90 \text{ V}, \text{ L} = 14.5 \text{ mH}$$

$$E_{AS} = \frac{1}{2} \cdot \text{L} \cdot \text{I}^{2} \cdot \left(\frac{\text{B}_{VDSS}}{\text{B}_{VDSS} - \text{V}_{DD}}\right)$$

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