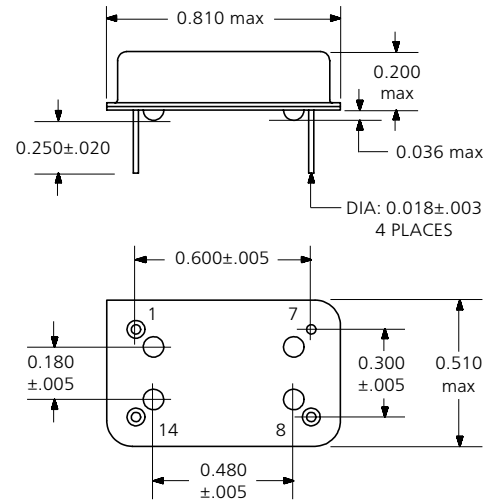
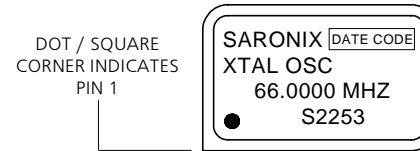


- 1) MODEL IDENTIFICATION NUMBER:** S2253 CMOS CLOCK
- 2) OPERATING TEMPERATURE RANGE:** 0°C to +70°C
- 3) STORAGE TEMPERATURE RANGE:** -55°C to +125°C
- 4) INPUT (SUPPLY) VOLTAGE:** +3.3 VDC ±10% Recommended Operating  
+7 VDC Absolute max
- 5) INPUT (SUPPLY) CURRENT:** 25mA max
- 6) OUTPUT CHARACTERISTICS (ANY COMBINATION OF OPERATING (SUPPLY) VOLTAGE, OPERATING TEMPERATURE RANGE AND LOADING)**
- a) SYMMETRY: 40/60% max at 0.5 V<sub>DD</sub>
  - b) RISE & FALL TIMES: 5 nsec max measured 20% to 80% V<sub>DD</sub>
  - c) "0" LEVEL: 10% V<sub>DD</sub> max
  - d) "1" LEVEL: 90% V<sub>DD</sub> min
  - e) OUTPUT LOAD: 15 pF
- 7) ENVIRONMENTAL:**
- a) THERMAL SHOCK: MIL-STD-883, Method 1011, Condition A
  - b) MOISTURE RESISTANCE: MIL-STD-883, Method 1004
- 8) MECHANICAL:**
- a) DIMENSIONS: Per Figure 1
  - b) SHOCK: MIL-STD-883, Method 2002, Condition B
  - c) SOLDERABILITY: MIL-STD-883, Method 2003
  - d) TERMINAL STRENGTH: MIL-STD-883, Method 2004, Test Condition B2
  - e) VIBRATION: MIL-STD-883, Method 2007, Condition A
  - f) SOLVENT RESISTANCE: MIL-STD-202, Method 215
  - g) RESISTANCE TO SOLDERING HEAT: MIL-STD-202, Method 210, Condition I or J
- 9) OPERATING FREQUENCY:** 66.0000 MHz
- 10) MODULATION CHARACTERISTICS:**
- a) OUTPUT: oscillation @ V<sub>IN</sub> ≥ 2.2V, OUTPUT: high impedance @ V<sub>IN</sub> ≤ 0.8V
  - b) INTERNAL PULLUP RESISTANCE: 50KΩ min
  - c) CONTROL INPUT → DISABLE OUTPUT DELAY: 100 nsec max
- 11) FREQUENCY STABILITY: (OVER ALL COMBINATIONS OF OPERATING TEMPERATURE, RATED INPUT (SUPPLY) VOLTAGE CHANGES, LOAD CHANGES, CALIBRATION TOLERANCE, 15-YEAR AGING, SHOCK AND VIBRATION):** ±100 ppm

STANDARD MARKING FORMAT\*



PIN CONNECTION	
1	TRISTATE CONTROL
7	GND
8	OUTPUT
14	+3.3 VDC

FIGURE 1 – PACKAGE DRAWING  
\*Exact location of items may vary

REMOVE ALL BURRS AND SHARP EDGES  
ALL DIAMETERS TO BE CONCENTRIC WITHIN .005 TIR  
TOLERANCES: XXX→+0.005 XX→-0.005 HOLE: ±.003  
DIMENSION IN: mm/inches  
ANGULAR: ±°

UNLESS OTHERWISE SPECIFIED

REVISIONS				ISSUED		DATE	<b>SaRonix</b> 141 Jefferson Drive, Menlo Park California 94025, U.S.A.
REV	DESCRIPTION	DATE	APPD	DRN			
IR	INITIAL RELEASE	98/10/09	V V	CHK	BORIS D.	98/10/01	<b>TITLE</b> ELECTRICAL, ENVIRONMENTAL MECHANICAL SPECIFICATION S2253 – SERIES
				ENG	CRAIG T.	98/10/02	
				MFG	GARY S.	98/10/05	
				QA	GARY S.	98/10/08	
<b>SIZE</b>		<b>CAGE CODE</b>		<b>DWG. NO.</b>		S2253	
<b>A</b>		<b>61441</b>		<b>SHEET</b>		<b>1 of 2</b>	

11.1) FREQUENCY STABILITY AT THE TIME OF SHPMENT:  $\pm 70$  ppm

12) DESIGN/MANUFACTURING DETAILS:

APPROVED MANUFACTURER	CIRCUIT / PART #
MENLO PARK / SKIL / KONY	NTH080B - 66 MHz

NOTE:

NO CHANGES SHALL BE MADE THAT AFFECT DESIGN, BILL OF MATERIALS, OR ASSEMBLY PROCESS, WITHOUT PRIOR AUTHORIZATION FROM SARONIX MENLO PARK. ANY REQUEST FOR CHANGE SHALL BE INITIATED BY COMPLETION OF FORM SAR-456 AT LEAST 90 DAYS PRIOR TO PRODUCT SHIPMENT AND MAY BE SUBJECT TO REQUALIFICATION (REFER TO QAP-81).

INTERNAL USE ONLY

**SaRonix** 141 Jefferson Drive, Menlo Park  
California 94025, U.S.A.

SIZE  
**A**

CAGE CODE  
**61441**

DWG. NO.

S2253

REV

IR

DATE

98/10/01

SHEET

2 of 2