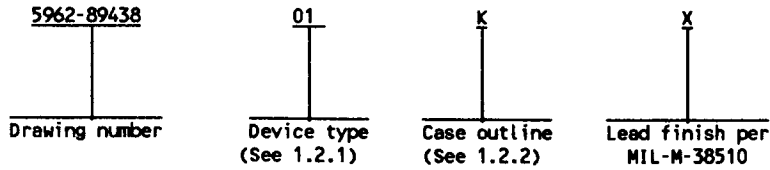


1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part or Identifying Number (PIN). The complete PIN shall be as shown in the following example:



1.2.1 Device type(s). The device type(s) shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	54F821	10-bit noninverting register

1.2.2 Case outline(s). The case outline(s) shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
K	F-6 (24-lead, .640" x .420" x .090"), flat package
L	D-9 (24-lead, 1.280" x .310" x .200"), dual-in-line package
3	C-4 (28-terminal, .460" x .460" x .100"), square chip carrier package

1.3 Absolute maximum ratings.

Supply voltage range (V_{CC})	-0.5 V dc to +7.0 V dc
Input voltage range	-0.5 V dc to +7.0 V dc
Input current range	-30 mA to +5.0 mA
Voltage applied to any output in the disabled state	-0.5 V dc to +5.5 V dc
Voltage applied to any output in the high state	-0.5 V dc to V_{CC}
Current into any output in the low state	40 mA
Storage temperature range	-65°C to +150°C
Maximum power dissipation (P_D) ^{1/}	550 mW
Lead temperature (soldering, 10 seconds)	+300°C
Junction temperature (T_J)	+175°C
Thermal resistance, junction-to-case (θ_{JC})	See MIL-M-38510, appendix C

1.4 Recommended operating conditions.

Supply voltage range (V_{CC})	+4.5 V dc to +5.5 V dc
Minimum high level input voltage (V_{IH})	2.0 V dc
Maximum low level input voltage (V_{IL})	0.8 V dc
Maximum input clamp current (I_{IC})	-18 mA
Maximum high level output current (I_{OH})	-3 mA
Maximum low level output current (I_{OL})	20 mA
Case operating temperature range (T_C)	-55°C to +125°C

^{1/} Must withstand the added P_D due to the short circuit output current test; e.g., I_{OS} .

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1.4 Recommended operating conditions. (cont.)

t_s High or Low, Dn to CP (t_s):		
$V_{CC} = 5.0$ V dc at 25°C - ^s	-----	3.0 ns
$V_{CC} = 4.5$ to 5.5 V dc at -55°C to +125°C	-----	4.0 ns
t_h HTgh or Low, Dn to CP (t_h):		
$V_{CC} = 5.0$ V dc at 25°C - ^h	-----	2.5 ns
$V_{CC} = 4.5$ to 5.5 V dc at -55°C to +125°C	-----	2.5 ns
t_w HTgh or Low, CP, $t_r = 1.0$ ns, $t_f = 1.0$ ns (t_w):		
$V_{CC} = 5.0$ V dc at 25°C	-----	5.0 ns
$V_{CC} = 4.5$ to 5.5 V dc at -55°C to +125°C	-----	6.0 ns
f_{MAX} , $t_L = 1.0$ ns, $t_F = 1.0$ ns:		
$V_{CC} = 5.0$ V dc at 25°C	-----	100 MHz
$V_{CC} = 4.5$ to 5.5 V dc at -55°C to +125°C	-----	60 MHz

2. APPLICABLE DOCUMENTS

2.1 Government specification, standard, and bulletin. Unless otherwise specified, the following specification, standard, and bulletin of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

BULLETIN

MILITARY

MIL-BUL-103 - List of Standardized Military Drawings (SMD's).

(Copies of the specification, standard, and bulletin required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAM devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.2 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.3 Truth table. The truth table shall be as specified on figure 2.

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3.2.4 Test circuit and switching waveforms. The test circuit and switching waveforms shall be specified on figure 3.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and apply over the full case operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in MIL-BUL-103 (see 6.6 herein).

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-BUL-103 (see 6.6 herein). The certificate of compliance submitted to DESC-ECC prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.7 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.8 Notification of change. Notification of change to DESC-ECC shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.9 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.6 herein).

(2) $T_A = +125^\circ\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C ≤ T _c ≤ +125°C unless otherwise specified		Group A subgroups	Limits		Units
					Min	Max	
High level output voltage	V _{OH}	V _{CC} = 4.5 V V _{IH} = 2.0 V V _{IL} = 0.8 V	I _{OH} = -3 mA	1,2,3	2.4		V
Low level output voltage	V _{OL}		I _{OL} = 20 mA	1,2,3		0.5	V
Input clamp voltage	V _{IC}		I _{IC} = -18 mA	1,2,3		-1.2	V
High level input current	I _{IH1}	V _{CC} = 5.5 V	V _{IN} = 2.7 V	1,2,3		20	μA
	I _{IH2}		V _{IN} = 7.0 V			100	
Low level input current	I _{IL}	V _{CC} = 5.5 V	V _{IN} = 0.5 V	1,2,3		-0.6	mA
Short circuit output current	I _{OS}	V _{CC} = 5.5 V V _{OUT} = 0.0 V 1/		1,2,3	-60	-150	mA
Off state output current	I _{OZH}	V _{CC} = 5.5 V V _{IH} = 2.0 V	V _{IN} = 2.7 V	1,2,3		50	μA
	I _{OZL}		V _{IN} = 0.5 V	1,2,3		-50	
Power supply current	I _{CCZ}	V _{CC} = 5.5 V		1,2,3		100	mA
Functional tests		See 4.3.1c, V _{CC} = 4.5 V, 5.5 V		7,8			

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C ≤ T _C ≤ +125°C unless otherwise specified		Group A subgroups	Limits		Units
					Min	Max	
Propagation delay time, CP to Qn	t _{PLH}	R _L = 500 ohms C _L = 50 pF see figure 3	V _{CC} = 5.0 V	9	2.0	9.5	ns
	t _{PHL}		V _{CC} = 4.5 V and 5.5 V	10,11	2.0	10.5	
Output enable time, $\overline{\text{OE}}$ to Qn	t _{PZL}		V _{CC} = 5.0 V	9	2.0	10.5	ns
	t _{PZH}		V _{CC} = 4.5 V and 5.5 V	10,11	2.0	13.0	
Output disable time, $\overline{\text{OE}}$ to Qn	t _{PLZ}	V _{CC} = 5.0 V	9	1.5	7.0	ns	
	t _{PHZ}	V _{CC} = 4.5 V and 5.5 V	10,11	1.0	7.5		

1/ Not more than one output will be shorted at one time and the duration of the short circuit condition shall not exceed one second.

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Device type	01	
Case outlines	K and L	3
Terminal numbers	Terminal symbol	
1	OE	NC
2	D0	OE
3	D1	D0
4	D2	D1
5	D3	D2
6	D4	D3
7	D5	D4
8	D6	NC
9	D7	D5
10	D8	D6
11	D9	D7
12	GND	D8
13	CP	D9
14	Q9	GND
15	Q8	NC
16	Q7	CP
17	Q6	Q9
18	Q5	Q8
19	Q4	Q7
20	Q3	Q6
21	Q2	Q5
22	Q1	NC
23	Q0	Q4
24	V _{CC}	Q3
25	---	Q2
26	---	Q1
27	---	Q0
28	---	V _{CC}

FIGURE 1. Terminal connections

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Input			Output	Function
\overline{OE}	Dn	CP	Qn	
H	L	$_ \overline{}$	Z	Load
H	H	$_ \overline{}$	Z	
H	X	H	Z	Hold
H	X	L	Z	
L	L	$_ \overline{}$	L	Data available
L	H	$_ \overline{}$	H	
L	X	H	NC	No change
L	X	L	NC	

H = High voltage level
 L = Low voltage level
 X = Irrelevant
 $_|\overline{}$ = Low to high clock transition
 NC = No change
 Z = High impedance

FIGURE 2. Truth table

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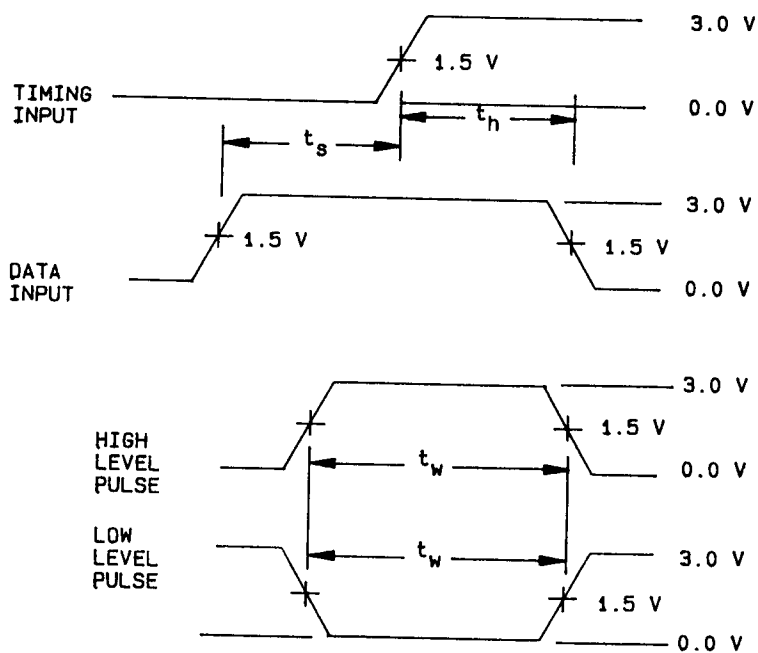
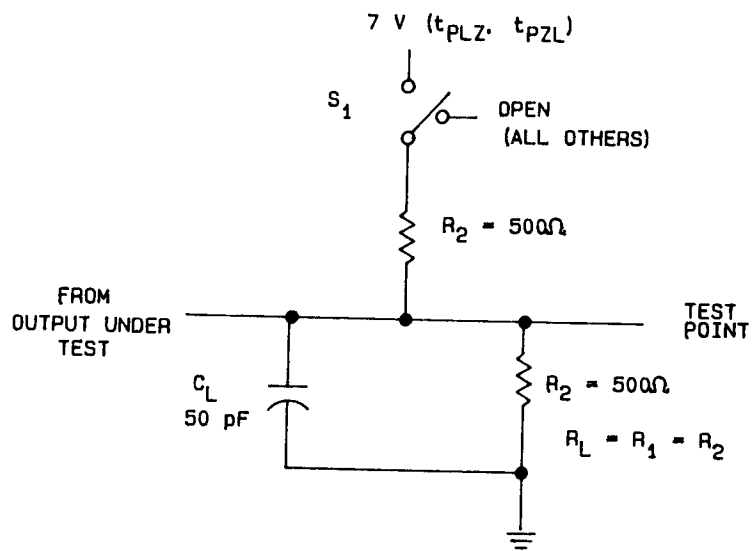
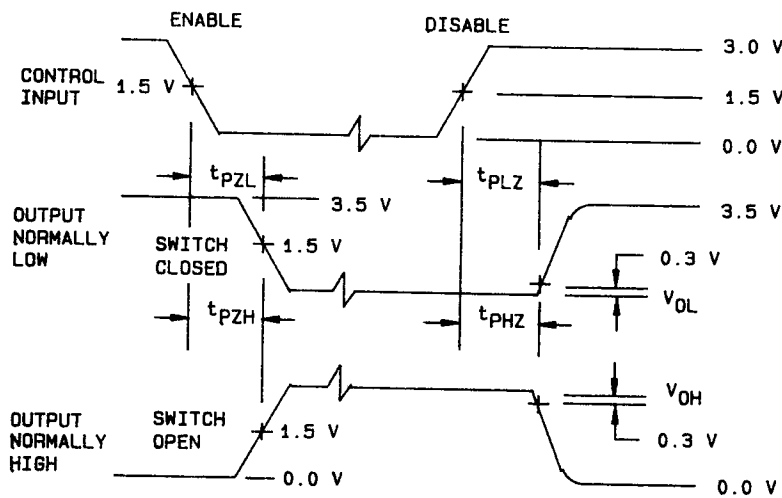
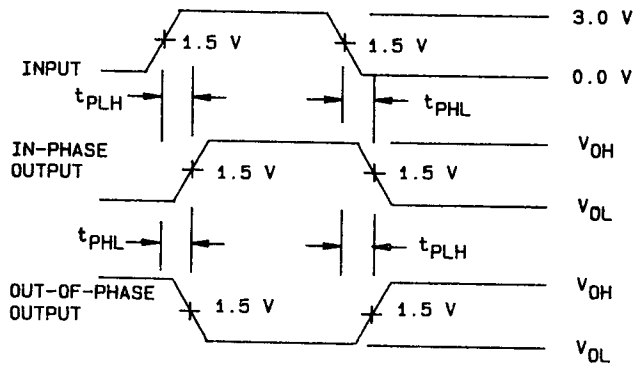


Figure 3. Test circuit and switching waveforms

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NOTES:

1. C_i includes probe and jig capacitance.
2. All input pulses have the following characteristics: PRR = 1 MHz, $t_r = t_f = 2.5$ ns, duty cycle = 50 percent.
3. When measuring propagation delay times of three-state outputs, switch 1 is open.
4. When measuring pulse widths, $t_w \leq 1$ ns, $t_r \leq 1$ ns
5. The outputs are measured one at a time with one input transition per measurement.

FIGURE 3. Test circuit and switching waveforms - continued.

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4.3.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, and 6 in table I, method 5005 of MIL-STD-883 shall be omitted.
- c. Subgroup 7 and 8 tests shall include verification of the truth table.

4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test conditions, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.6 herein).
 - (2) $T_A = +125^\circ\text{C}$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	- - -
Final electrical test parameters (method 5004)	1*, 2, 3, 7, 8, 9, 10, 11
Group A test requirements (method 5005)	1, 2, 3, 7, 8, 9, 10, 11
Groups C and D end-point electrical parameters (method 5005)	1, 2, 3

*PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

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6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).

6.4 Record of users. Military and industrial users shall inform the Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DESC-ECC, telephone (513) 296-6022.

6.5 Comments. Comments on this drawing should be directed to DESC-ECC, Dayton, Ohio 45444, or telephone 513-296-8525.

6.6 Approved sources of supply. Approved sources of supply are listed in MIL-BUL-103. The vendors listed in MIL-BUL-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DESC-ECC.

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