

Vishay Semiconductors

## Hyperfast Rectifier, 30 A FRED Pt®





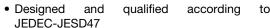
**TO-220 FULL-PAK** 

PRODUCT SUMMARY				
Package	TO-220FP			
I <sub>F(AV)</sub>	30 A			
$V_{R}$	600 V			
V <sub>F</sub> at I <sub>F</sub>	2.6 V			
t <sub>rr</sub> (typ.)	23 ns			
T <sub>J</sub> max.	175 °C			
Diode variation	Single die			

#### **FEATURES**

- Reduced Q<sub>rr</sub> and soft recovery
- 175 °C T<sub>.1</sub> maximum
- For PFC CRM/CCM operation
- Fully isolated package (V<sub>INS</sub> = 2500 V<sub>RMS</sub>)
- UL E78996 pending





 Halogen-free according to IEC 61249-2-21 definition (-N3 only)







# COMPLIANT HALOGEN FREE Available

#### **DESCRIPTION/APPLICATIONS**

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	$V_{RRM}$		600	V	
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 37 °C	30	Α	
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	220	A	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	600	-	-	
Forward voltage V <sub>F</sub>	I <sub>F</sub> = 30 A	-	2.00	2.60	V	
	I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C	-	1.34	1.75		
Reverse leakage current		$V_R = V_R$ rated	-	0.3	50	
neverse leakage current	I <sub>R</sub>	T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	60	500	μΑ
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	33	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nΗ



Reverse recovery charge

 $Q_{rr}$ 

T<sub>J</sub> = 125 °C

# **VS-30ETH06FP-F3, VS-30ETH06FP-N3**

345

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nC

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A, } dI_F/dt = 50 \text{ A/}\mu\text{s, } V_R = 30 \text{ V}$		=	28	35	
Reverse recovery time $t_{rr}$		$I_F = 1 \text{ A, } dI_F/dt = 100 \text{ A/}\mu\text{s, } V_R = 30 \text{ V}$		=	23	30	
	Lrr	T <sub>J</sub> = 25 °C		- 3-	31	-	ns
		T <sub>J</sub> = 125 °C		-	77	-	
Peak recovery current I <sub>RRM</sub>		T <sub>J</sub> = 25 °C	I <sub>F</sub> = 30 A	-	3.5	-	^
	T <sub>J</sub> = 125 °C	dl <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 200 V	-	7.7	-	A	
D		T <sub>J</sub> = 25 °C		-	65	-	. 0

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	-	2.85	
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.2	-	
Woight			-	2	-	g
Weight			-	0.07	-	oz.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220 FULL-PAK		30ETH	H06FP	



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#### www.vishay.com

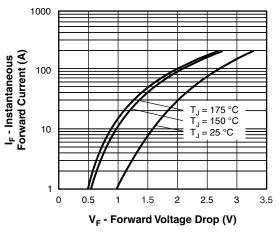


Fig. 1 - Typical Forward Voltage Drop Characteristics

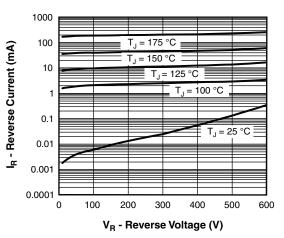


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

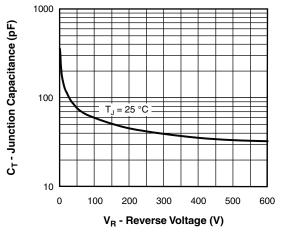


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

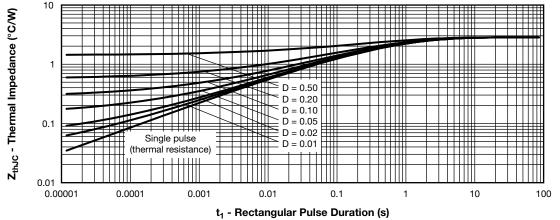


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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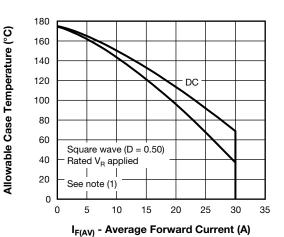


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

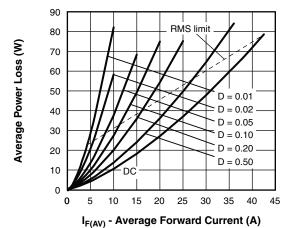


Fig. 6 - Forward Power Loss Characteristics

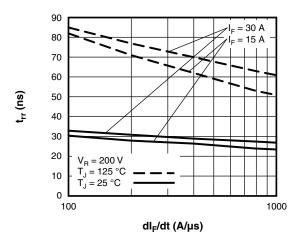


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

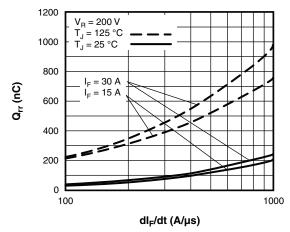


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

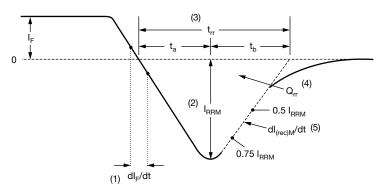
 $^{(1)}$  Formula used:  $T_{C} = T_{J}$  - (Pd + Pd\_{REV}) x  $R_{thJC};$  $Pd = Forward\ power\ loss = I_{F(AV)}\ x\ V_{FM}\ at\ (I_{F(AV)}/D)\ (see\ fig.\ 6);$   $Pd_{REV} = Inverse\ power\ loss = V_{R1}\ x\ I_R\ (1\ -\ D);\ I_R\ at\ V_{R1} = Rated\ V_R$ 



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# $V_{R} = 200 \text{ V}$ $L = 70 \, \mu\text{H}$ $O.01 \, \Omega$ O

Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{\mathrm{RRM}}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $dl_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

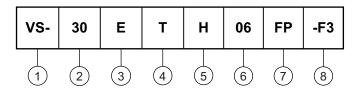
Fig. 10 - Reverse Recovery Waveform and Definitions



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#### **ORDERING INFORMATION TABLE**

**Device code** 



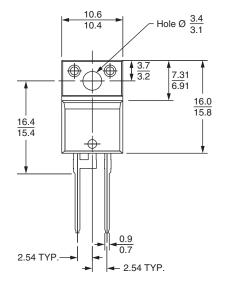
- 1 Vishay Semiconductors product
- 2 Current rating (30 A)
- 3 E = Single diode
- 4 T = TO-220
- 5 H = Hyperfast recovery
- 6 Voltage rating (06 = 600 V)
- 7 FULL-PAK
- 8 Environmental digit:
  - -F3 = RoHS compliant and totally lead (Pb)-free
  - -N3 = Halogen-free, RoHS compliant and totally lead (Pb)-free

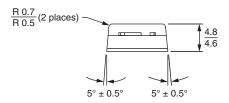
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-30ETH06FP-F3	50	1000	Antistatic plastic tube		
VS-30ETH06FP-N3	50	1000	Antistatic plastic tube		

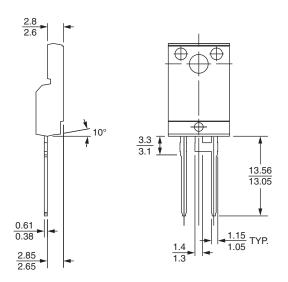
LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95005				
Part marking information	www.vishay.com/doc?95440			

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#### **DIMENSIONS** in millimeters







#### Lead assignments

<u>Diodes</u> 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220 FULL-PAK



#### **Legal Disclaimer Notice**

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