

Data Sheet	January 2002

8A, 1000V Hyperfast Diode

The RHRP8100 is a hyperfast diode with soft recovery characteristic (t_{rr} < 60ns). It has half the recovery time of ultrafast diodes and is of silicon nitride passivated ion-implanted epitaxial planar construction.

This device is intended for use as freewheeling/clamping diode and rectifier in a variety of switching power supplies and other power switching applications. Its low stored charge and hyperfast soft recovery minimize ringing and electrical noise in many power switching circuits, thus reducing power loss in the switching transistors.

Formerly developmental type TA49060.

Ordering Information

PART NUMBER	PACKAGE	BRAND
RHRP8100	TO-220AC	RHRP8100

NOTE: When ordering, use the entire part number.

Symbol



Features

•	Hyperfast with Soft Recovery	<55ns
•	Operating Temperature	175°C
•	Reverse Voltage	V000

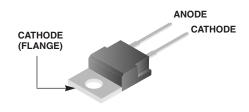
- · Avalanche Energy Rated
- Planar Construction

Applications

- · Switching Power Supplies
- · Power Switching Circuits
- General Purpose

Packaging

JEDEC TO-220AC



Absolute Maximum Ratings T_C = 25°C, Unless Otherwise Specified

	RHRP8100	UNITS
Peak Repetitive Reverse Voltage	1000	V
Working Peak Reverse Voltage	1000	V
DC Blocking Voltage V _R	1000	V
Average Rectified Forward Current $I_{F(AV)}$ ($T_{C} = 140^{\circ}C$)	8	Α
Repetitive Peak Surge Current	16	Α
Nonrepetitive Peak Surge Current	100	Α
Maximum Power Dissipation	75	W
Avalanche Energy (See Figures 8 and 9)	20	mJ
Operating and Storage Temperature	-65 to 175	°C

Electrical Specifications $T_C = 25^{\circ}C$, Unless Otherwise Specified

SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
V _F	I _F = 8A	-	-	3.0	V
	I _F = 8A, T _C = 150°C	-	-	2.5	V
I _R	V _R = 1000V	-	-	100	μΑ
	V _R = 1000V, T _C = 150°C	-	-	500	μΑ
t _{rr}	I _F = 1A, dI _F /dt = 100A/μs	-	-	55	ns
	I _F = 8A, dI _F /dt = 100A/μs	-	-	65	ns
t _a	$I_F = 8A$, $dI_F/dt = 100A/\mu s$	-	30	-	ns
t _b	$I_F = 8A$, $dI_F/dt = 100A/\mu s$	-	20	-	ns
Q _{RR}	$I_F = 8A$, $dI_F/dt = 100A/\mu s$	-	175	-	nC
СЈ	V _R = 10V, I _F = 0A	-	30	-	pF
$R_{ heta JC}$		-	-	2.0	°C/W

DEFINITIONS

 V_F = Instantaneous forward voltage (pw = 300 μ s, D = 2%).

I_R = Instantaneous reverse current.

 t_{rr} = Reverse recovery time (Figure 9), summation of $t_a + t_b$.

 t_a = Time to reach peak reverse current (See Figure 9).

 t_b = Time from peak I_{RM} to projected zero crossing of I_{RM} based on a straight line from peak I_{RM} through 25% of I_{RM} (See Figure 9).

Q_{RR} = Reverse Recovery Charge.

 C_J = Junction Capacitance.

 $R_{\theta,JC}$ = Thermal resistance junction to case.

pw = Pulse Width.

D = Duty Cycle.

Typical Performance Curves

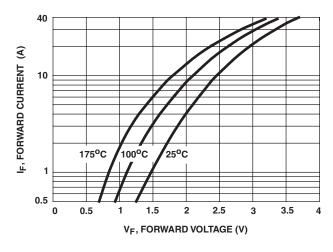


FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

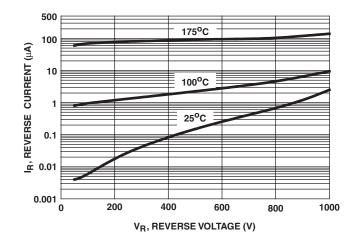


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

Typical Performance Curves (Continued)

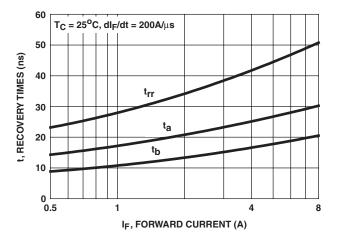


FIGURE 3. t_{rr}, t_a AND t_b CURVES vs FORWARD CURRENT

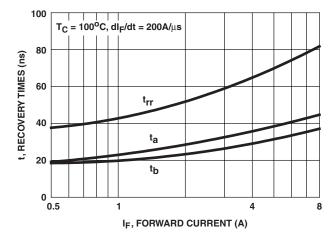


FIGURE 4. t_{rr} , t_a AND t_b curves vs FORWARD CURRENT

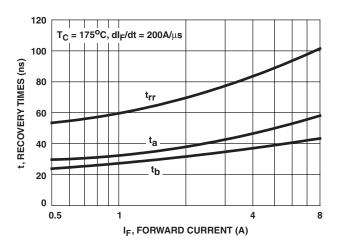


FIGURE 5. t_{rr} , t_a and t_b curves vs forward current

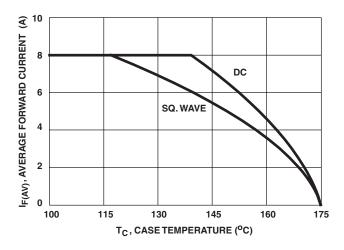


FIGURE 6. CURRENT DERATING CURVE

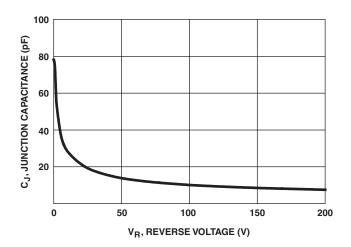


FIGURE 7. JUNCTION CAPACITANCE vs REVERSE VOLTAGE

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Test Circuits and Waveforms

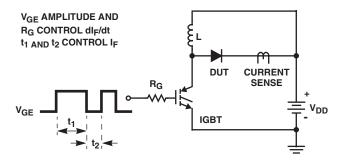


FIGURE 8. t_{rr} TEST CIRCUIT

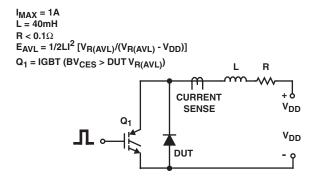


FIGURE 10. AVALANCHE ENERGY TEST CIRCUIT

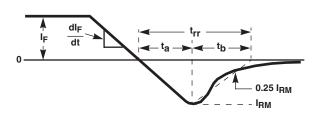


FIGURE 9. t_{rr} WAVEFORMS AND DEFINITIONS

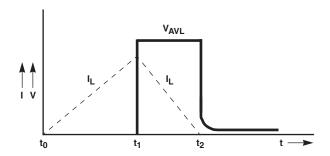


FIGURE 11. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

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Rev. H4