

UTC UNISONIC TECHNOLOGIES CO., LTD

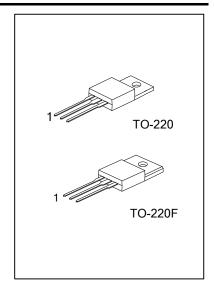
4N40 **Preliminary Power MOSFET**

4A, 400V N-CHANNEL POWER MOSFET

DESCRIPTION

The UTC 4N40 is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology specializes in allowing a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

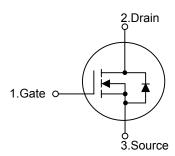
The UTC 4N40 is universally applied in electronic lamp ballast based on half bridge topology and high efficient switched mode power supply.



FEATURES

- * High switching speed
- * $R_{DS(ON)}$ =1.5 Ω @ V_{GS} =10V
- * 100% avalanche tested

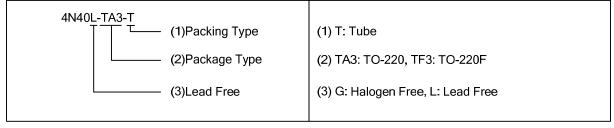
SYMBOL



ORDERING INFORMATION

Ordering Number		Dookogo	Pin	Dooking			
Lead Free	Halogen Free	Package	1	2	3	Packing	
4N40L-TA3-T	4N40G-TA3-T	TO-220	G	D	S	Tube	
4N40L-TF3-T	4N40G-TF3-T	TO-220F	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	400	٧
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous (T _C =25°C)	I_{D}	4	Α
	Pulsed (Note 1)	I_{DM}	8	Α
Peak Diode Recovery dv/dt (Note 3)		dv/dt	4.5	V/ns
Dower Dissination	TO-220		60	W
Power Dissipation	TO-220F	Б	27	W
TO-220		P_D	0.48	W/°C
Derate above 25°C	TO-220F		0.22	W/°C
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient		θ_{JA}	62.5	°C/W	
lunction to Coop	TO-220	0	2.08	°C/W	
Junction to Case	TO-220F	θ_{JC}	4.5	C/VV	

■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise specified)

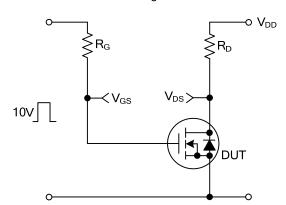
PARAMETER		SYMBOL	TEST CONDITIONS MIN		TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV _{DSS}	$I_D=250\mu A, V_{GS}=0V$				V	
Drain-Source Leakage Current		I _{DSS}	V _{DS} =400V, V _{GS} =0V			1	μΑ	
Gate- Source Leakage Current	Forward	1000	V _{GS} =+30V, V _{DS} =0V			+100	nA	
	Reverse		V_{GS} =-30V, V_{DS} =0V			-100	nA	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_{D}=250\mu A$ 2.0			4.0	V	
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =4A		1.2	1.5	Ω	
DYNAMIC PARAMETERS								
Input Capacitance		C _{ISS}				750	pF	
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =25V, f=1.0MHz			150	pF	
Reverse Transfer Capacitance		C _{RSS}				100	pF	
SWITCHING PARAMETERS								
Turn-ON Delay Time		t _{D(ON)}	V_{DD} =200V, I_{D} =4A, R_{G} =25 Ω (Note 2, 3)		12	45	ns	
Rise Time		t_R			42	60	ns	
Turn-OFF Delay Time		t _{D(OFF)}			130	200	ns	
Fall-Time		t_{F}			62	100	ns	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Drain-Source Diode Forward Voltage		V_{SD}	I _S =2A, V _{GS} =0V			1.4	V	
Body Diode Reverse Recovery Time		t _{rr}	I_S =4A, V_{GS} =0V, dI_F/dt =100A/ μ s(Note 2)		800		ns	

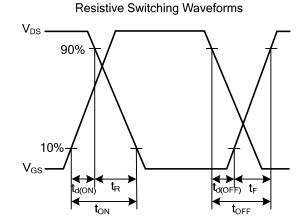
Notes: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

- 2. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%
- 3. Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

Resistive Switching Test Circuit





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