

XP0D873 (XP1D873)

Silicon N-channel junction FET

For analog switching

■ Features

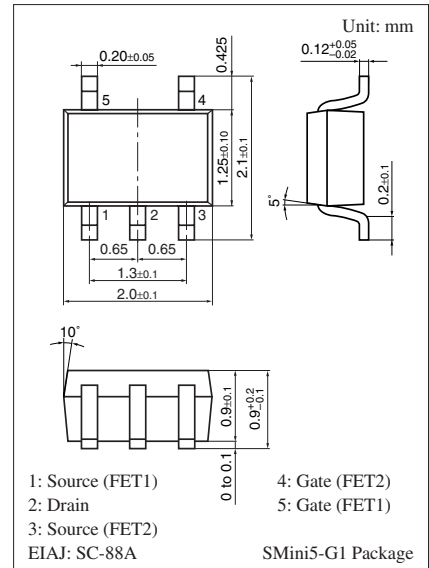
- Two elements incorporated into one package
- Reduction of the mounting area and assembly cost by one half

■ Basic Part Number

- 2SK1103 × 2

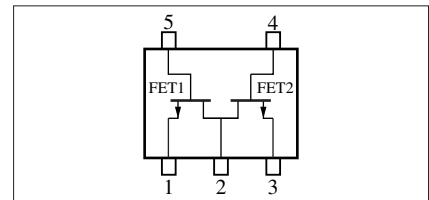
■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Gate-drain surrender voltage	V_{GDS}	-50	V
Drain current	I_D	30	mA
Gate current	I_G	10	mA
Total power dissipation	P_T	150	mW
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$



Marking Symbol: OC

Internal Connection

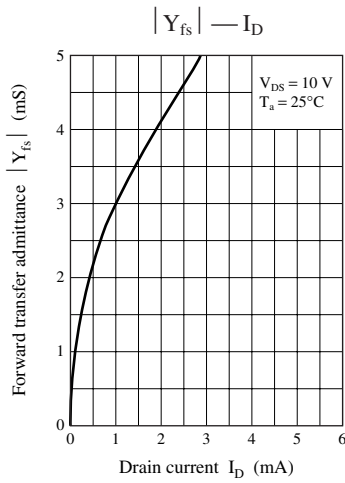
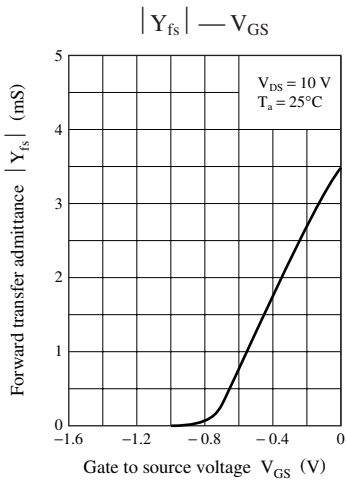
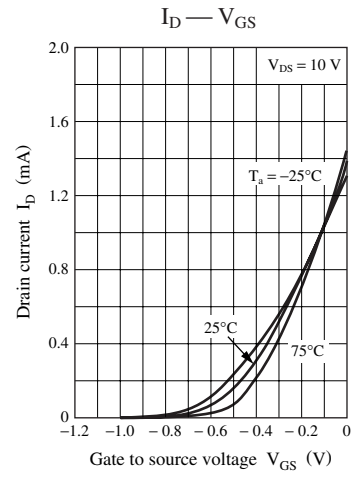
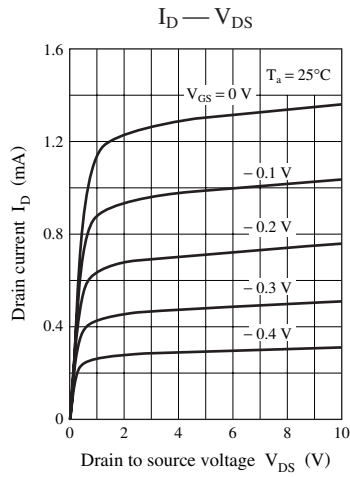
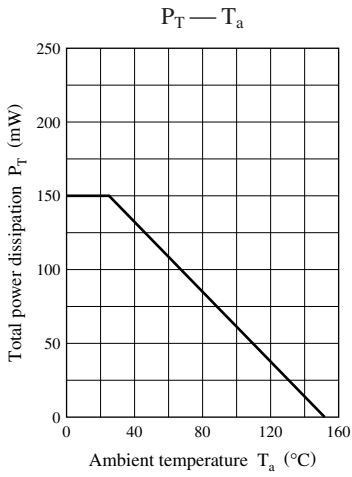


■ Electrical Characteristics $T_a = 25^\circ\text{C} \pm 3^\circ\text{C}$

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Gate-drain surrender voltage	V_{GDS}	$I_C = -10 \mu\text{A}$, $V_{DS} = 0$	-50			V
Drain-source cutoff current	I_{DSS}	$V_{DS} = 10 \text{ V}$, $V_{GS} = 0$	0.2		6.0	mA
Gate-source cutoff current	I_{GSS}	$V_{GS} = -30 \text{ V}$, $V_{DS} = 0$			-10	nA
Gate-source cutoff voltage	V_{GSC}	$V_{DS} = 10 \text{ V}$, $I_D = 10 \mu\text{A}$		-1.5	-3.5	V
Drain-source ON resistance	$R_{DS(on)}$	$V_{DS} = 10 \text{ mV}$, $V_{GS} = 0$		300		Ω
Mutual conductance	gm	$V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$, $f = 1 \text{ kHz}$	1.8	2.5		mS
Short-circuit forward transfer capacitance (Common-source)	C_{iss}	$V_{DS} = 10 \text{ V}$, $V_{GS} = 0$, $f = 1 \text{ MHz}$		7		pF
Short-circuit output capacitance (Common-source)	C_{oss}			1.5		pF
Reverse transfer capacitance (Common-source)	C_{rss}			1.5		pF

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

Note) The part number in the parenthesis shows conventional part number.



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