Class A Amplifier with 3 Independent Gain Blocks

LC508 DATA SHEET

FEATURES

- 1 μV input referred noise
- 1.0 to 5 VDC operating range
- 73 dB typical gain (adjustable)
- . 0.28 to 2.0 mA range of transducer current
- 1% electrical distortion
- the first and second blocks, or second and third blocks can be DC coupled
- 100 Hz to 50 kHz frequency response
- · suitable for active filtering

STANDARD PACKAGING

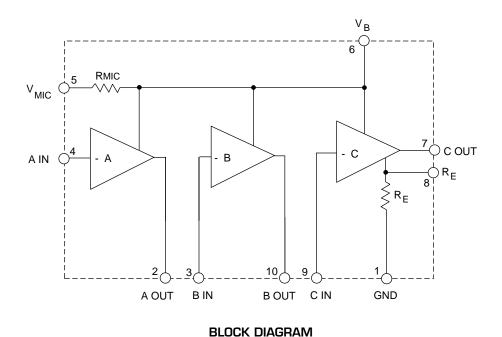
- 10 PIN MICROpac
- •10 pin PLID®
- 10 pin SLT
- Chip (52 x 49 mils)

DESCRIPTION

The LC508 is a 10 pin Class A amplifier utilizing Gennum's proprietary low voltage bipolar JFET technology. It consists of 3 single ended, low noise inverting gain blocks. The first two blocks have a typical open loop gain of 50 dB. The closed loop gain is set by the ratio of the feedback resistor to the source impedance. The third block is an open collector output stage with the bias being set by R_F and V_{RF} at pin 8 which is 54 mV.

Typically, the gain of the first two blocks is set to 25 dB each, with the third block at 23 dB, giving a total gain of 73 dB.

Gain trim can be accomplished with the use of a feedback resistor on the first block, while the volume control is used as the feedback control on the second block. This gives a volume control range greater than 40 dB.

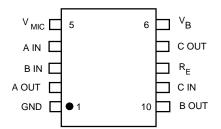


Revision Date: January 2001 Document No. 500 - 61 - 12

ABSOLUTE MAXIMUM RATINGS

Storage Temperature	-20° to + 70°C			
Operating Temperature	-10° to + 40° C			
Power Dissipation	25 mW			
Supply Voltage	5V DC			
PARAMETER	VALUE / UNITS			

PIN CONNECTION



ELECTRICAL CHARACTERISTICS

CLASS 1 ESD SENSITIVITY

Conditions: Supply Voltage =1.3 VDC, Frequency = 1 kHz, Temperature = $25\,^{\circ}$ C

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Gain (Closed Loop)	A_{CL}	V _{OUT} = 500 VRMS	69	73	77	dB
Amplifier Current	I_{AMP}	$I_{AMP} = I_A + I_{MIC}$	160	245	340	μА
Transducer Current	I _{TRANS H}	R _E = 47.5	1.1	1.3	1.7	mA
Transducer Current	I _{TRANS L}	R _E = ∞	200	275	350	μА
Distortion	THD	V _{OUT} = 500 VRMS	-	1	4	%
Input Referred Noise	IRN	NFB 0.2 to 10kHz at 12dB/Oct	-	1	2	μV
Stable with Battery Resistance to	R _B		-	-	22	Ω
Input Bias Current	I _B		-50	0	50	nA
On Chip Emitter Resistor	R_{E}		-	200	-	Ω
Emitter Bias Voltage (pin 8)	V _{RE}		-	54	-	mV
Microphone Decoupling Resistor	R _{MIC}		-	4	-	kΩ

All switches and parameters remain as shown in Test Circuit unless otherwise stated in Conditions column.

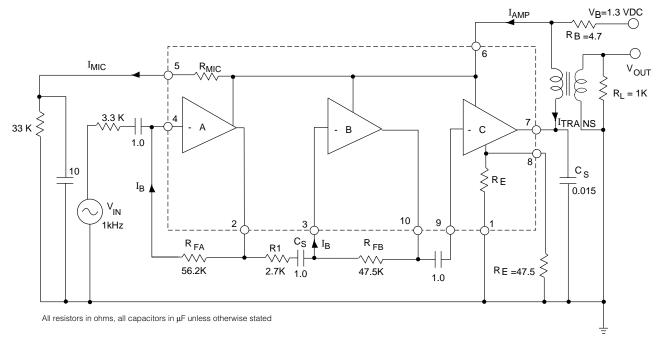


Fig. 1 Test Circuit

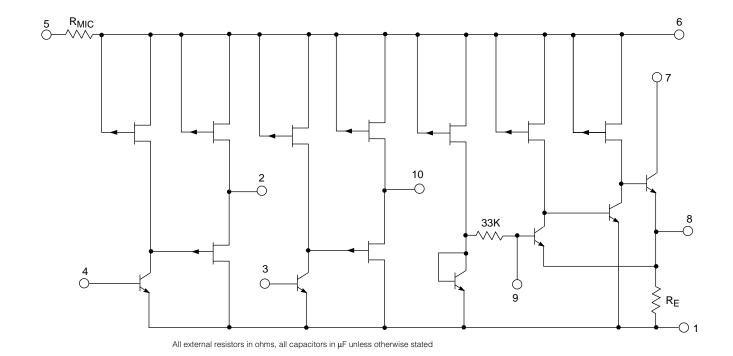


Fig. 2 Functional Schematic

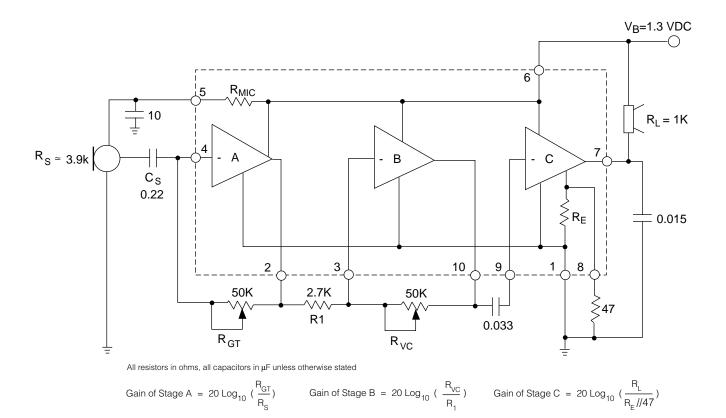


Fig. 3 Typical Hearing Aid Application

3

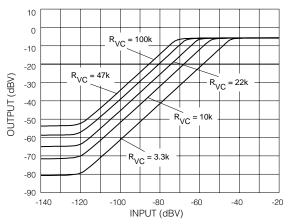


Fig. 4 $\,$ I/O Characteristics at Various R $_{
m VC}$ Values

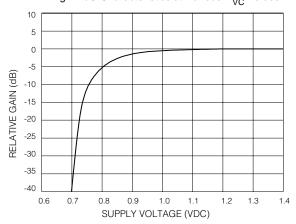


Fig.6 Gain vs Supply Voltage

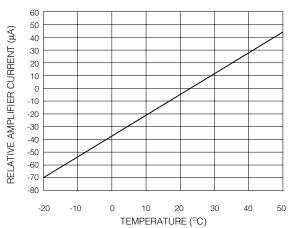


Fig. 8 Amplifier Current vs Temperature

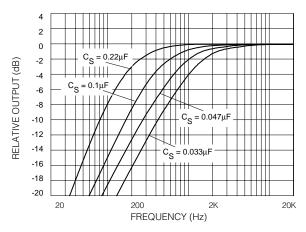


Fig. 5 Frequency Response at Various $\mathbf{C}_{\mathbf{S}}$ Values

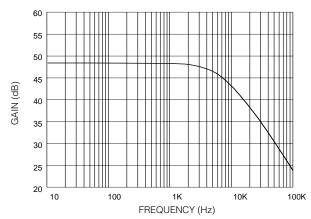


Fig.7 Preamplifier A Open Loop Frequency Response

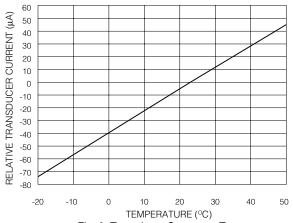


Fig. 9 Transducer Current vs Temperature

Printed in Canada.

GENNUM CORPORATION

MAILING ADDRESS:

P.O. Box 489, Stn. A, Burlington, Ontario, Canada L7R 3Y3
Tel. +1 (905) 632-2996 Fax +1 (905) 632-2814

SHIPPING ADDRESS:

970 Fraser Drive, Burlington, Ontario, Canada L7L 5P5

GENNUM JAPAN CORPORATION

C-101, Miyamae Village, 2-10-42 Miyamae, Suginami-ku, Tokyo 168-0081, Japan Tel. +81 (3) 3334-7700 Fax: +81 (3) 3247-8839

DOCUMENT IDENTIFICATION: DATA SHEET

The product is in production. Gennum reserves the right to make changes at any time to improve reliability, function or design, in order to provide the best product possible.

REVISION NOTES:

Changes to standard packaging information