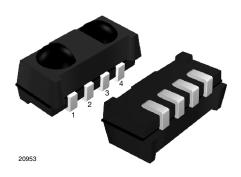
COMPLIANT GREEN \*\*

(5-2008)



### Vishay Semiconductors

### **IR Receiver Modules for Remote Control Systems**



#### **MECHANICAL DATA**

#### Pinning:

1, 4 = GND,  $2 = V_S$ , 3 = OUT

### **FEATURES**

- Continuous data transmission possible
- Very low supply current
- Photo detector and preamplifier in one package
- Internal filter for PCM frequency
- Supply voltage: 2.7 V to 5.5 V
- Improved immunity against ambient light
- · Capable of side or top view
- Insensitive to supply voltage ripple and noise
- Two lenses for high sensitivity and wide receiving angle
- Narrow optical filter to reduce interference from plasma TV emissions
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

### **DESCRIPTION**

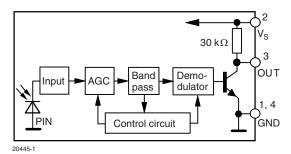
The TSOP773.. is a two lens miniaturized receiver module for infrared remote control systems. One PIN diode per lens and a preamplifier are assembled on a leadframe, the epoxy lens cap is designed as an IR filter.

The demodulated output signal can be directly decoded by a microprocessor. The TSOP773.. is optimized to better suppress spurious pulses from fluorescent lamps, LCD, TVs, or plasma displays.

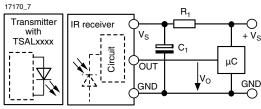
This component has not been qualified according to automotive specifications.

PARTS TABLE				
CARRIER FREQUENCY	NOISY ENVIRONMENTS AND SHORT BURSTS (AGC3)			
30 kHz	TSOP77330			
33 kHz	TSOP77333			
36 kHz	TSOP77336			
38 kHz	TSOP77338			
40 kHz	TSOP77340			
56 kHz	TSOP77356			

#### **BLOCK DIAGRAM**



#### **APPLICATION CIRCUIT**



The external components R<sub>1</sub> and C<sub>1</sub> are optional to improve the robustness against electrical overstress (typical values are  $R_1 = 100 \Omega$ ,  $C_1 = 0.1 \mu F$ ). The output voltage  $V_o$  should not be pulled down to a level below 1 V by the external circuit.

The capacitive load at the output should be less than 2 nF.

<sup>\*\*</sup> Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

# IR Receiver Modules for Remote Control Systems



ABSOLUTE MAXIMUM RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		V <sub>S</sub>	- 0.3 to + 6	V
Supply current		I <sub>S</sub>	5	mA
Output voltage		Vo	- 0.3 to (V <sub>S</sub> + 0.3)	V
Output current		Io	5	mA
Junction temperature		Tj	100	°C
Storage temperature range		T <sub>stg</sub>	- 25 to + 85	°C
Operating temperature range		T <sub>amb</sub>	- 25 to + 85	°C
Power consumption	T <sub>amb</sub> ≤ 85 °C	P <sub>tot</sub>	10	mW

#### Note

• Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

<b>ELECTRICAL AND OPTICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply current	$E_{V} = 0, V_{S} = 5 V$	I <sub>SD</sub>	0.65	0.85	1.05	mA
	$E_v = 40 \text{ klx, sunlight}$	I <sub>SH</sub>		0.95		mA
Supply voltage		Vs	2.7		5.5	V
Transmission distance	$E_{v}$ = 0, test signal see fig. 1, IR diode TSAL6200, $I_{F}$ = 400 mA	d		40		m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2,$ test signal see fig. 1	V <sub>OSL</sub>			100	mV
Minimum irradiance	Pulse width tolerance: $t_{pi}$ - 5/ $f_o$ < $t_{po}$ < $t_{pi}$ + 6/ $f_o$ , test signal see fig. 1	E <sub>e min.</sub>		0.3	0.45	mW/m²
Maximum irradiance	$t_{pi}$ - 5/f <sub>o</sub> < $t_{po}$ < $t_{pi}$ + 6/f <sub>o</sub> , test signal see fig. 1	E <sub>e max.</sub>	30			W/m <sup>2</sup>
Directivity	Angle of half transmission distance	Ψ1/2		± 50		deg

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

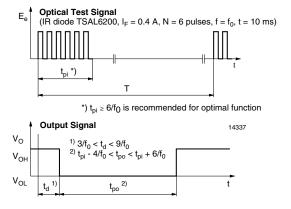


Fig. 1 - Output Active Low

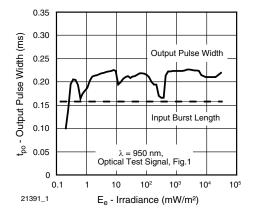


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient



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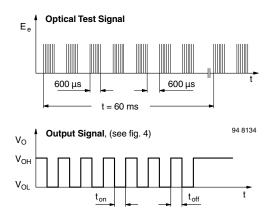


Fig. 3 - Output Function

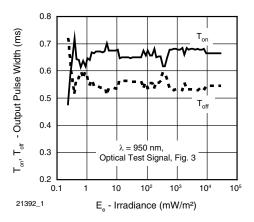


Fig. 4 - Output Pulse Diagram

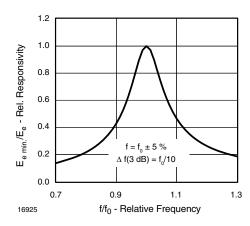


Fig. 5 - Frequency Dependence of Responsivity

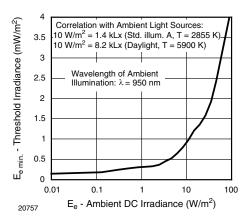
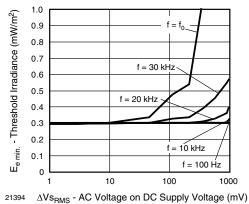


Fig. 6 - Sensitivity in Bright Ambient



Trongs he voltage on Be capply voltage (iiiv)

Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

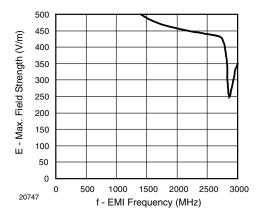


Fig. 8 - Sensitivity vs. Electric Field Disturbances

### IR Receiver Modules for Remote Control Systems



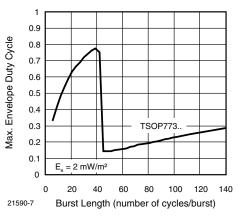


Fig. 9 - Max. Envelope Duty Cycle vs. Burst Length

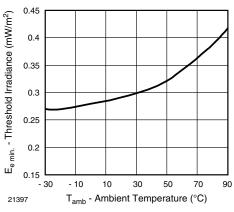


Fig. 10 - Sensitivity vs. Ambient Temperature

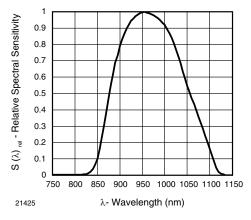


Fig. 11 - Relative Spectral Sensitivity vs. Wavelength

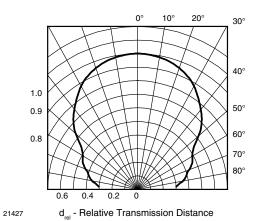


Fig. 12 - Horizontal Directivity

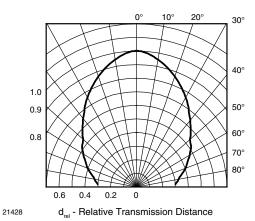


Fig. 13 - Vertical Directivity

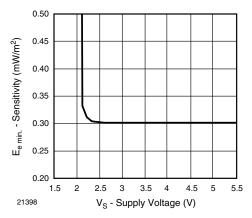


Fig. 14 - Sensitivity vs. Supply Voltage



## Vishay Semiconductors

### **SUITABLE DATA FORMAT**

The TSOP773.. is designed to suppress spurious output pulses due to noise or disturbance signals. Data and disturbance signals can be distinguished by the devices according to carrier frequency, burst length and envelope duty cycle. The data signal should be close to the band-pass center frequency (e.g. 40 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the TSOP773.. in the presence of a disturbance signal, the sensitivity of the receiver is reduced to insure that no spurious pulses are present at the output. Some examples of disturbance signals which are suppressed are:

- DC light (e.g. from tungsten bulb or sunlight)
- Continuous signals at any frequency
- Strongly or weakly modulated noise from fluorescent lamps with electronic ballasts (see fig. 15 or fig. 16)

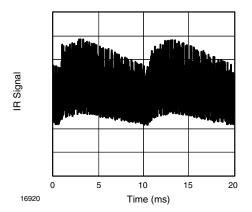


Fig. 15 - IR Signal from Fluorescent Lamp with Low Modulation

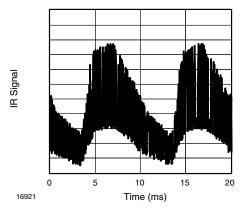


Fig. 16 - IR Signal from Fluorescent Lamp with High Modulation

	TSOP773
Minimum burst length	6 cycles/burst
After each burst of length a minimum gap time is required of	6 to 35 cycles ≥ 10 cycles
For bursts greater than a minimum gap time in the data stream is needed of	35 cycles > 6 x burst length
Maximum number of continuous short bursts/second	2000
Recommended for NEC code	yes
Recommended for RC5/RC6 code	yes
Recommended for Sony code	yes
Recommended for RECS-80 code	yes
Recommended for RCMM code	yes
Recommended for r-step code	yes
Recommended for XMP code	yes
Suppression of interference from fluorescent lamps	Even critical disturbance signals are suppressed (e.g. waveform of figure 15)

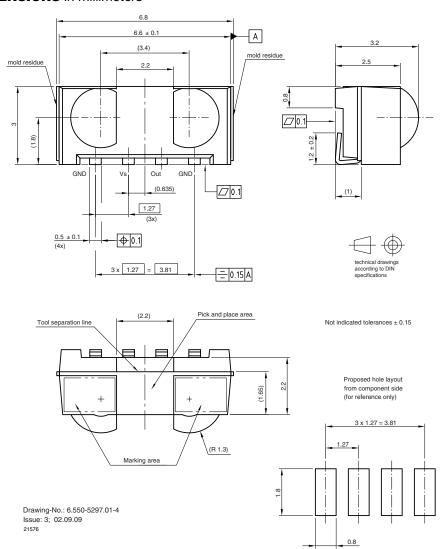
#### Note

• For data formats with long bursts (10 carrier cycles or longer) we recommend the TSOP772.., TSOP774.. because of the better noise suppression.

### IR Receiver Modules for Remote Control Systems



### **PACKAGE DIMENSIONS** in millimeters



### **ASSEMBLY INSTRUCTIONS**

### **Reflow Soldering**

- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Excercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

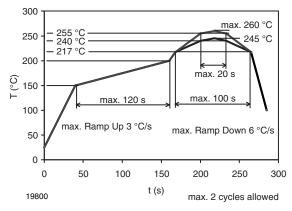
### **Manual Soldering**

- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off

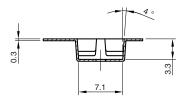


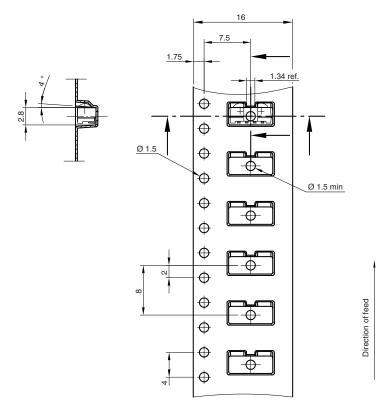
## Vishay Semiconductors

### **VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE**



### TAPING VERSION TSOP..TR DIMENSIONS in millimeters







Drawing-No.: 9.700-5337.01-4

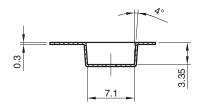
Issue: 1; 16.10.08

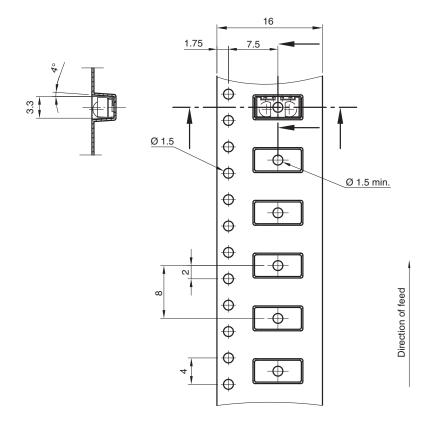
21577

### IR Receiver Modules for Remote Control Systems



### **TAPING VERSION TSOP..TT DIMENSIONS** in millimeters





technical drawings according to DIN specifications

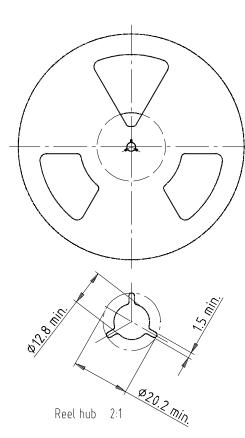
Drawing-No.: 9.700-5338.01-4 Issue: 3; 09.06.09

21578



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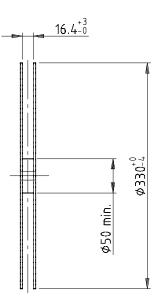
### **REEL DIMENSIONS** in millimeters



Drawing-No.: 9.800-5052.V2-4

Issue: 1; 07.05.02

16734



Form of the leave open of the wheel is supplier specific.

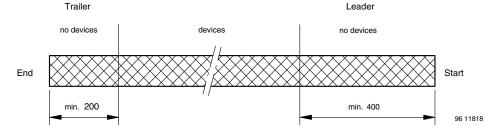
Dimension acc. to IEC EN 60 286-3

Tape width 16



technical drawings according to DIN specifications

### **LEADER AND TRAILER DIMENSIONS** in millimeters



### **COVER TAPE PEEL STRENGTH**

According to DIN EN 60286-3 0.1 N to 1.3 N  $300 \pm 10$  mm/min.  $165^{\circ}$  to  $180^{\circ}$  peel angle

### **LABEL**

### Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

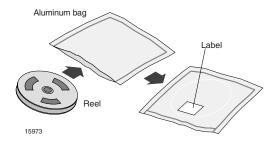
# IR Receiver Modules for Remote Control Systems



VISHAY SEMICONDUCTOR GmbH STANDARD BAR CODE PRODUCT LABEL (finished goods)			
PLAIN WRITING	ABBREVIATION	LENGTH	
Item-description	-	18	
Item-number	INO	8	
Selection-code	SEL	3	
LOT-/serial-number	BATCH	10	
Data-code	COD	3 (YWW)	
Plant-code	PTC	2	
Quantity	QTY	8	
Accepted by	ACC	-	
Packed by	PCK	-	
Mixed code indicator	MIXED CODE	-	
Origin	XXXXXXX+	Company logo	
Long bar code top	Туре	Length	
Item-number	N	8	
Plant-code	N	2	
Sequence-number	X	3	
Quantity	N	8	
Total length	-	21	
Short bar code bottom	Туре	Length	
Selection-code	X	3	
Data-code	N	3	
Batch-number	X	10	
Filter	-	1	
Total length	-	17	

#### **DRY PACKING**

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



### **FINAL PACKING**

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

### RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

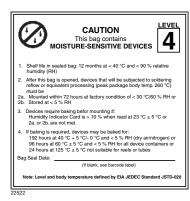
- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 72 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 125  $^{\circ}$ C + 5  $^{\circ}$ C not suitable for reel or tubes. An EIA JEDEC standard JSTD-020 level 4 label is included on all dry bags.



EIA JEDEC standard JSTD-020 level 4 is included on all dry bags



# Vishay Semiconductors

### **ESD PRECAUTION**

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

# VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



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