

# **SUR496H**

#### Epitaxial planar NPN/PNP silicon transistor

## **Description**

• Dual chip digital Transistor

#### **Features**

- Both SRC1204 chip and SRA2207 chip in SOT-353 package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

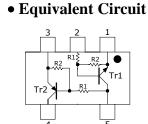


## **Ordering Information**

Type NO.	Marking	Package Code
SUR496H	<b>X7</b> □	SOT-353

□ : Year & Week Code

## **Equivalent circuit & PIN Connections**



	R <sub>1</sub>	$\mathbf{R}_2$
Tr1	47ΚΩ	47ΚΩ
Tr2	10ΚΩ	47ΚΩ

#### **PIN Connections**

- 1. COMMON 1
- 2. IN 1
- 3. COMMON 2
- 4. OUT 2
- 5. OUT 1, IN 2

## **Absolute Maximum Ratings** [Tr1,Tr2]

 $(Ta=25^{\circ}C)$ 

Characteristic	Crymbal	Rating		Unit	
Characteristic	Symbol	Tr1	Tr2	Omt	
Output voltage	Vo	50	-50	V	
Input voltage	VI	40,-10 -30,7		V	
Output current	I <sub>O</sub>	100 -100		mA	
Power dissipation	P <sub>D</sub> **	200		mW	
Junction temperature	T <sub>3</sub>	150		°C	
Storage temperature range	$T_{stg}$	-55 ~ 150		°C	

\*: Total rating

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# **Electrical Characteristics** [Tr1]

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	V <sub>0</sub> =50V, V <sub>I</sub> =0	-	1	500	nA
DC current gain	$G_{\mathrm{I}}$	V <sub>0</sub> =5V, I <sub>0</sub> =10mA	80	200	-	-
Output voltage	V <sub>O(ON)</sub>	$I_{O}$ =10mA, $I_{I}$ =0.5mA	-	0.1	0.3	V
Input voltage (ON)	$V_{I(ON)}$	V <sub>0</sub> =0.2V, I <sub>0</sub> =5mA	-	2.8	5.0	V
Input voltage (OFF)	V <sub>I(OFF)</sub>	V <sub>0</sub> =5V, I <sub>0</sub> =0.1mA	1.0	1.2	-	V
Transition frequency	$f_T^*$	$V_0$ =10V, $I_0$ =5mA, f=1MHz	-	200	ı	MHz
Input current	I <sub>I</sub>	$V_I$ =5V, $I_O$ =0	-	ı	0.18	mA
Input resistor (Input to base)	R <sub>1</sub>	-	33	47	61	ΚΩ
Input resistor (Base to common)	R <sub>2</sub>	-	33	47	61	<b>K</b> Ω

<sup>\* :</sup> Characteristic of transistor only

# **Electrical Characteristics** [Tr2]

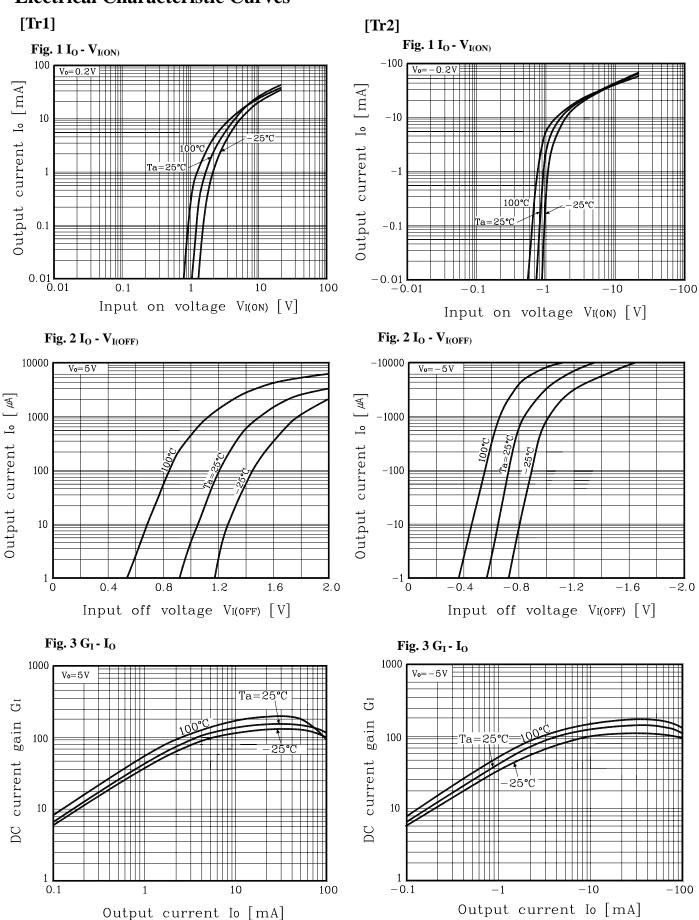
(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	V <sub>O</sub> =-50V, V <sub>I</sub> =0	-	-	-500	nA
DC current gain	$G_{\mathrm{I}}$	V <sub>O</sub> =-5V, I <sub>O</sub> =-10mA	80	150	-	-
Output voltage	V <sub>O(ON)</sub>	I <sub>O</sub> =-10mA, I <sub>I</sub> =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	V <sub>O</sub> =-0.2V, I <sub>O</sub> =-5mA	-	-	-1.8	V
Input voltage (OFF)	V <sub>I(OFF)</sub>	V <sub>O</sub> =-5V, I <sub>O</sub> =-0.1mA	-0.5	-	-	V
Transition frequency	f <sub>T</sub> *	V <sub>O</sub> =-10V, I <sub>O</sub> =-5mA, f=1MHz	-	200	-	MHz
Input current	$I_{I}$	V <sub>I</sub> =-5V, I <sub>O</sub> =0	-	-	-0.88	mA
Input resistor (Input to base)	R <sub>1</sub>	-	7	10	13	<b>K</b> Ω
Input resistor (Base to common)	R <sub>2</sub>	-	33	47	61	<b>K</b> Ω

<sup>\* :</sup> Characteristic of transistor only

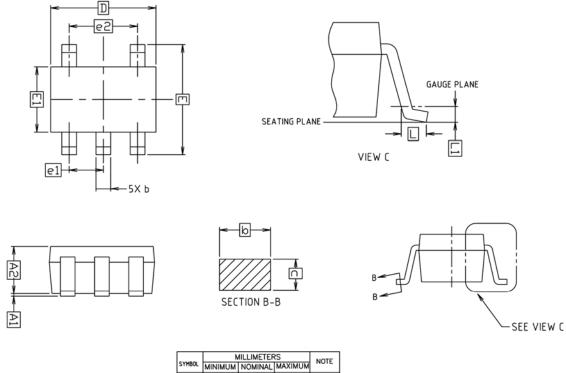
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## **Electrical Characteristic Curves**



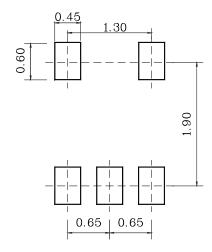
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## **Outline Dimension**



	, n	NOTE				
SYMBOL	MINIMOMINOM		MAXIMUM	NOTE		
A1	0.00	ı	0.10			
A2	0.90	0.95	1.00			
Ь	0.25	-	0.40			
С	0.10	ı	0.25			
D	1.90	2.00	2.10			
Ē	1.95	2.10	2.25			
E1	1.15	1.25	1.35			
e1						
e2						
L	0.25	_	_			
L1						

#### \* Recommend PCB solder land [Unit: mm]



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