

STPSC20H065C

650 V power Schottky silicon carbide diode

Datasheet - production data

Features

- No or negligible reverse recovery
- Switching behavior independent of temperature
- Dedicated to PFC applications
- High forward surge capability

Description

The SiC diode is an ultrahigh performance power Schottky diode. It is manufactured using a silicon carbide substrate. The wide bandgap material allows the design of a Schottky diode structure with a 650 V rating. Due to the Schottky construction, no recovery is shown at turn-off and ringing patterns are negligible. The minimal capacitive turn-off behavior is independent of temperature.

Especially suited for use in PFC applications, this ST SiC diode will boost the performance in hard switching conditions. Its high forward surge capability ensures a good robustness during transient phases.

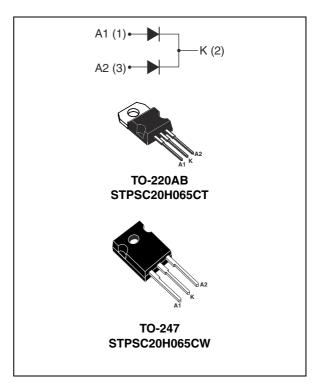


Table 1. Device summary

	_
Symbol	Value
I _{F(AV)}	2 x 10 A
V _{RRM}	650 V
T _j (max)	175 °C

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1 Characteristics

Table 2. Absolute ratings (limiting values per diode at 25 °C unless otherwise specified)

Symbol	Para	Value	Unit		
V_{RRM}	Repetitive peak reverse voltage		650	V	
I _{F(RMS)}	Forward rms current		22	Α	
1	Average forward current, $\delta = 0.5$	T _c = 120 °C, per diode	10		
^I F(AV)	Average forward current, $\delta = 0.5$	T _c = 105 °C, per device	20	A	
	Curae non repetitive femuera	$t_p = 10 \text{ ms sinusoidal, } T_c = 25 ^{\circ}\text{C}$			
I _{FSM}	Surge non repetitive forward current	$t_p = 10 \text{ ms sinusoidal}, T_c = 125 ^{\circ}\text{C}$	80	Α	
	our one	$t_p = 10 \mu s square, T_c = 25 °C$	470		
I _{FRM}	Repetitive peak forward current	$T_c = 120 \text{ °C,} T_j = 150 \text{ °C, } \delta = 0.1$	36	Α	
T _{stg}	Storage temperature range		-55 to +175	°C	
T _j	Operating junction temperature ⁽¹⁾		-40 to +175	°C	

^{1.} $\frac{dPtot}{dT_j} < \frac{1}{Rth(j-a)}$ condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal resistance (typical values)

Symbol	vimbal Davamatar			Val	Value	
Symbol			Тур.	Max.	Unit	
		Per diode	TO-247	1.25	1.5	°C/W
	Junction to case per diode	rei diode	TO-220AB	1.25		
hth(j-c)		Total	TO-247	0.83	0.95	
		Total	TO-220AB	0.63		
R _{th(c)}	R _{th(c)} Coupling			0.4		

When the two diodes 1 and 2 are used simultaneously:

 ΔT_j (diode 1) = P(diode 1) x $R_{th(j-c)}$ (Per diode) + P(diode 2) x $R_{th(c)}$

Table 4. Static electrical characteristics per diode

Symbol	Parameter	Tests conditions		Min.	Тур.	Max.	Unit
I _R ⁽¹⁾	(1) Reverse leakage current	T _j = 25 °C	$V_R = V_{RRM}$	-	9	100	μΑ
'R`		T _j = 150 °C		ı	85	425	
V_ (2)	V _F ⁽²⁾ Forward voltage drop	T _j = 25 °C	I _F = 10 A	-	1.56	1.75	V
VF \		T _j = 150 °C	1F - 10 A	ı	1.98	2.5	V

^{1.} $t_p = 10 \text{ ms}, \delta < 2\%$

To evaluate the conduction losses use the following equation:

$$P = 1.35 \text{ x } I_{F(AV)} + 0.115 \text{ x } I_{F^{2}(RMS)}$$

^{2.} $t_p = 500 \ \mu s, \ \delta < 2\%$

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Table 5. Dynamic electrical characteristics per diode

Symbol	Parameter	Test conditions	Тур.	Unit
Q _{cj} ⁽¹⁾	Total capacitive charge	V _R = 400 V	28.5	nC
Ci	Total capacitance	$V_R = 0 \text{ V}, T_c = 25 \text{ °C}, F = 1 \text{ MHz}$	480	ρF
l O _j	oj Total capacitance	$V_R = 400 \text{ V}, T_c = 25 \text{ °C}, F = 1 \text{ MHz}$	48	рг

^{1.} Most accurate value for the capacitive charge: $Q_{cj} = \int_{0}^{V_{OUT}} c_{j}^{V_{OUT}} dV_{R}$

Figure 1. Forward voltage drop versus forward current (typical values per diode, low level)

Figure 2. Forward voltage drop versus forward current (typical values per diode, high level)

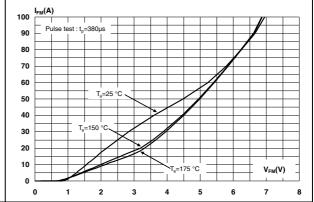
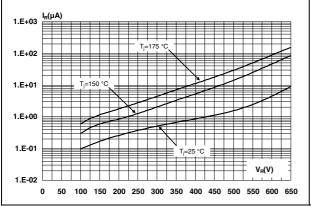
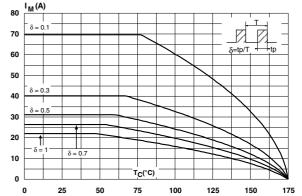


Figure 3. Reverse leakage current versus reverse voltage applied (typical values per diode)

Figure 4. Peak forward current versus case temperature, per diode





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Figure 5. Junction capacitance versus reverse voltage applied (typical values, per diode)

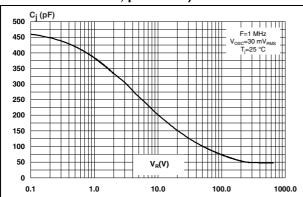


Figure 6. Relative variation of thermal impedance junction to case versus pulse duration per diode

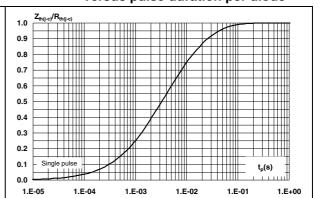
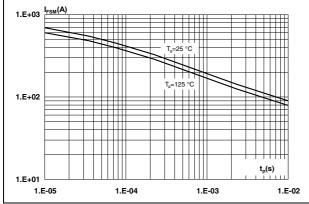
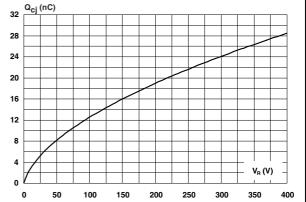


Figure 7. Non-repetitive peak surge forward current versus pulse duration per diode (sinusoidal waveform)

Total capacitive charges versus Figure 8. reverse voltage applied (typical values per diode) 32 Q_{cj} (nC) 24



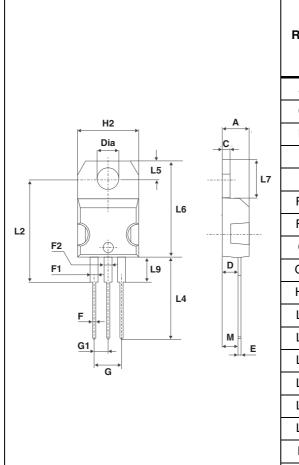


2 Package information

- Epoxy meets UL94, V0
- Cooling method: conduction (C)
- Recommended torque value:
 - TO-220AB 0.4 to 0.6 N⋅m,
 - TO-247 0.55 N⋅m (1.0 N⋅m maximum)

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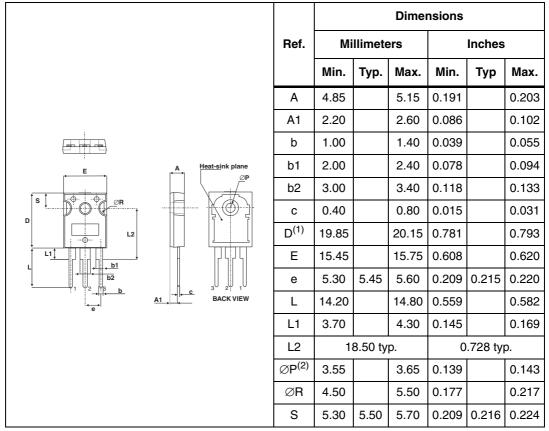
TO-220AB dimensions



	Dimensions				
Ref.	Millimeters		Inc	hes	
	Min.	Max.	Min.	Max.	
Α	4.40	4.60	0.173	0.181	
С	1.23	1.32	0.048	0.051	
D	2.40	2.72	0.094	0.107	
Е	0.49	0.70	0.019	0.027	
F	0.61	0.88	0.024	0.034	
F1	1.14	1.70	0.044	0.066	
F2	1.14	1.70	0.044	0.066	
G	4.95	5.15	0.194	0.202	
G1	2.40	2.70	0.094	0.106	
H2	10	10.40	0.393	0.409	
L2	16.4	typ.	0.645 typ.		
L4	13	14	0.511	0.551	
L5	2.65	2.95	0.104	0.116	
L6	15.25	15.75	0.600	0.620	
L7	6.20	6.60	0.244	0.259	
L9	3.50	3.93	0.137	0.154	
М	2.6	typ.	0.102	2 typ.	
Diam.	3.75	3.85	0.147	0.151	

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Table 6. TO-247 dimensions



- 1. Dimension D plus gate protrusion does not exceed 20.5 mm
- 2. Resin thickness around the mounting hole is not less than 0.9 \mbox{mm}

3 Ordering information

Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPSC20H065CT	STPSC20H065C	TO-220AB	1.86 g	50	Tube
STPSC20H065CW	STPSC20H065CW	TO-247	4.43 g	30	Tube

4 Revision history

Table 8. Document revision history

Date	Revision	Changes
31-Aug-2012	1	First issue.
10-Oct-2012	2	Added Max. values to <i>Table 3</i> .

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