



Linear Systems replaces discontinued Siliconix J506

The Linear Systems LSJ506 is a ± 20% range current regulator

The LSJ506 is a ±20% range current regulator designed for demanding applications in test equipment and instrumentation. The LSJ506 utilizes JFET techniques to produce a single twoleaded device which is extremely simple to operate.

- Two-Lead Plastic Package
- Guaranteed ±20% Tolerance
- Operation up to 50V
- **Excellent Temperature Stability**
- Simple Series Circuitry, No Separate Voltage Source
- Tight Guaranteed Circuit Performance
- Excellent Performance in Low-Voltage/Battery Circuits and High-Voltage Spike Protection
- High Circuit Stability vs. Temperature

LSJ506	Applicatio	ns:
--------	------------	-----

- Constant-Current Supply
- Current-Limiting
- **Timing Circuits**

FEATURES					
REPLACEMENT SOURCE FOR SILICONIX J506					
WIDE CURRENT RANGE 1.40mA ± 20%					
BIASING NOT REQUIRED	$V_{GS} = 0V$				
ABSOLUTE MAXIMUM RATINGS ¹					
@ 25 °C (unless otherwise stated)					
Maximum Temperatures					
Storage Temperature	-55 to 150°C				
Junction Operating Temperature	-55 to 135°C				
Maximum Power Dissipation					
Continuous Power Dissipation @125°C	360mW				
Maximum Currents					
Forward Current	20mA				
Reverse Current	50mA				
Maximum Voltages					
Peak Operating Voltage	P _{OV} = 50V				

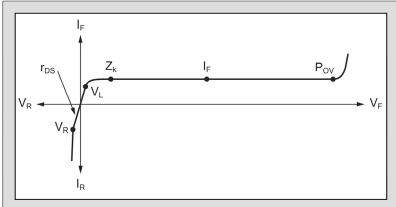
ELECTRICAL CHARACTERISTICS @ 25 °C (unless otherwise stated)

SYMBOL	CHARACTERISTIC	MIN	TYP	MAX	UNITS	CONDITIONS
Pov	Peak Operating Voltage ²	50			V	$I_{F} = 1.1I_{F(max)}$
V_R	Reverse Voltage		0.8		V	$I_R = 1mA$
C _F	Forward Capacitance		2.2		рF	V _F = 25V, <i>f</i> = 1MHz

SPECIFIC ELECTRICAL CHARACTERISTICS @ 25 °C (unless otherwise stated)

PART	Forward Current ³ I _F		Dynamic Impedance ⁴ Z _d		Knee Impedance Z _k	Limiting Voltage ⁵ V _L		
	V _F = 25V			V _F = 25V		V _F = 6V	$I_{F} = 0.8I_{F(min)}$	
	MIN	NOM	MAX	MIN	TYP	TYP	TYP	MAX
J506	1.120	1.40	1.680	0.33	1.5	0.25	2.5	1.1

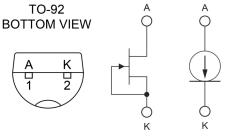
V-I CHARACTERISTICS CURRENT REGULATING DIODE



- 1. Absolute maximum ratings are limiting values above which serviceability may be impaired. 2. Pulsed, t = 2ms. Maximum V_F where IF < 1.1 $_{\rm IF}$ (max).
- 3. Pulsed, t = 2ms. Continuous currents may vary.
- 4. Pulsed, t = 2ms. Continuous impedances may vary. 5. Min V_F required to ensure $I_F = 0.8_{IF}$ (min).

Available Packages:

TO-92 Bare Die.



Please contact Micross for full package and die dimensions

Micross Components Europe



Tel: +44 1603 788967

Email: chipcomponents@micross.com Web: http://www.micross.com/distribution

Information furnished by Linear Integrated Systems and Micross Components is believed to be accurate and reliable. However, no responsibility is assumed for its use; nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Linear Integrated Systems.